

F5045P

FUJI High-side IPS

High-side Intelligent Power Switch

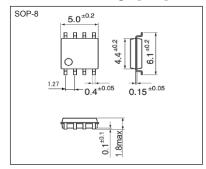
■ Features

- Low minimum operating voltage(min.3V)
- · Low stand by current
- Two input terminals (for on state hold)
- · Over current detection
- · Over temperature shutdown
- · High speed switching

Applications

· Main relay driver

■ Outline drawings [mm]



Connection

TERMINAL No.	FUNCTION	SYMBOL
1	OUTPUT	OUT
2	GROUND	GND
3	STATUS	ST
④	INPUT	IN
\$ 6 7 8	SUPPLY VOLTAGE	Vcc

■ Maximum ratings and characteristics

● Absolute maximum ratings (at Tc=25°C)

Description	Symbol	Characteristics	Unit	Remarks
Cumply valtage	Vcc	50	V	Pulse 0.25 sec
Supply voltage	Vcc	33	V	DC
Continuous drain current	ΙD	1.0	Α	*
Input voltage	VIN	-0.3~Vcc+0.3	V	DC
Maximum power dissipation	P□	1.5	W	*
Operating junction temperature	Tj	150	°C	_
Storage temperature range	Tstg	-55~150	°C	-
Single pulse inductive load switch-off energy dissipation	Ecl	25	mJ	Tj=150°C, L=50mH, Single pulse, dv/dt≤10V/us

^{*} Surface mounted on 1000mm² PCB(FR-4)

● Electrical characteristics (at Tc=25°C, unless otherwise specified.)

Description	Symbol	Conditions	min.	typ.	max.	Unit
Operating voltage	Vcc**	R _L =26Ω	2.87	-	33	V
Standby current	Icc	Vcc=13V, RL=26Ω, VIN1=VIN2=0V		-	130	μA
Vin(H),Vin(L) 3.5V	VIN(H) VIN(H)	Vcc=3~5V	0.7xVcc	-	-	V
	V _{IN} (L)	Vcc=3~5V	-	-	0.3xVcc	V
Input voltage 2.1V 1.5V 9.9V 3V 5V V/NL.	V _{IN} (H)	Vcc>5V	3.5	-	-	V
	Vin(L)	Vcc>5V	_	-	1.5	V
Input current 1	I _{IN} (H)1	Vcc=13V, Vin1=5V	_	_	100	μA
Input current 2	I _{IN} (H)2	Vcc=13V, Vin2=5V	_	_	100	μA
On-state resistance	R _{DS} (on)	Vcc=13V, IL=0.5A	-	-	0.6	Ω
Output leakeage current	lou	V _{cc} =13V	_	_	80	μA
Over current detection	loc	Vcc=13V	2	-	-	Α
Over-temperature shutdown	Ttrip	Vcc=13V	150	-	-	°C
Turn-on time	Ton	V _{cc} =13V, R _L =26Ω	-	-	120	μs
Turn-off time	Toff	V _{cc} =13V, R _L =26Ω	_	_	40	μs
Output-clamp voltage	V _{clamp} ***	Vcc=13V, IL=0.5A, VIN1=VIN2=0V, L=10mH	-(50-Vcc)	-	-(60-Vcc)	V
Diode forward on-voltage	V _{SD}	V _{IN1} =V _{IN2} =0V, I _{SD} =-1A	-	-	1.5	V

^{**} Minimum of operating voltage is difined as the voltage of RDS(on) = 2.45Ω
*** <Output-clamp voltage: Vclamp> 50V≤VCC+Vclamp≤60V (Tc=25°C) ←



● Electrical characteristics (at Tc=-40~105°C, unless otherwise specified.)

Description		Symbol	Conditions	min.	typ.	max.	Unit
Operating voltage		Vcc**	R _L =26Ω	3	-	33	V
Standby current	Standby current		V _{cc} =13V, R _L =26Ω, V _{IN1} =V _{IN2} =0V	-	_	150	μA
VIN(H), VIN(L)		Vin(H)	Vcc=3~5V	0.7xVcc	_	-	V
3.5V		V _{IN} (L)	Vcc=3~5V	-	_	0.3xVcc	V
Input voltage 2.1V 1.5V 0.9V VINIL) 3V 5V	,	V _{IN} (H)	Vcc>5V	3.5	-	-	V
		V _{IN} (L)	Vcc>5V	_	-	1.5	V
Input current 1		I _{IN} (H)1	Vcc=13V, VIN1=5V	_	-	110	μA
Input current 2		I _{IN} (H)2	Vcc=13V, VIN1=5V	_	-	110	μA
On-state resistance		Ros(on)	Vcc=13V, IL=0.5A	-	-	1.0	Ω
Output leakeage curre	ent	lou	Vcc=13V	_	-	92	μA
Over current detection	n	loc	Vcc=13V	1.3	-	-	Α
Turn-on time		Ton	Vcc=13V, RL=26Ω	_	-	140	μs
Turn-off time		Toff	Vcc=13V, RL=26Ω	_	-	45	μs
Output-clamp voltage		V _{clamp} ***	Vcc=13V, IL=0.5A, VIN1=VIN2=0V, L=10mH	-(48.4-Vcc)	-	-(61.8-Vcc)	V
Diode forward on-volt	tage	VsD	V _{IN1} =V _{IN2} =0V, I _{SD} =-1A	-	-	1.8	V

^{**} Minimum of operating voltage is difined as the voltage of RDS(on) = 2.45Ω *** <Output-clamp voltage: Vclamp> $50V \le VCC + Vclamp \le 60V$ (Tc= $25^{\circ}C$)



Thermal characteristics

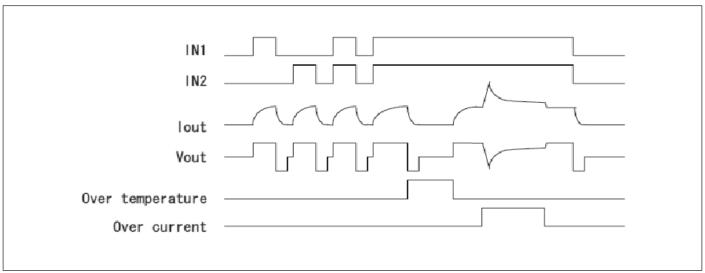
Description	Symbol	Test conditions	min.	typ.	max.	Unit
Thermal resistance	Rth(j-a)	Junction - ambient****	_	_	83.0	°C/W

^{****} Surface mounted on 1000mm² PCB (FR-4)

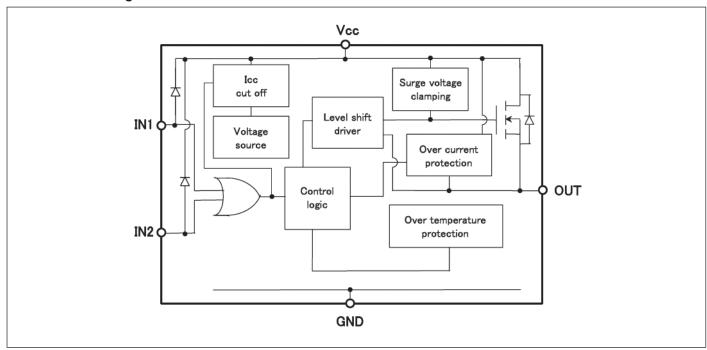
Truth table

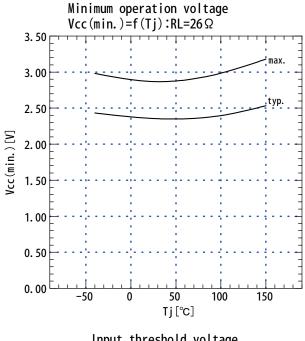
	Input1 voltage	Input2 voltage	Output voltage	Remarks
	L	L	L	
Normal operation	L	Н	Н	
Normal operation	Н	L	Н	_
	Н	Н	Н	
	L	L	L	
Over-current	L	Н	*	Auto-restart
Over-current	Н	L	*	*Current-limiting mode
	Н	Н	*	
	L	L	L	
Over temperature	L	Н	L	Auto-restart
Over-temperature	Н	L	L	Auto-restart
	Н	Н	L	
	Open	Open	L	
	Open	L	L	"0""" "! " ! ! ! ! ! ! !
Over of input terminals	L	Open	L	"Open" means "L" level,
	Open	Н	Н	because of pull-down circuit.
	H	Open	Н	

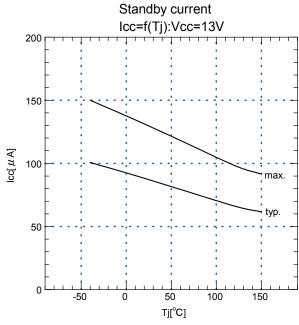
■ Timing chart

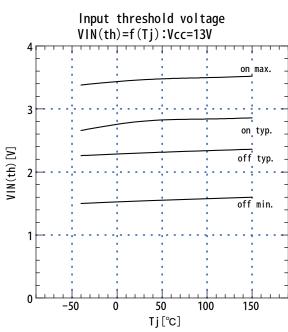


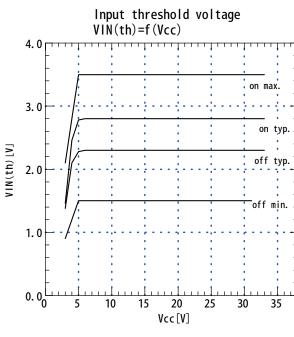
■ Circuit block diagram

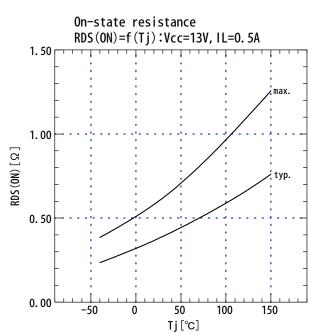


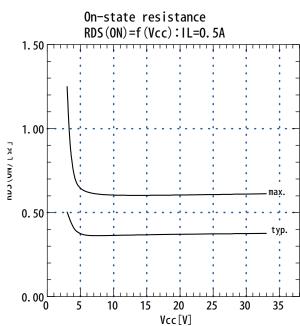


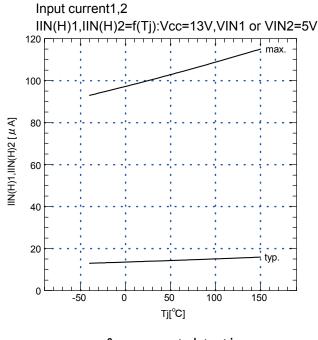


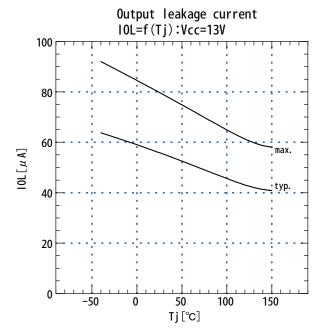


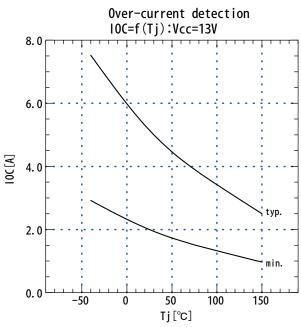


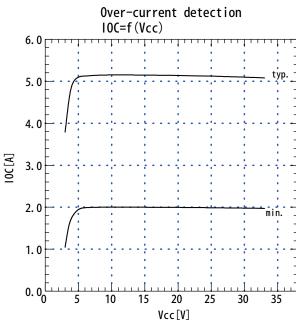


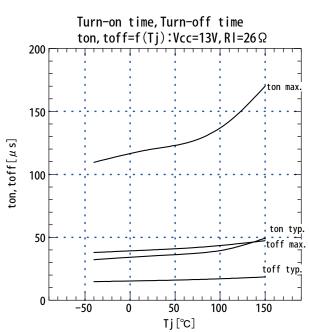


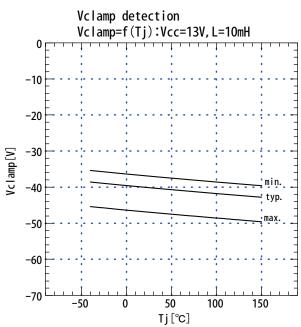












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