

F5055

FUJI Intelligent Power MOSFET

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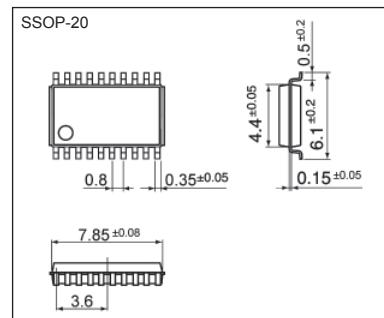
■ Features

- Two N-ch power MOSFET circuits
- Over temperature protection
- Short circuit protection
- Low on-resistance
- High speed switching

■ Applications

- Solenoid driver
- Lamp driver
- Replacements for fuse and relay

■ Outline drawings [mm]



■ Connection

TERMINAL No.	FUNCTION
①	DRAIN 1
②③	SOURCE 1
④	GATE 1
⑤	NC
⑥⑦	SOURCE 2
⑧	GATE 2
⑨	NC
⑩	DRAIN 2
⑪~⑯	DRAIN 2
⑯~⑳	DRAIN 1

■ Maximum ratings and characteristics

● Absolute maximum ratings (at $T_c=25^\circ\text{C}$, unless otherwise specified)

Description	Symbol	Characteristics	Unit	Remarks
Drain-source voltage	V_{DSS}	40	V	DC
Gate-source voltage	V_{GSS}	-0.3~7.0	V	DC
Continuous drain current	I_D	5.9	A	for each channel
Maximum power dissipation	P_0	7.8	W	for each channel
Operating junction temperature	T_j	150	$^\circ\text{C}$	–
Storage temperature range	T_{stg}	-55 ~ 150	$^\circ\text{C}$	–
Single pulse inductive load switch-off energy dissipation	E_{CL}	100	mJ	$T_j=150^\circ\text{C}$, $L=5\text{mH}$, $I_{DP}=8\text{A}$ Single pulse, $dv/dt \leq 10\text{V}/\mu\text{s}$ for each channel

● Electrical characteristics (at $T_c=25^\circ\text{C}$ unless otherwise specified)

Description	Symbol	Conditions	min.	typ.	max.	Unit
Drain-source clamp voltage	V_{DSS}	$I_D=1\text{mA}$, $V_{GS}=0\text{V}$	40	–	60	V
Gate threshold voltage	$V_{GS(\text{th})}$	$I_D=10\text{mA}$, $V_{DS}=13\text{V}$	1.0	–	2.8	V
Operation gate voltage (protection circuit operates)	$V_{GS(p)}$	–	2.8	–	7.0	V
Zero gate voltage drain current	I_{DSS}	$V_{DS}=16\text{V}$, $V_{GS}=0\sim 1.5\text{V}$	–	–	60	μA
		$V_{DS}=30\text{V}$, $V_{GS}=0\sim 1.5\text{V}$	–	–	1	mA
Gate-source leakage current	$I_{GS(n)}^*$	$V_{GS}=5\text{V}$	–	–	250	μA
	$I_{GS(un)}^{**}$	$V_{GS}=5\text{V}$, $T_j > 150^\circ\text{C}$	–	–	350	μA
Drain-source on-state resistance	$R_{DS(on)}$	$I_D=5\text{A}$, $V_{GS}=5\text{V}$	–	–	140	$\text{m}\Omega$
Turn-on time	t_{on}	$V_{DS}=13\text{V}$, $I_D=0.5\text{A}$, $V_{GS}=5\text{V}$	–	–	50	μs
Turn-off time	t_{off}		–	–	50	μs
Over-temperature protection	T_{trip}	$V_{GS}=5\text{V}$	150	–	–	$^\circ\text{C}$
Short circuit protection	I_{SC}	$V_{GS}=5\text{V}$	12	–	32	A

Note * : Under normal operation

Note ** : Under self protection (Short circuit ~ Short circuit protection ~ Over-temperature protection)

● Electrical characteristics (at $T_c=-40\sim 105^\circ\text{C}$ unless otherwise specified)

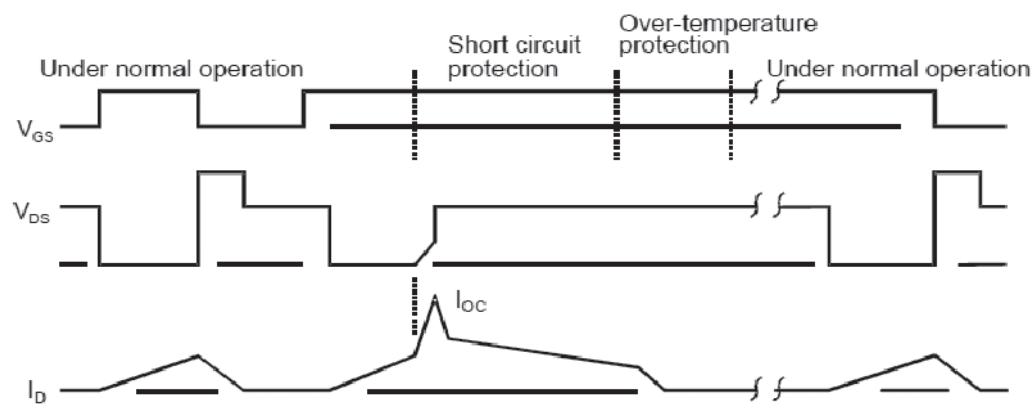
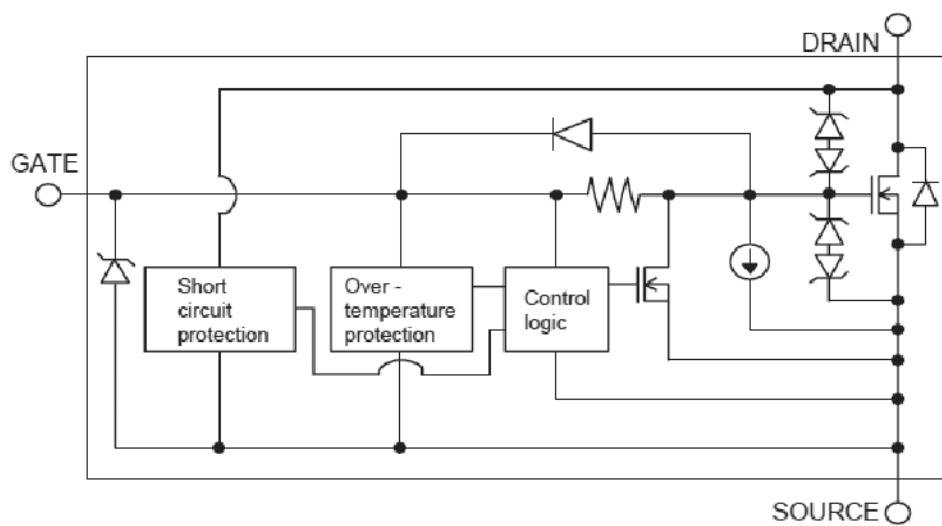
Description	Symbol	Conditions	min.	typ.	max.	Unit
Drain-source clamp voltage	V_{DSS}	$I_D=1\text{mA}$, $V_{GS}=0\text{V}$	38	–	62	V
Gate threshold voltage	$V_{GS(\text{th})}$	$I_D=10\text{mA}$, $V_{DS}=14\text{V}$	1.0	–	3.0	V
Operation gate voltage (protection circuit operates)	$V_{GS(p)}$	–	3.0	–	6.7	V
Zero gate voltage drain current	I_{DSS}	$V_{DS}=16\text{V}$, $V_{GS}=0\text{V}$	–	–	100	μA
		$V_{DS}=30\text{V}$, $V_{GS}=0\text{V}$	–	–	1.6	mA
Gate-source leakage current	$I_{GS(n)}^*$	$V_{GS}=5\text{V}$	–	–	300	μA
	$I_{GS(un)}^{**}$	$V_{GS}=5\text{V}$, $T_j > 150^\circ\text{C}$	–	–	350	μA
Drain-source on-state resistance	$R_{DS(on)}$	$I_D=5\text{A}$, $V_{GS}=5\text{V}$	–	–	205	$\text{m}\Omega$
Turn-on time	t_{on}	$V_{DS}=13\text{V}$, $I_D=5\text{A}$, $V_{GS}=5\text{V}$	–	–	62	μs
Turn-off time	t_{off}		–	–	52	μs
Short circuit protection	I_{SC}	$V_{GS}=5\text{V}$	8.4	–	42	A

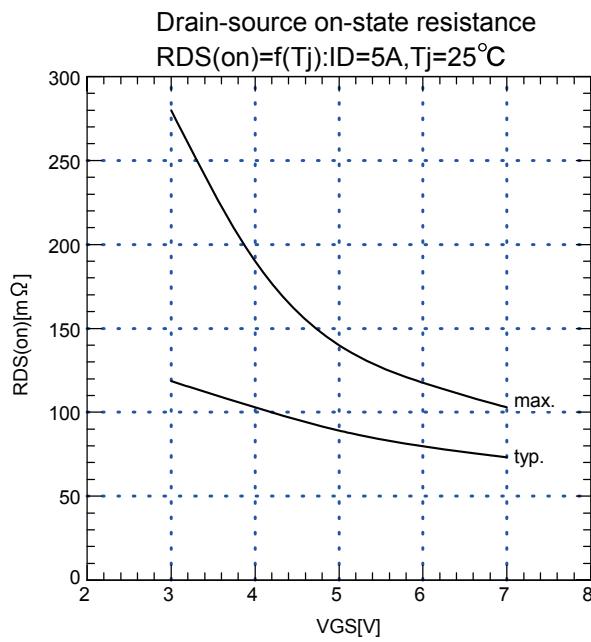
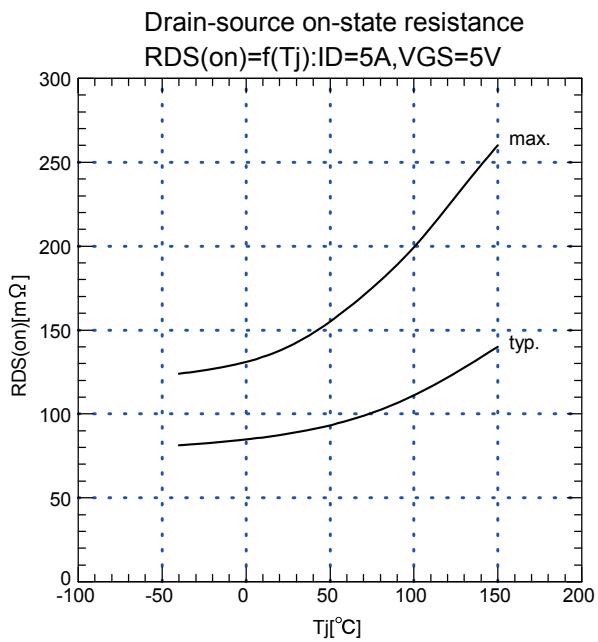
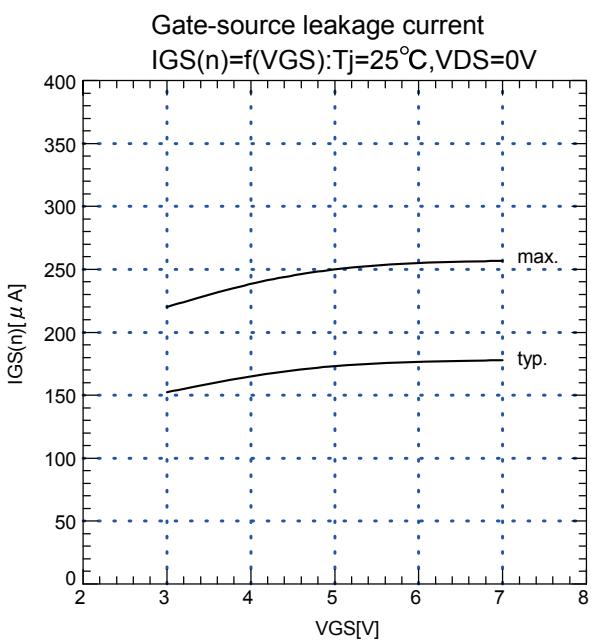
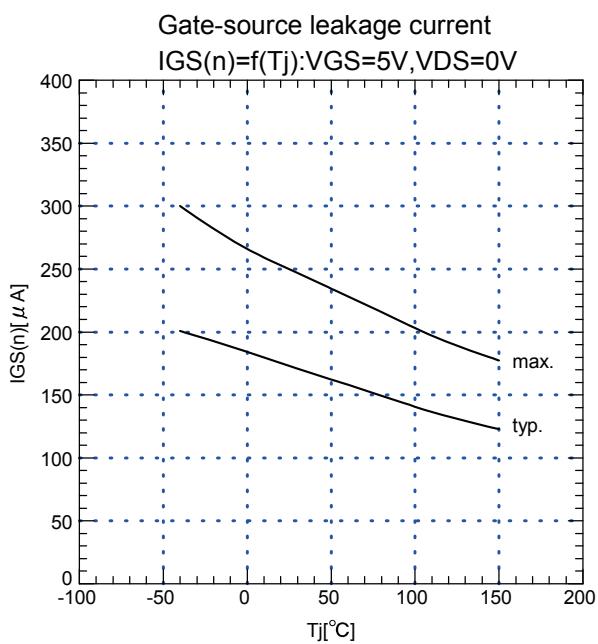
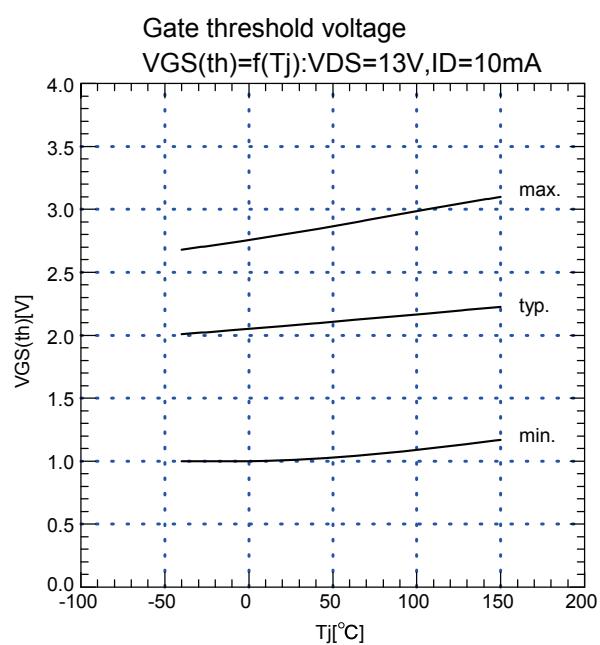
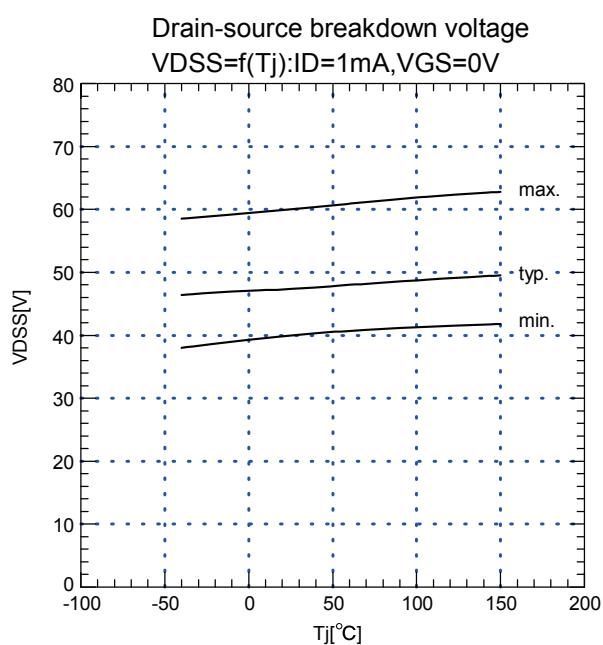
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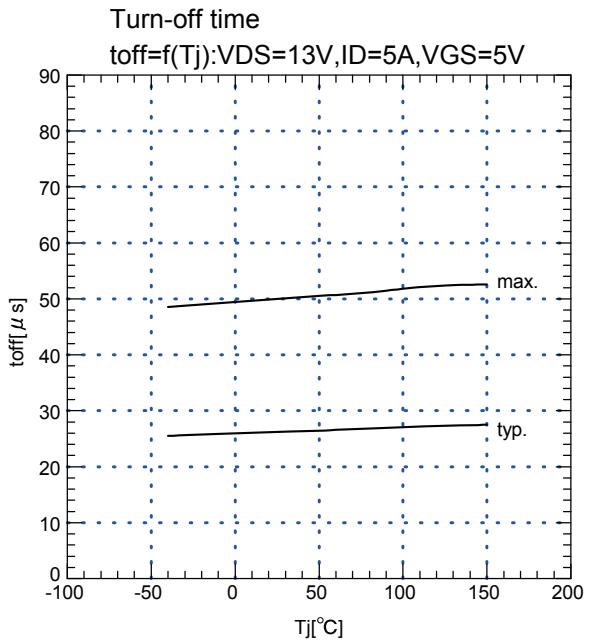
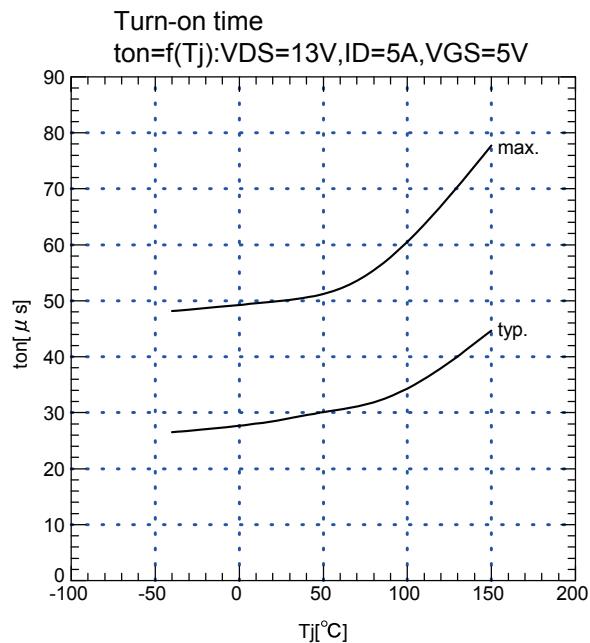
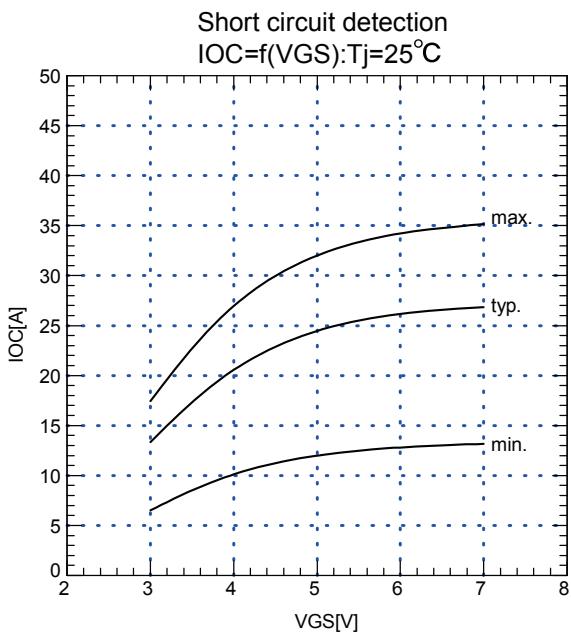
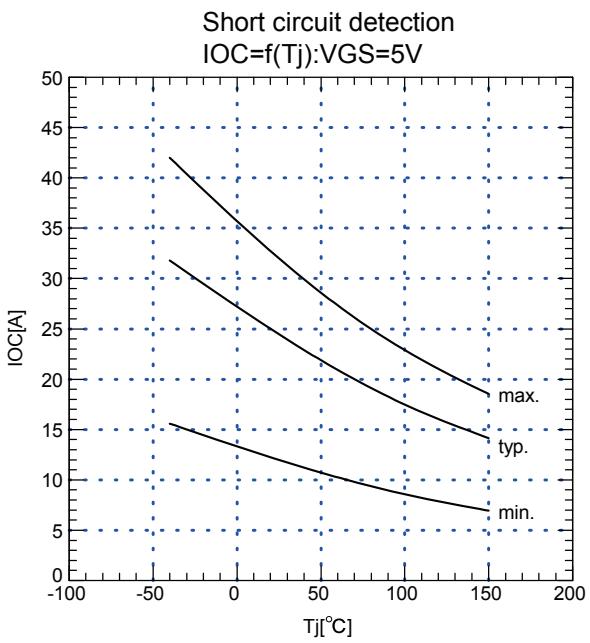
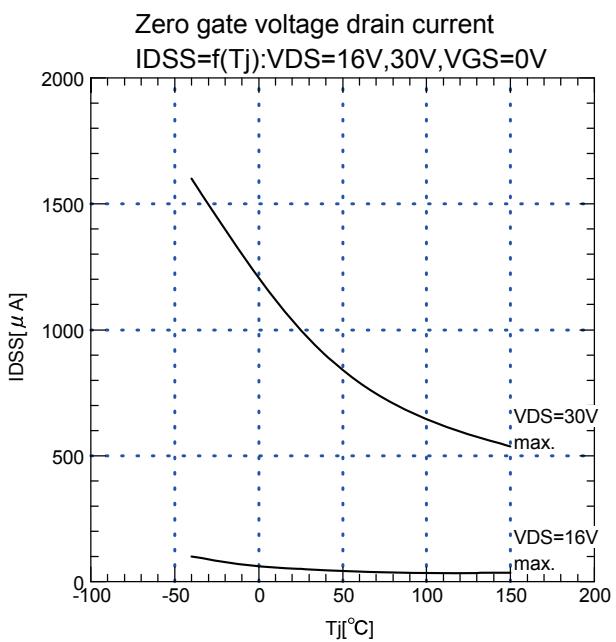
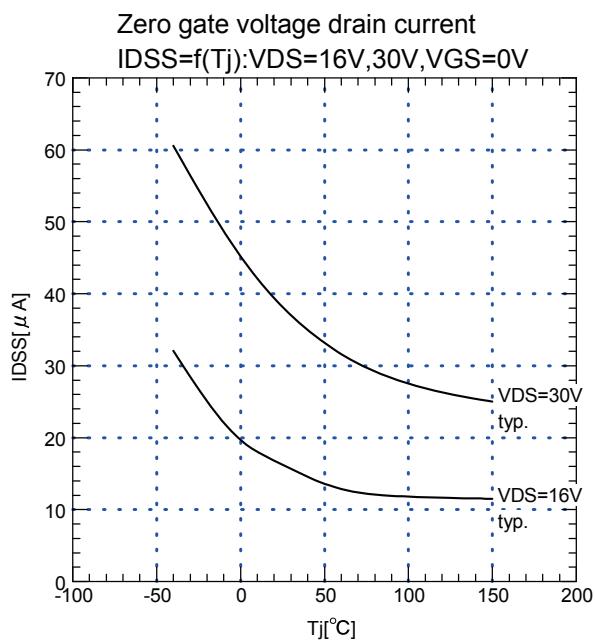
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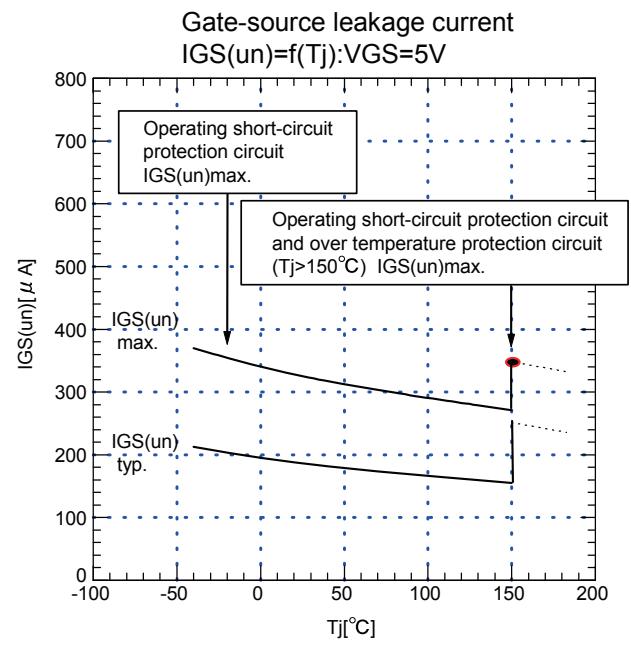
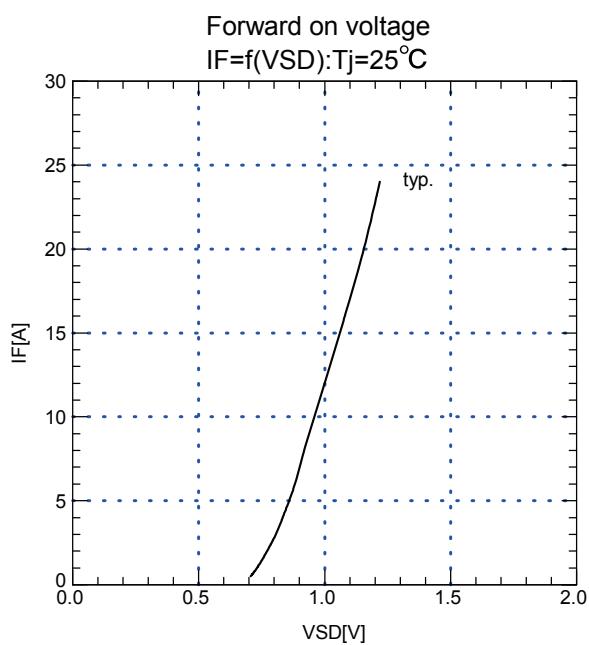
● Thermal resistance

Description	Symbol	Test conditions	min.	typ.	max.	Unit
Thermal resistance	$R_{th(j-c)}$	Junction-case	–	–	16.0	$^\circ\text{C}/\text{W}$

■ Timing chart**■ Circuit block diagram**







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