

# FMH19N60E

FUJI POWER MOSFET

## Super FAP-E<sup>3</sup> series

## N-CHANNEL SILICON POWER MOSFET

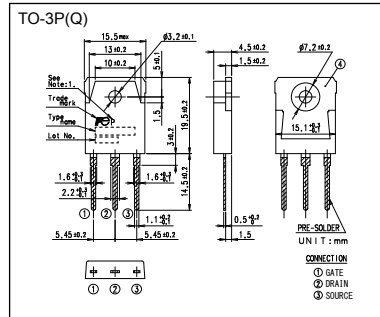
### ■ Features

Maintains both low power loss and low noise  
Lower  $R_{DS(on)}$  characteristic  
More controllable switching  $dV/dt$  by gate resistance  
Smaller  $V_{GS}$  ringing waveform during switching  
Narrow band of the gate threshold voltage ( $3.0 \pm 0.5V$ )  
High avalanche durability

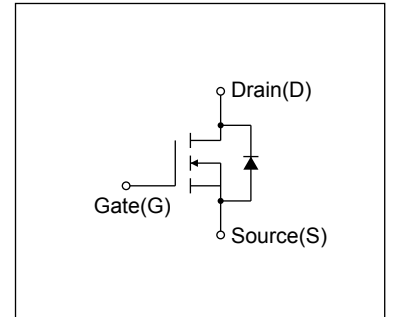
### ■ Applications

Switching regulators  
UPS (Uninterruptible Power Supply)  
DC-DC converters

### ■ Outline Drawings [mm]



### ■ Equivalent circuit schematic



### ■ Maximum Ratings and Characteristics

#### ● Absolute Maximum Ratings at $T_c=25^\circ C$ (unless otherwise specified)

| Description   | Symbol    | Characteristics | Unit        | Remarks          |
|---|-----------|-----------------|-------------|------------------|
| Drain-Source Voltage                                    | $V_{DS}$  | 600             | V           |                  |
|   | $V_{DSX}$ | 600             | V           | $V_{GS} = -30V$  |
| Continuous Drain Current                                | $I_D$     | $\pm 19$        | A           |                  |
| Pulsed Drain Current                                    | $I_{DP}$  | $\pm 76$        | A           |                  |
| Gate-Source Voltage                                     | $V_{GS}$  | $\pm 30$        | V           |                  |
| Repetitive and Non-Repetitive Maximum Avalanche Current | $I_{AS}$  | 19              | A           | Note*1           |
| Non-Repetitive Maximum Avalanche Energy                 | $E_{AS}$  | 799             | mJ          | Note*2           |
| Repetitive Maximum Avalanche Energy                     | $E_{AR}$  | 31.5            | mJ          | Note*3           |
| Peak Diode Recovery $dV/dt$                             | $dV/dt$   | 6.5             | kV/ $\mu s$ | Note*4           |
| Peak Diode Recovery $-di/dt$                            | $-di/dt$  | 100             | A/ $\mu s$  | Note*5           |
| Maximum Power Dissipation                               | $P_D$     | 2.50            | W           | $T_a=25^\circ C$ |
|   |           | 315             |             | $T_c=25^\circ C$ |
| Operating and Storage Temperature range                 | $T_{ch}$  | 150             | $^\circ C$  |                  |
|   | $T_{stg}$ | -55 to + 150    | $^\circ C$  |                  |

#### ● Electrical Characteristics at $T_c=25^\circ C$ (unless otherwise specified)

| Description                      | Symbol       | Conditions                                   | min. | typ. | max.  | Unit     |
|----------------------------------|--------------|--|------|------|-------|----------|
| Drain-Source Breakdown Voltage   | $BV_{DSS}$   | $I_D=250\mu A, V_{GS}=0V$                    | 600  | -    | -     | V        |
| Gate Threshold Voltage           | $V_{GS(th)}$ | $I_D=250\mu A, V_{DS}=V_{GS}$                | 2.5  | 3.0  | 3.5   | V        |
| Zero Gate Voltage Drain Current  | $I_{DSS}$    | $V_{DS}=600V, V_{GS}=0V, T_{ch}=25^\circ C$  | -    | -    | 25    | $\mu A$  |
|                                  |              | $V_{DS}=480V, V_{GS}=0V, T_{ch}=125^\circ C$ | -    | -    | 250   |          |
| Gate-Source Leakage Current      | $I_{GSS}$    | $V_{GS}=\pm 30V, V_{DS}=0V$                  | -    | 10   | 100   | nA       |
| Drain-Source On-State Resistance | $R_{DS(on)}$ | $I_D=9.5A, V_{GS}=10V$                       | -    | 0.31 | 0.365 | $\Omega$ |
| Forward Transconductance         | $g_{fs}$     | $I_D=9.5A, V_{DS}=25V$                       | 13   | 26   | -     | S        |
| Input Capacitance                | $C_{iss}$    | $V_{DS}=25V$                                 | -    | 3600 | 5400  | pF       |
| Output Capacitance               | $C_{oss}$    | $V_{GS}=0V$                                  | -    | 310  | 465   |          |
| Reverse Transfer Capacitance     | $C_{rss}$    | $f=1MHz$                                     | -    | 23   | 35    |          |
| Turn-On Time                     | $t_{d(on)}$  | $V_{cc}=300V$                                | -    | 26   | 39    | ns       |
|                                  | $t_r$        | $V_{GS}=10V$                                 | -    | 13   | 20    |          |
| Turn-Off Time                    | $t_{d(off)}$ | $I_D=9.5A$                                   | -    | 150  | 225   |          |
|                                  | $t_f$        | $R_{GS}=8.2\Omega$                           | -    | 20   | 30    |          |
| Total Gate Charge                | $Q_G$        | $V_{cc}=300V$                                | -    | 105  | 160   | nC       |
| Gate-Source Charge               | $Q_{GS}$     | $I_D=19A$                                    | -    | 23   | 35    |          |
| Gate-Drain Charge                | $Q_{GD}$     | $V_{GS}=10V$                                 | -    | 30   | 45    |          |
| Avalanche Capability             | $I_{AV}$     | $L=1.71mH, T_{ch}=25^\circ C$                | 19   | -    | -     | A        |
| Diode Forward On-Voltage         | $V_{SD}$     | $I_F=19A, V_{GS}=0V, T_{ch}=25^\circ C$      | -    | 0.90 | 1.35  | V        |
| Reverse Recovery Time            | $t_{rr}$     | $I_F=19A, V_{GS}=0V$                         | -    | 0.6  | -     | $\mu s$  |
| Reverse Recovery Charge          | $Q_{rr}$     | $-di/dt=100A/\mu s, T_{ch}=25^\circ C$       | -    | 10   | -     | $\mu C$  |

### ● Thermal Characteristics

| Description        | Symbol         | Test Conditions    | min. | typ. | max. | Unit         |
|--------------------|----------------|--------------------|------|------|------|--------------|
| Thermal resistance | $R_{th(ch-c)}$ | Channel to case    |      |      | 0.40 | $^\circ C/W$ |
|                    | $R_{th(ch-a)}$ | Channel to ambient |      |      | 50.0 | $^\circ C/W$ |

Note \*1 :  $T_{ch} \leq 150^\circ C$

Note \*2 : Stating  $T_{ch}=25^\circ C, I_{AS}=8A, L=22.9mH, V_{cc}=60V, R_G=50\Omega$

$E_{AS}$  limited by maximum channel temperature and avalanche current.

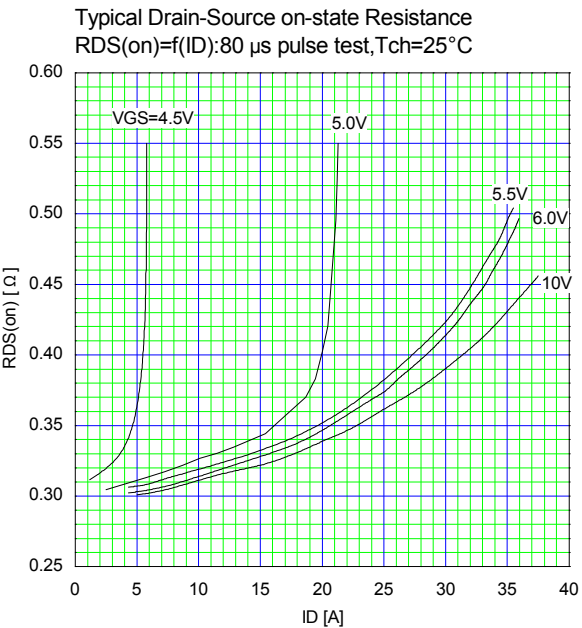
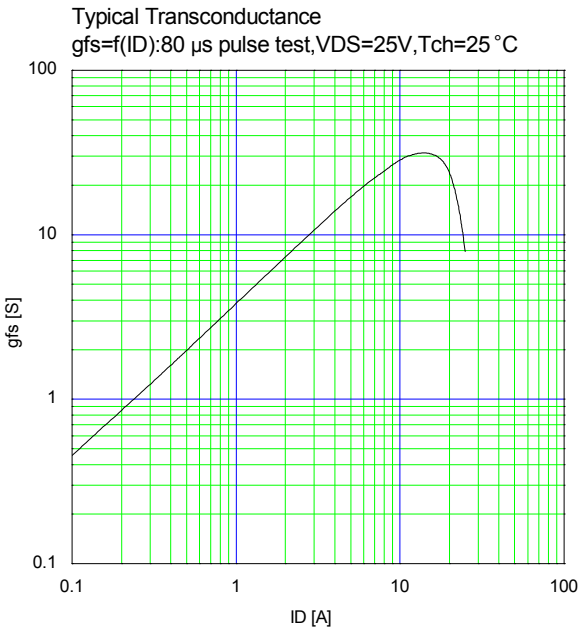
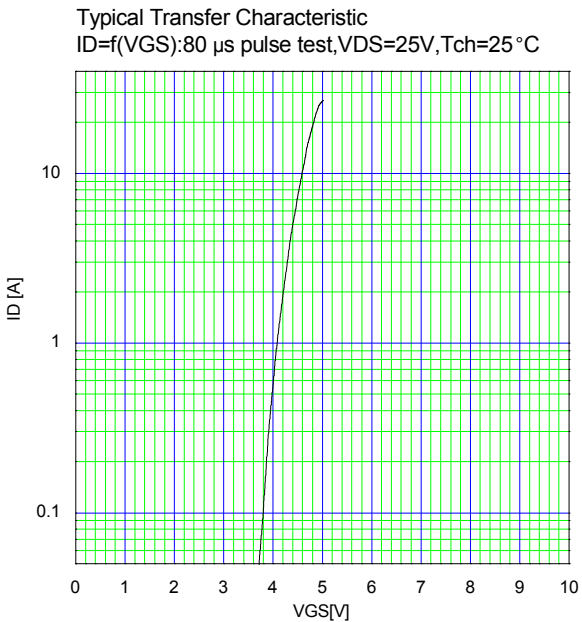
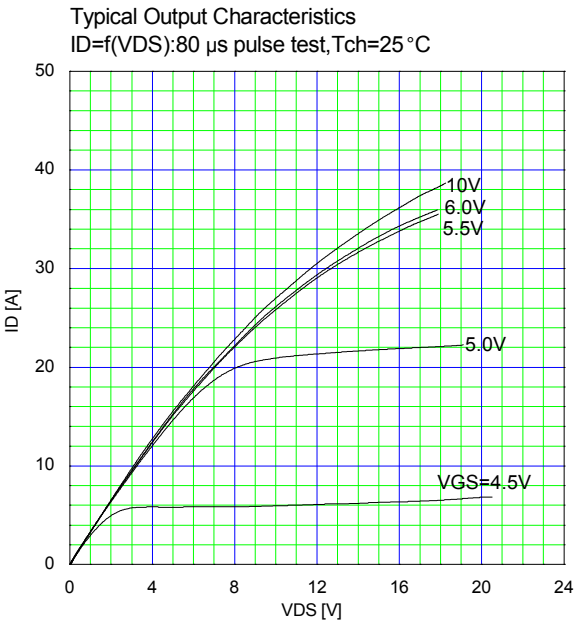
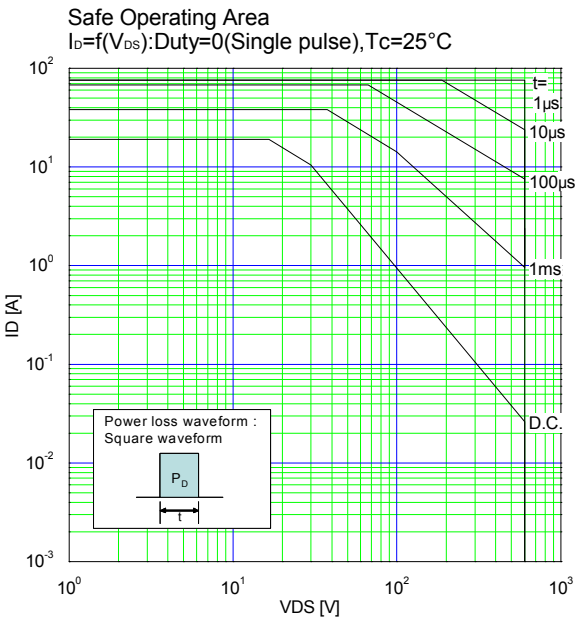
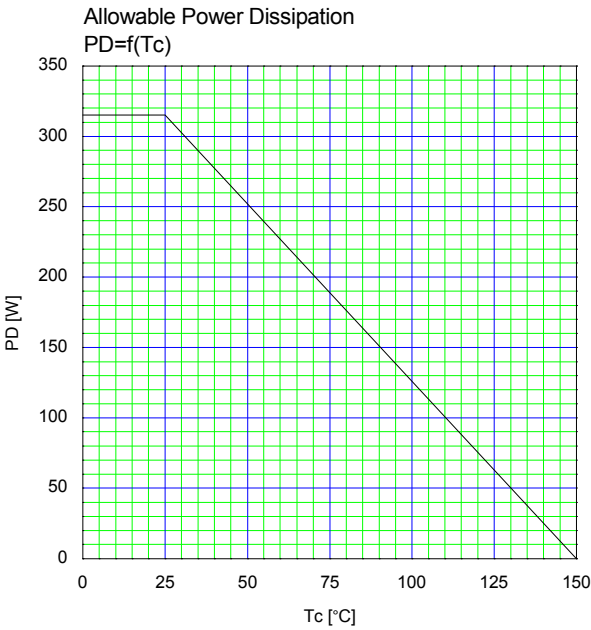
See to 'Avalanche Energy' graph.

Note \*3 : Repetitive rating : Pulse width limited by maximum channel temperature.

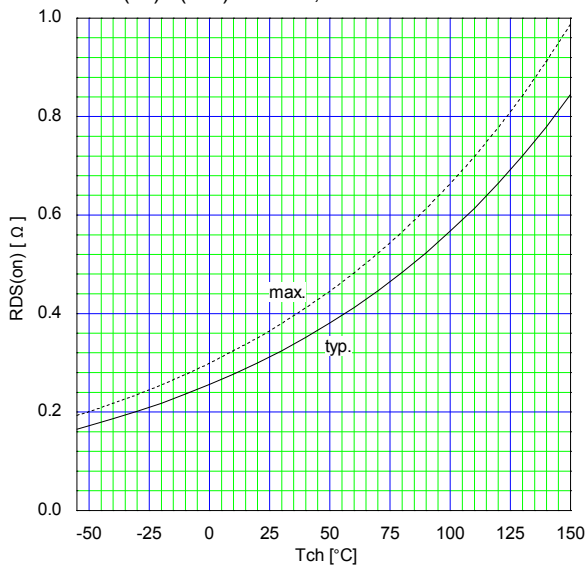
See to the 'Transient Thermal Impedance' graph.

Note \*4 :  $I_{FS}=I_D, -di/dt=100A/\mu s, V_{cc}=BV_{DS}, T_{ch} \leq 150^\circ C$ .

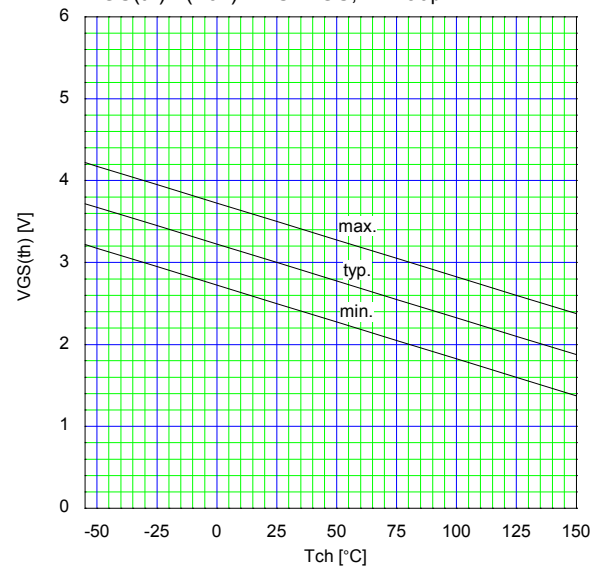
Note \*5 :  $I_{FS}=I_D, dv/dt=5.0kV/\mu s, V_{cc}=BV_{DS}, T_{ch} \leq 150^\circ C$ .



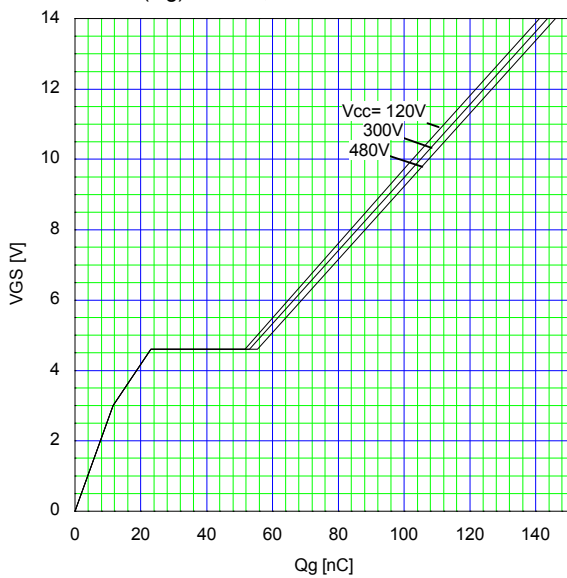
Drain-Source On-state Resistance  
 $R_{DS(on)} = f(T_{ch}): I_D = 9.5A, V_{GS} = 10V$



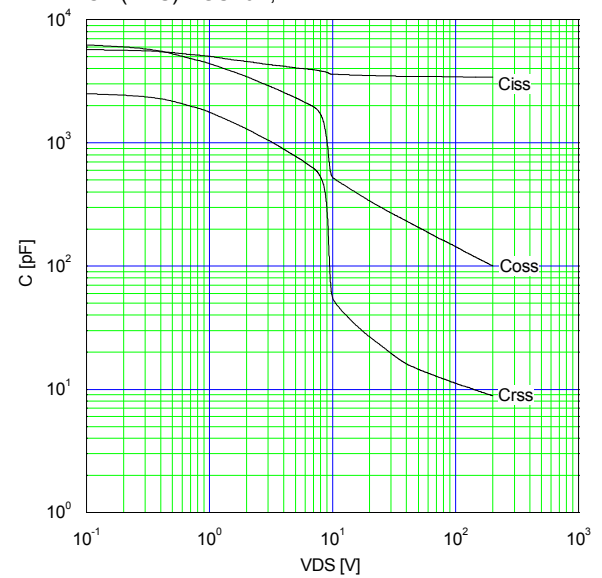
Gate Threshold Voltage vs.  $T_{ch}$   
 $V_{GS(th)} = f(T_{ch}): V_{DS} = V_{GS}, I_D = 250\mu A$



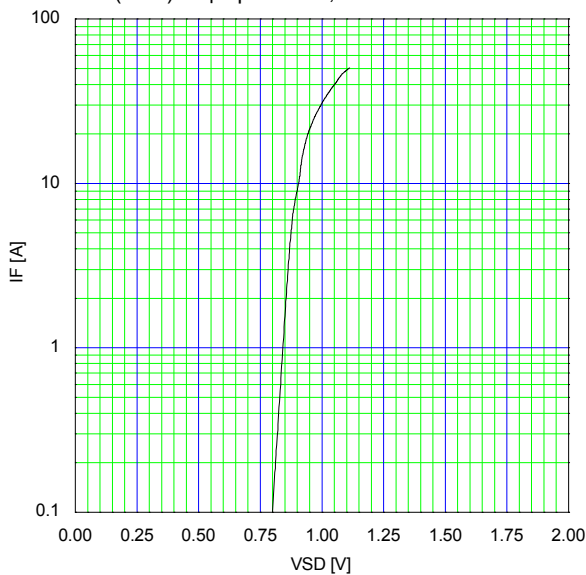
Typical Gate Charge Characteristics  
 $V_{GS} = f(Q_g): I_D = 19A, T_{ch} = 25^\circ C$



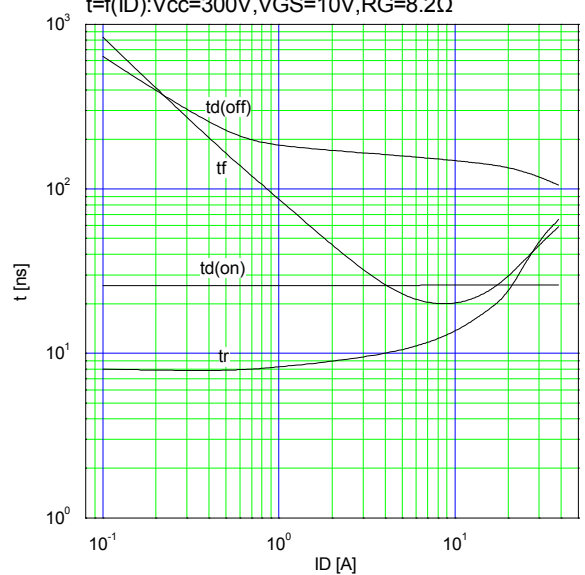
Typical Capacitance  
 $C = f(V_{DS}): V_{GS} = 0V, f = 1MHz$

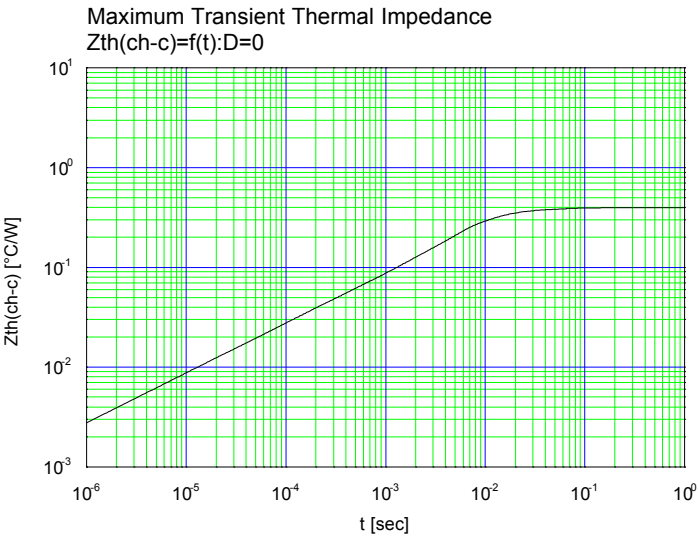
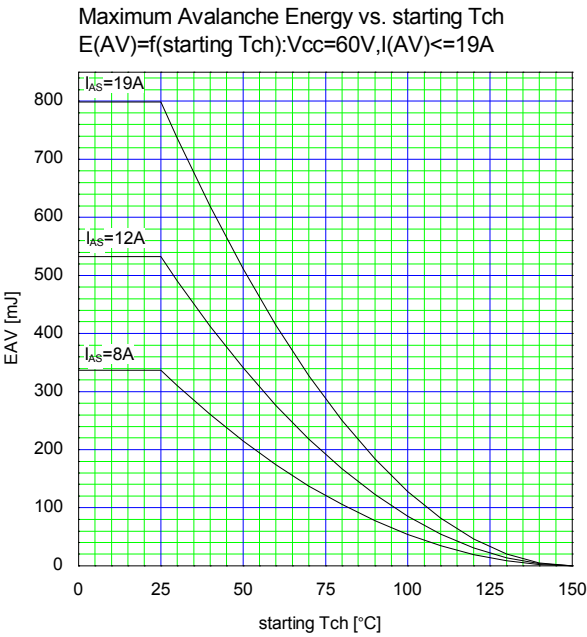


Typical Forward Characteristics of Reverse Diode  
 $I_F = f(V_{SD}): 80\mu s \text{ pulse test}, T_{ch} = 25^\circ C$



Typical Switching Characteristics vs.  $I_D$   
 $t = f(I_D): V_{CC} = 300V, V_{GS} = 10V, R_G = 8.2\Omega$





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|                 |                         |   |                          |
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|---|---|
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