

# **FMH21N50ES**

**FUJI POWER MOSFET** 

## Super FAP-E<sup>3S</sup> series

### **N-CHANNEL SILICON POWER MOSFET**

### Features

Maintains both low power loss and low noise Lower  $R_{DS}(on)$  characteristic More controllable switching dv/dt by gate resistance Smaller  $V_{GS}$  ringing waveform during switching Narrow band of the gate threshold voltage (4.2±0.5V) High avalanche durability

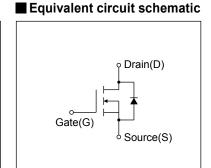
### Applications

Switching regulators UPS (Uninterruptible Power Supply) DC-DC converters

### ■ Maximum Ratings and Characteristics

### ● Absolute Maximum Ratings at Tc=25°C (unless otherwise specified)

# TO-3P (Q) 15.5m 15.5m 15.5m 15.124 1.521 1.52



Description	Symbol	Characteristics	Unit	Remarks	
Dunin Sauraa Valtana	V <sub>DS</sub>	500	V		
Drain-Source Voltage	V <sub>DSX</sub>	500	V	V <sub>GS</sub> = -30V	
Continuous Drain Current	In	±21	Α		
Pulsed Drain Current	IDP	±84	А		
Gate-Source Voltage	V <sub>GS</sub>	±30	V		
Repetitive and Non-Repetitive Maximum Avalanche Current	Iar	21	А	Note*1	
Non-Repetitive Maximum Avalanche Energy	Eas	714.5	mJ	Note*2	
Repetitive Maximum Avalanche Energy	Ear	28.5	mJ	Note*3	
Peak Diode Recovery dV/dt	dV/dt	5.7	kV/μs	Note*4	
Peak Diode Recovery -di/dt	-di/dt	100	A/µs	Note*5	
Maximum Power Dissipation	Po	2.50	14/	Ta=25°C	
		285	W	Tc=25°C	
Operating and Storage Temperature range	Tch	150	°C		
	T <sub>stq</sub>	-55 to + 150	°C		

### ● Electrical Characteristics at Tc=25°C (unless otherwise specified)

Description	Symbol	Conditions		min.	typ.	max.	Unit
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	I <sub>D</sub> =250μA, V <sub>GS</sub> =0V		500	-	-	V
Gate Threshold Voltage	V <sub>GS</sub> (th)	In=250µA, Vns=Vgs	I <sub>D</sub> =250µA, V <sub>DS</sub> =V <sub>GS</sub>		4.2	4.7	V
Zero Gate Voltage Drain Current	Ipss	V <sub>DS</sub> =500V, V <sub>GS</sub> =0V	Tch=25°C	-	-	25	μA
	IDSS	V <sub>DS</sub> =400V, V <sub>GS</sub> =0V	T <sub>ch</sub> =125°C	-	-	250	
Gate-Source Leakage Current	Igss	V <sub>GS</sub> =±30V, V <sub>DS</sub> =0V	V <sub>GS</sub> =±30V, V <sub>DS</sub> =0V		10	100	nA
Drain-Source On-State Resistance	Ros (on)	I <sub>D</sub> =10.5A, V <sub>GS</sub> =10V		-	0.23	0.27	Ω
Forward Transconductance	g <sub>fs</sub>	I <sub>D</sub> =10.5A, V <sub>DS</sub> =25V		7.5	15	-	S
Input Capacitance	Ciss	V <sub>DS</sub> =25V V <sub>GS</sub> =0V f=1MHz		-	2450	3675	pF
Output Capacitance	Coss			-	320	480	
Reverse Transfer Capacitance	Crss			-	19	28.5	
Turn-On Time	td(on)	V <sub>cc</sub> =300V V <sub>dS</sub> =10V I <sub>D</sub> =10.5A R <sub>dS</sub> =10Ω		-	41	61.5	ns
	tr			-	33	49.5	
Turn-Off Time	td(off)			-	90	135	
	tf			-	16	24	
Total Gate Charge	Q <sub>G</sub>	V <sub>cc</sub> =250V I <sub>D</sub> =21A V <sub>GS</sub> =10V		-	68	102	nC
Gate-Source Charge	Q <sub>GS</sub>			-	23	34.5	
Gate-Drain Charge	Q <sub>GD</sub>			-	26	39	
Gate-Drain Crossover Charge	Qsw			-	10	15	
Avalanche Capability	lav	L=1.27mH, Tch=25°C	L=1.27mH, Tch=25°C		-	-	Α
Diode Forward On-Voltage	V <sub>SD</sub>	I <sub>F</sub> =21A, V <sub>GS</sub> =0V, T <sub>ch</sub> =25°C		-	0.90	1.35	V
Reverse Recovery Time	trr	I <sub>F</sub> =21A, V <sub>GS</sub> =0V	I <sub>F</sub> =21A, V <sub>GS</sub> =0V		0.45	-	μs
Reverse Recovery Charge	Qrr	-di/dt=100A/µs, Tch=25°C		-	7.2	-	μC

### ● Thermal Characteristics

Description	Symbol	Test Conditions	min.	typ.	max.	Unit
Thermal resistance	Rth (ch-c)	Channel to Case			0.440	°C/W
	Rth (ch-a)	Channel to Ambient			50.0	°C/W

Note \*1 : Tch≤150°C.

Note '2: Stating Tch=25°C, I<sub>As</sub>=9A, L=16.2mH, Vcc=50V, R<sub>6</sub>=50Ω.

Ess limited by maximum channel temperature and avalanche current.

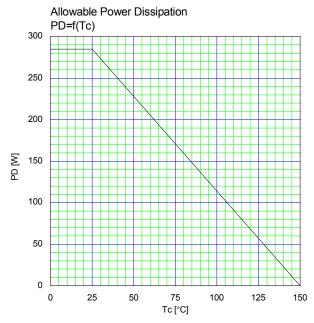
See to 'Avalanche Energy' graph.

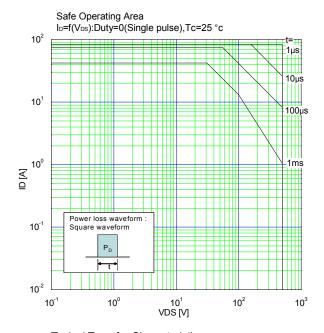
Note \*3 : Repetitive rating : Pulse width limited by maximum channel temperature.

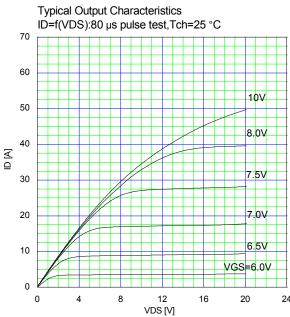
See to the 'Transient Themal impeadance' graph.

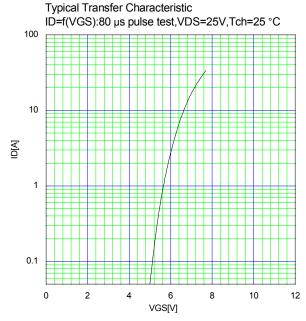
Note \*4 : Ir≤-Ip, -di/dt=100A/µs, Vcc≤BVbss, Tch≤150°C.

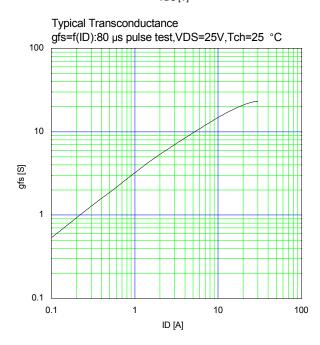
Note \*5 : Ir≤-Ip, dv/dt=5.7kV/µs, Vcc≤BVbss, Tch≤150°C.

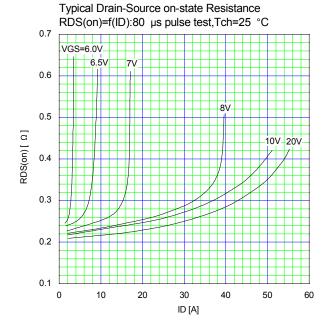


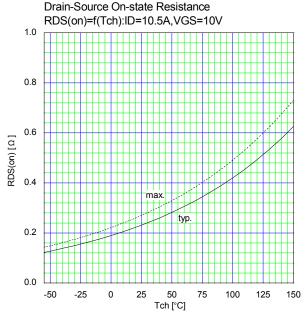


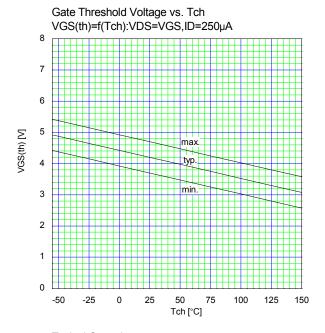


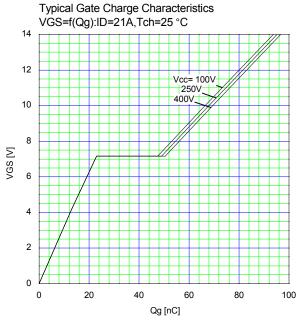


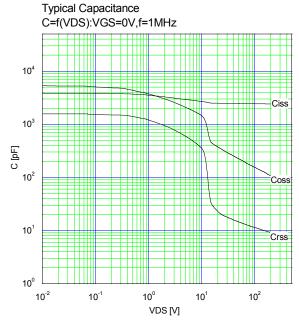


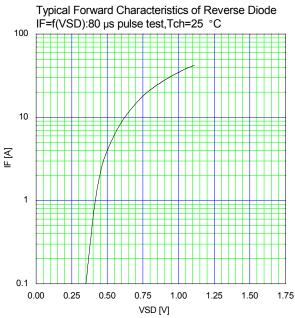


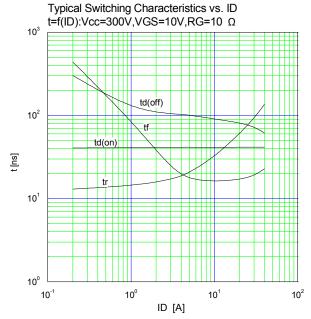


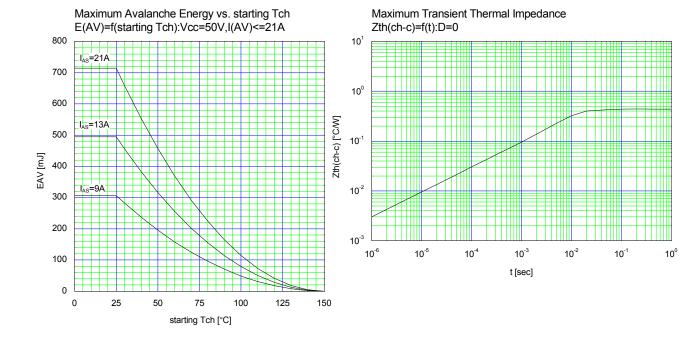












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