

# FMI05N50E

**FUJI POWER MOSFET**

## Super FAP-E<sup>3</sup> series

## N-CHANNEL SILICON POWER MOSFET

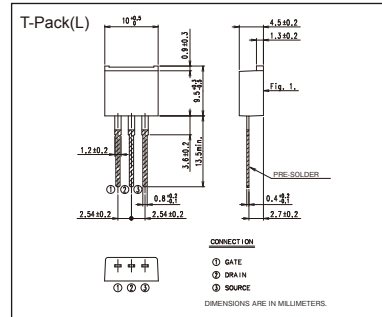
### ■ Features

Maintains both low power loss and low noise  
 Lower  $R_{DS(on)}$  characteristic  
 More controllable switching  $dV/dt$  by gate resistance  
 Smaller  $V_{GS}$  ringing waveform during switching  
 Narrow band of the gate threshold voltage ( $3.0 \pm 0.5V$ )  
 High avalanche durability

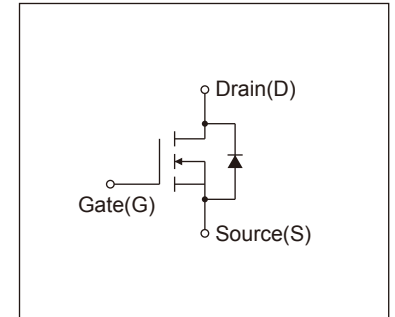
### ■ Applications

Switching regulators  
 UPS (Uninterruptible Power Supply)  
 DC-DC converters

### ■ Outline Drawings [mm]



### ■ Equivalent circuit schematic



### ■ Maximum Ratings and Characteristics

#### ● Absolute Maximum Ratings at $T_c=25^\circ\text{C}$ (unless otherwise specified)

Description	Symbol	Characteristics	Unit	Remarks
Drain-Source Voltage	$V_{DS}$	500	V	
	$V_{DSX}$	500	V	$V_{GS} = -30V$
Continuous Drain Current	$I_D$	$\pm 5$	A	
Pulsed Drain Current	$I_{DP}$	$\pm 20$	A	
Gate-Source Voltage	$V_{GS}$	$\pm 30$	V	
Repetitive and Non-Repetitive Maximum Avalanche Current	$I_{AR}$	5	A	Note*1
Non-Repetitive Maximum Avalanche Energy	$E_{AS}$	171	mJ	Note*2
Repetitive Maximum Avalanche Energy	$E_{AR}$	6.0	mJ	Note*3
Peak Diode Recovery $dV/dt$	$dV/dt$	5.3	kV/ $\mu\text{s}$	Note*4
Peak Diode Recovery $-di/dt$	$-di/dt$	100	A/ $\mu\text{s}$	Note*5
Maximum Power Dissipation	$P_D$	1.67 60	W	$T_a=25^\circ\text{C}$ $T_c=25^\circ\text{C}$
Operating and Storage Temperature range	$T_{ch}$ $T_{stg}$	150 -55 to +150	$^\circ\text{C}$	

#### ● Electrical Characteristics at $T_c=25^\circ\text{C}$ (unless otherwise specified)

Description	Symbol	Conditions	min.	typ.	max.	Unit
Drain-Source Breakdown Voltage	$BV_{DSS}$	$I_D=250\mu\text{A}$ , $V_{GS}=0V$	500	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	$I_D=250\mu\text{A}$ , $V_{DS}=V_{GS}$	2.5	3.0	3.5	V
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=500V$ , $V_{GS}=0V$ , $T_{ch}=25^\circ\text{C}$	-	-	25	$\mu\text{A}$
		$V_{DS}=400V$ , $V_{GS}=0V$ , $T_{ch}=125^\circ\text{C}$	-	-	250	
Gate-Source Leakage Current	$I_{GSS}$	$V_{GS}=\pm 30V$ , $V_{DS}=0V$	-	10	100	nA
Drain-Source On-State Resistance	$R_{DS(on)}$	$I_D=2.5A$ , $V_{GS}=10V$	-	1.28	1.50	$\Omega$
Forward Transconductance	$g_{fs}$	$I_D=2.5A$ , $V_{DS}=25V$	2.5	5	-	S
Input Capacitance	$C_{iss}$	$V_{DS}=25V$	-	610	915	pF
Output Capacitance	$C_{oss}$	$V_{GS}=0V$	-	66	99	
Reverse Transfer Capacitance	$C_{rss}$	$f=1\text{MHz}$	-	4.7	7.1	
Turn-On Time	$t_{d(on)}$	$V_{cc}=300V$	-	10	15	ns
	$t_r$	$V_{GS}=10V$	-	7	10.5	
Turn-Off Time	$t_{d(off)}$	$I_D=2.5A$	-	45	67.5	
	$t_f$	$R_G=24\Omega$	-	13.5	20.3	
Total Gate Charge	$Q_G$	$V_{cc}=250V$	-	21	32	nC
Gate-Source Charge	$Q_{GS}$	$I_D=5A$	-	6	9	
Gate-Drain Charge	$Q_{GD}$	$V_{GS}=10V$	-	5.5	8.3	
Avalanche Capability	$I_{AV}$	$L=5.01\text{mH}$ , $T_{ch}=25^\circ\text{C}$	5	-	-	A
Diode Forward On-Voltage	$V_{SD}$	$I_F=5A$ , $V_{GS}=0V$ , $T_{ch}=25^\circ\text{C}$	-	0.86	1.30	V
Reverse Recovery Time	$t_{rr}$	$I_F=5A$ , $V_{GS}=0V$	-	0.28	-	$\mu\text{s}$
Reverse Recovery Charge	$Q_{rr}$	$-di/dt=100A/\mu\text{s}$ , $T_{ch}=25^\circ\text{C}$	-	1.8	-	$\mu\text{C}$

#### ● Thermal Characteristics

Description	Symbol	Test Conditions	min.	typ.	max.	Unit
Thermal resistance	$R_{th(ch-c)}$	Channel to Case			1.200	$^\circ\text{C/W}$
	$R_{th(ch-a)}$	Channel to Ambient			75.0	$^\circ\text{C/W}$

Note \*1 :  $T_{ch} \leq 150^\circ\text{C}$

Note \*2 :  $T_{ch}=25^\circ\text{C}$ ,  $I_{AS}=2A$ ,  $L=78.3\text{mH}$ ,  $V_{cc}=50V$ ,  $R_G=50\Omega$

$E_{AS}$  limited by maximum channel temperature and avalanche current.  
 See to 'Avalanche Energy' graph.

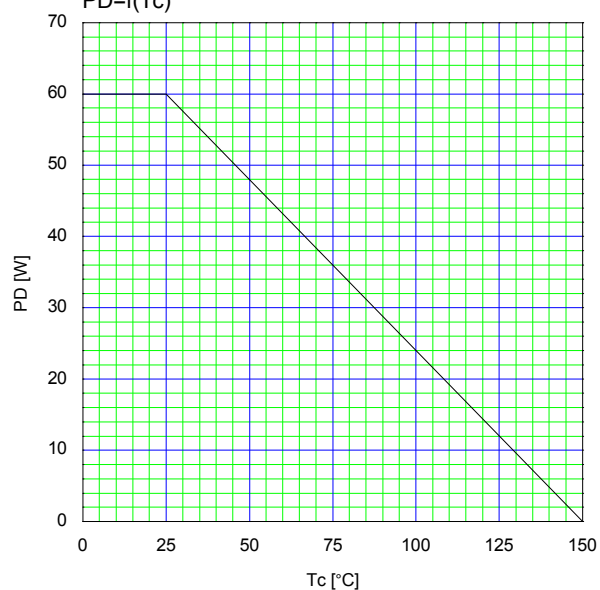
Note \*3 : Repetitive rating : Pulse width limited by maximum channel temperature.

See to the 'Transient Thermal Impedance' graph.

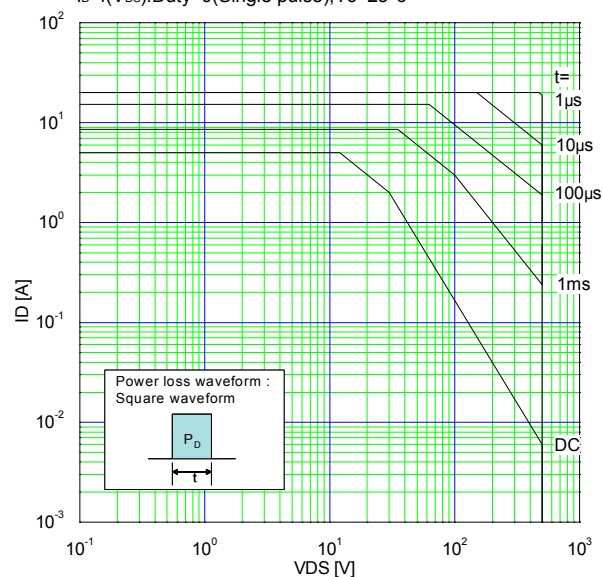
Note \*4 :  $I_F \leq I_D$ ,  $-di/dt=100A/\mu\text{s}$ ,  $V_{cc} \leq BV_{DSS}$ ,  $T_{ch} \leq 150^\circ\text{C}$ .

Note \*5 :  $I_F \leq I_D$ ,  $dV/dt=5.3kV/\mu\text{s}$ ,  $V_{cc} \leq BV_{DSS}$ ,  $T_{ch} \leq 150^\circ\text{C}$ .

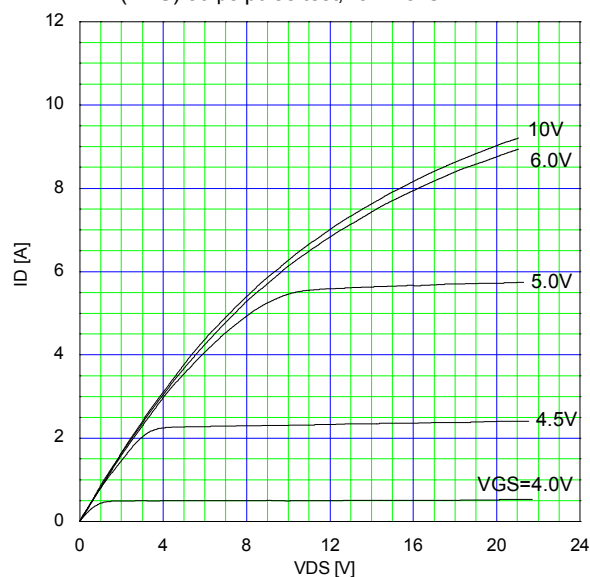
Allowable Power Dissipation  
 $P_D = f(T_c)$



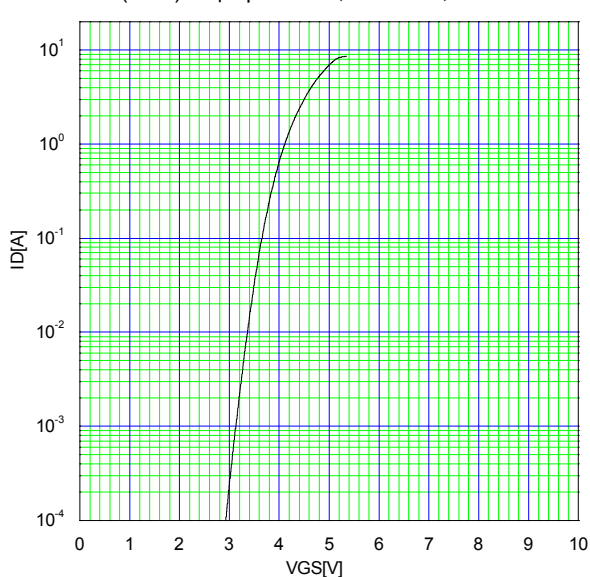
Safe Operating Area  
 $I_D = f(V_{DS})$ : Duty=0 (Single pulse),  $T_c = 25^\circ\text{C}$



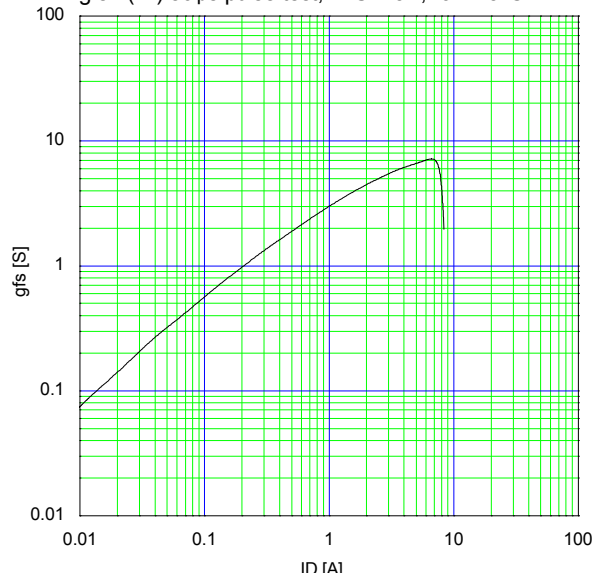
Typical Output Characteristics  
 $I_D = f(V_{DS})$ : 80 μs pulse test,  $T_{ch} = 25^\circ\text{C}$



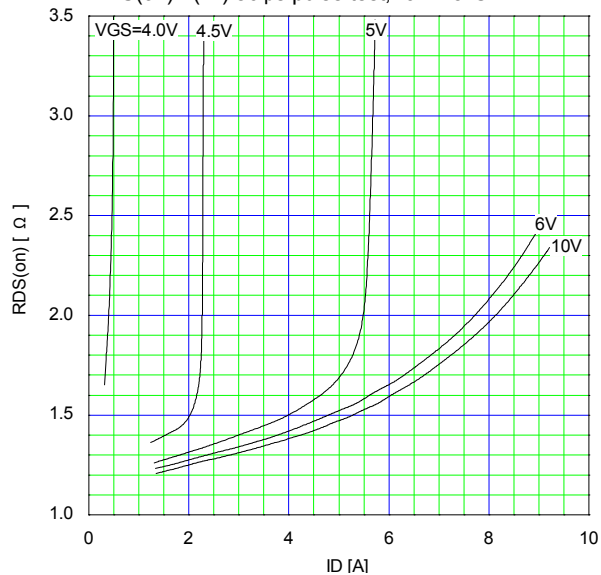
Typical Transfer Characteristic  
 $I_D = f(V_{GS})$ : 80 μs pulse test,  $V_{DS} = 25\text{V}$ ,  $T_{ch} = 25^\circ\text{C}$



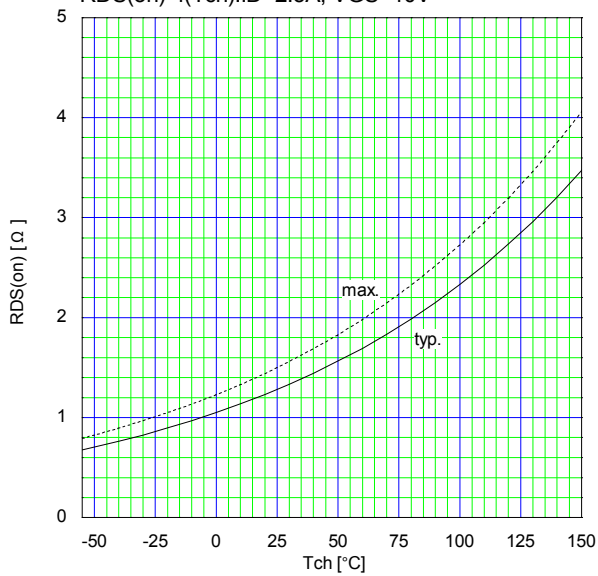
Typical Transconductance  
 $g_{fs} = f(I_D)$ : 80 μs pulse test,  $V_{DS} = 25\text{V}$ ,  $T_{ch} = 25^\circ\text{C}$



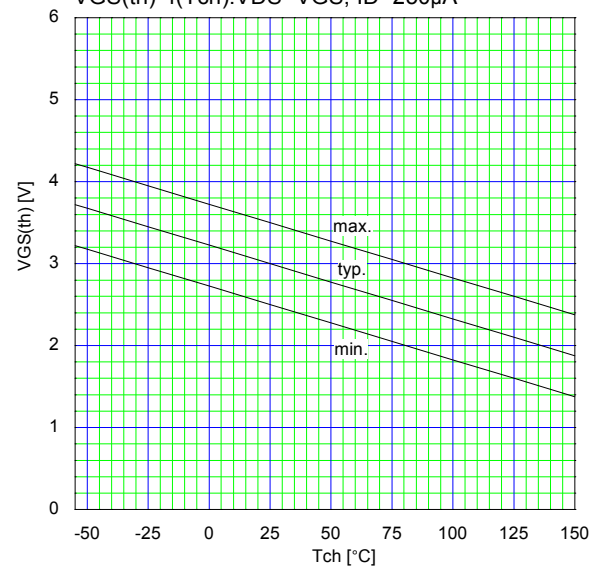
Typical Drain-Source on-state Resistance  
 $R_{DS(on)} = f(I_D)$ : 80 μs pulse test,  $T_{ch} = 25^\circ\text{C}$



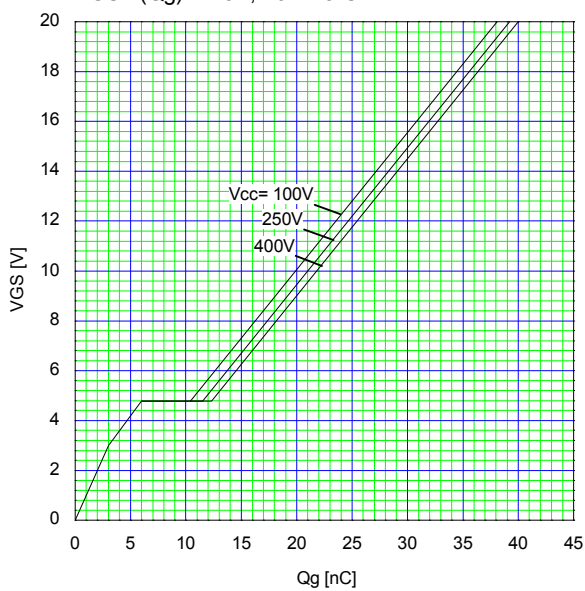
Drain-Source On-state Resistance  
 $R_{DS(on)} = f(T_{ch}): I_D = 2.5A, V_{GS} = 10V$



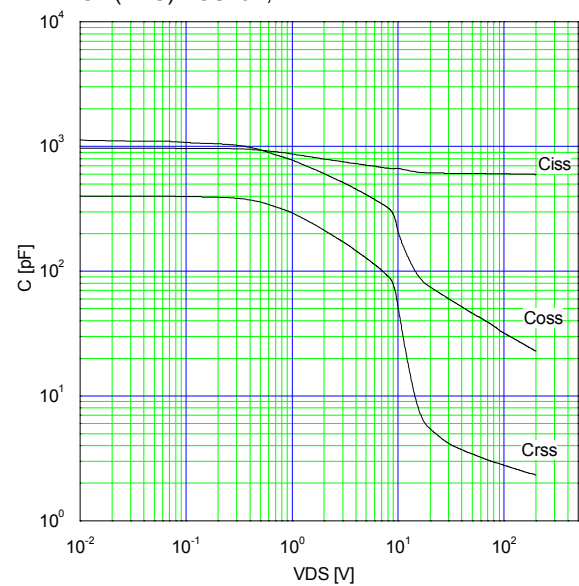
Gate Threshold Voltage vs.  $T_{ch}$   
 $V_{GS(th)} = f(T_{ch}): V_{DS} = V_{GS}, I_D = 250\mu A$



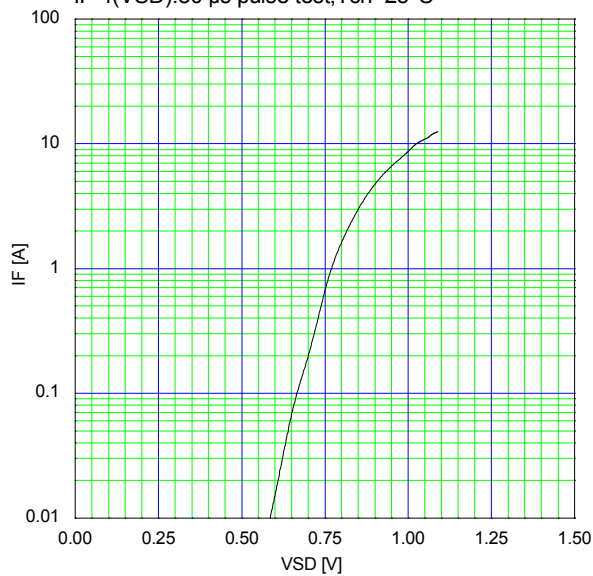
Typical Gate Charge Characteristics  
 $V_{GS} = f(Q_g): I_D = 5A, T_{ch} = 25^{\circ}C$



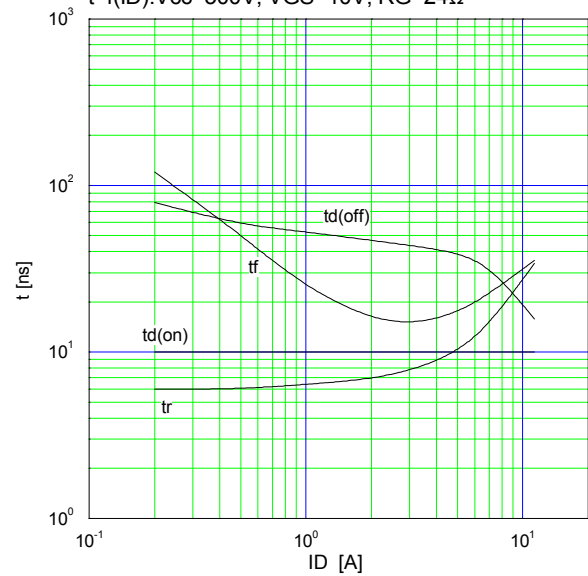
Typical Capacitance  
 $C = f(V_{DS}): V_{GS} = 0V, f = 1MHz$

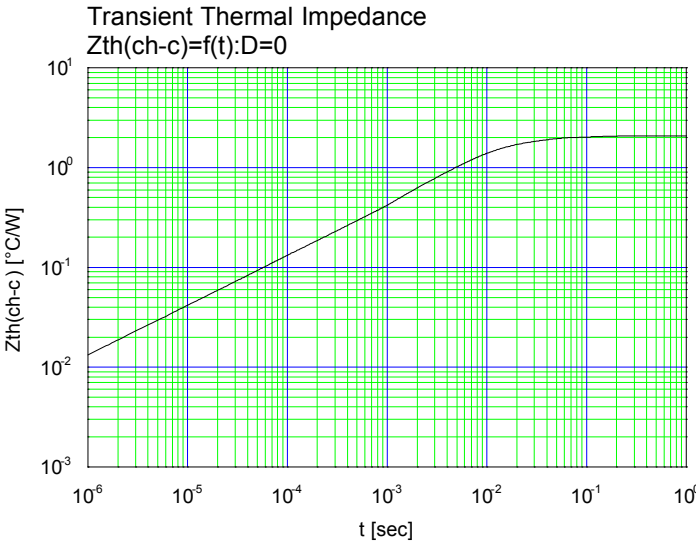
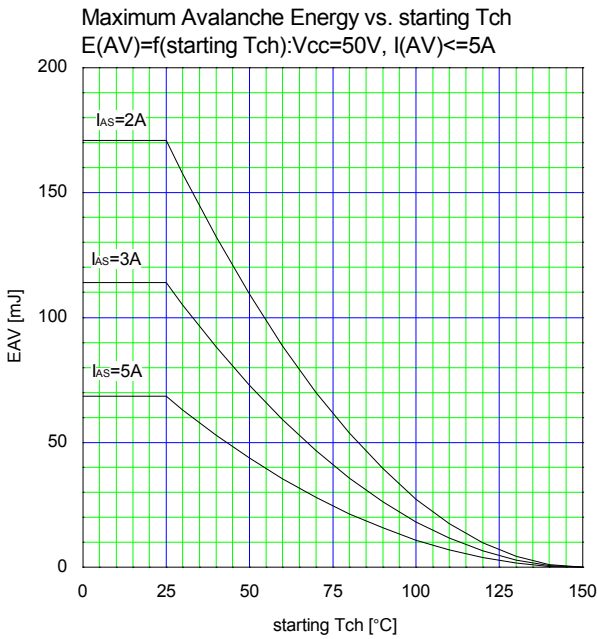


Typical Forward Characteristics of Reverse Diode  
 $I_F = f(V_{SD}): 80\mu s$  pulse test,  $T_{ch} = 25^{\circ}C$



Typical Switching Characteristics vs.  $I_D$   
 $t = f(I_D): V_{CC} = 300V, V_{GS} = 10V, R_G = 24\Omega$





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