

FML13N60ES

FUJI POWER MOSFET

Super FAP-E³ series

N-CHANNEL SILICON POWER MOSFET

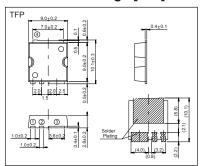
■ Features

Maintains both low power loss and low noise Lower R_{DS}(on) characteristic More controllable switching dv/dt by gate resistance Smaller V_{GS} ringing waveform during switching Narrow band of the gate threshold voltage (4.2±0.5V) High avalanche durability

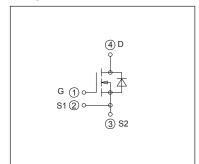
Applications

Switching regulators UPS (Uninterruptible Power Supply) DC-DC converters

■ Outline Drawings [mm]



■ Equivalent circuit schematic



■ Maximum Ratings and Characteristics

Absolute Maximum Ratings at Tc=25°C (unless otherwise specified)

Description	Symbol	Characteristics	Unit	Remarks	
Drain Cauras Valtara	V _{DS}	600	V		
Drain-Source Voltage	V _{DSX}	600	V	V _{GS} = -30V	
Continuous Drain Current	ID	±13	Α		
Pulsed Drain Current	IDP	±52	Α		
Gate-Source Voltage	V _G s	±30	V		
Repetitive and Non-Repetitive Maximum Avalanche Current	Iar	13	Α	Note*1	
Non-Repetitive Maximum Avalanche Energy	Eas	471.5	mJ	Note*2	
Repetitive Maximum Avalanche Energy	Ear	22.5	mJ	Note*3	
Peak Diode Recovery dV/dt	dV/dt	4.7	kV/µs	Note*4	
Peak Diode Recovery -di/dt	-di/dt	100	A/µs	Note*5	
Maximum Bower Discination	Po	1.44	W	Ta=25°C	
Maximum Power Dissipation	FD .	225	VV	Tc=25°C	
Oneveting and Staves Temperature rooms	Tch	150	°C		
Operating and Storage Temperature range	Tstg	-55 to +150	°C		

● Electrical Characteristics at Tc=25°C (unless otherwise specified)

Description	Symbol	Conditions		min.	typ.	max.	Unit
Drain-Source Breakdown Voltage	BVDSS	I _D =250μA, V _{GS} =0V		600	-	-	V
Gate Threshold Voltage	V _{GS} (th)	I _D =250µA, V _{DS} =V _{GS}		3.7	4.2	4.7	V
Zana Cata Valtana Busin Comment	Ipss	V _{DS} =600V, V _{GS} =0V	T _{ch} =25°C	-	-	25	μА
Zero Gate Voltage Drain Current		V _{DS} =480V, V _{GS} =0V	T _{ch} =125°C	-	-	250	
Gate-Source Leakage Current	Igss	V _{GS} =±30V, V _{DS} =0V	V _{GS} =±30V, V _{DS} =0V		10	100	nA
Drain-Source On-State Resistance	R _{DS} (on)	I _D =6.5A, V _{GS} =10V		-	0.50	0.58	Ω
Forward Transconductance	g fs	I _D =6.5A, V _{DS} =25V	I _D =6.5A, V _{DS} =25V		10	-	S
Input Capacitance	Ciss	V _{DS} =25V V _{GS} =0V f=1MHz		-	1700	2550	pF
Output Capacitance	Coss			-	190	285	
Reverse Transfer Capacitance	Crss			-	10	15	
Turn-On Time	td(on)	V_{cc} =300V V_{GS} =10V I_{D} =6.5A R_{G} =18 Ω		-	38	57	ns
	tr			-	24	36	
Turn-Off Time	td(off)			-	86	129	
	tf			-	16	24	
Total Gate Charge	Q _G	Vcc=300V Ib=13A Vcs=10V		-	48	56	nC
Gate-Source Charge	QGS			-	16	24	
Drain-Source Crossover Charge	Qsw			-	7	10.5	
Gate-Drain Charge	Q _{GD}			-	16	24	
Avalanche Capability	Iav	L=2.36mH, Tch=25°C		13	-	-	Α
Diode Forward On-Voltage	V _{SD}	I _F =13A, V _{GS} =0V, T _{ch} =25°C		-	0.90	1.08	V
Reverse Recovery Time	trr	I _F =13A, V _{GS} =0V		-	0.7	-	μS
Reverse Recovery Charge	Qrr	-di/dt=100A/µs, Tch=25°C		-	8	-	μC

Thermal Characteristics

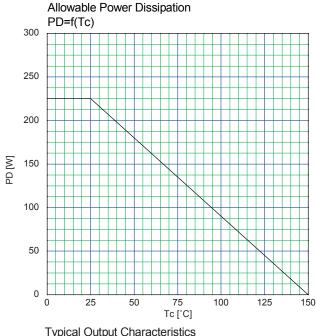
Description	Symbol	Test Conditions	min.	typ.	max.	Unit
Thermal resistance	Rth (ch-c)	Channel to case			0.56	°C/W
	Rth (ch-a)	Channel to Ambient			87	°C/W
	Rth (ch-a)	Channel to Ambient Note*6			52	°C/W

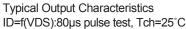
Note *1 : Tch≤150°C

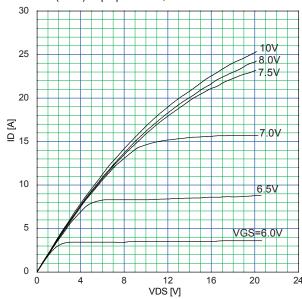
Note *2 : Stating Tch=25°C, Ias=5A, L=33.8mH, Vcc=50V, Re=10Ω, Eas limited by maximum channel temperature and avalanche current. Note *3 : Repetitive rating : Pulse width limited by maximum channel temperature Note *4 : IF≤-ID, -di/dt=100A/µs, Vcc≤BVDSS, Tch≤150°C

Note *5 : Ir≤-Ib, dv/dt=6.3kV/Js, Vcc≤BVbss, Tch≤150°C.
Note *6 : Surface mounted on 1000mm², t=1.6mm FR-4 PCB (Drain pad area : 500mm²)

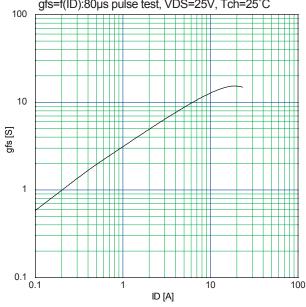
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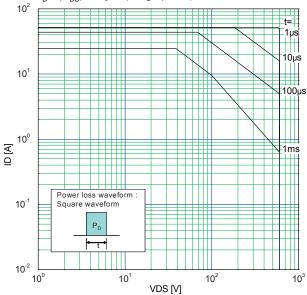




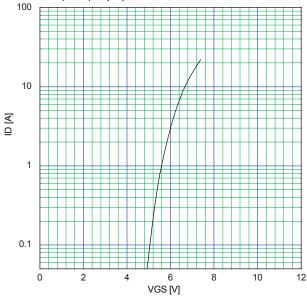
Typical Transconductance gfs=f(ID):80µs pulse test, VDS=25V, Tch=25°C



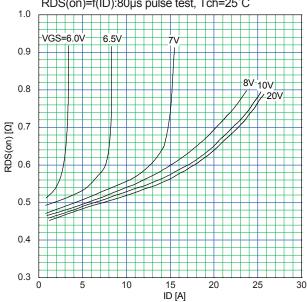
Safe Operating Area $I_D=f(V_{DS})$:Duty=0(Singlepulse), Tc=25 $^{\circ}$ C

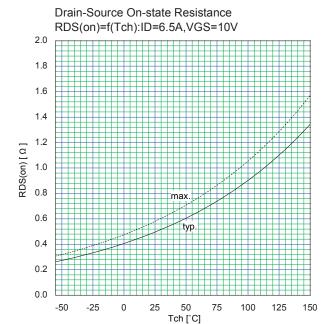


Typical Transfer Characteristic ID=f(VGS):80µs pulse test, VDS=25V, Tch=25°C

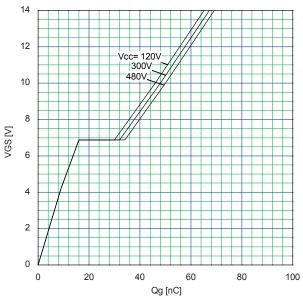


Typical Drain-Source on-state Resistance RDS(on)=f(ID):80µs pulse test, Tch=25°C

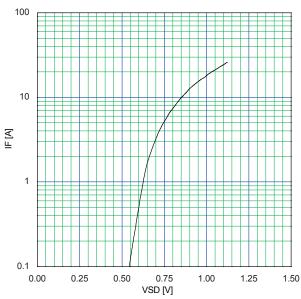




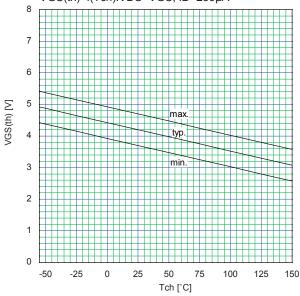
Typical Gate Charge Characteristics VGS=f(Qg):ID=13A, Tch=25°C



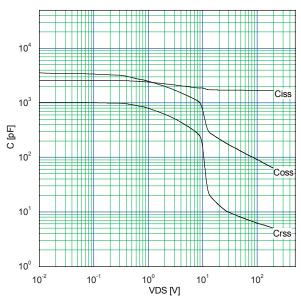
Typical Forward Characteristics of Reverse Diode IF=f(VSD):80µs pulse test, Tch=25°C



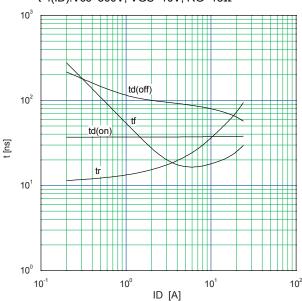
Gate Threshold Voltage vs. Tch VGS(th)=f(Tch):VDS=VGS, ID=250µA



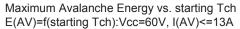
Typical Capacitance C=f(VDS):VGS=0V, f=1MHz

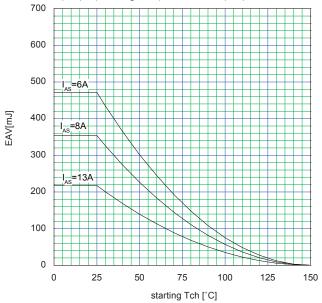


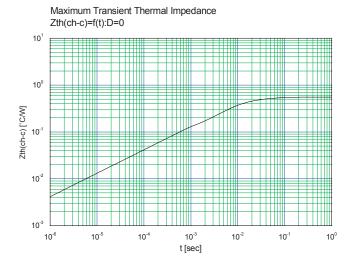
Typical Switching Characteristics vs. ID t=f(ID):Vcc=300V, VGS=10V, RG=18 Ω



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