

FMW30N60S1FDHF

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FUJI POWER MOSFET

Super J-MOS series

N-Channel enhancement mode power MOSFET

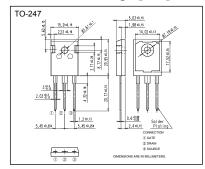
Features

Pb-free lead terminal RoHS compliant uses Halogen-free molding compound

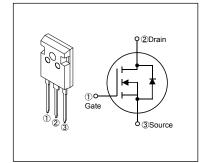
Applications

For switching

Outline Drawings [mm]



Equivalent circuit schematic



■ Absolute Maximum Ratings at T_c=25°C (unless otherwise specified)

Parameter	Symbol	Characteristics	Unit	Remarks
Duain Sauras Valtaga	V _{DS}	600	V	
Drain-Source Voltage	V _{DSX}	600	V	V _{GS} =-30V
Continuous Brain Current		±30	Α	Tc=25°C Note*1
Continuous Drain Current	l _D	±19	Α	Tc=100°C Note*1
Pulsed Drain Current	IDP	±90	Α	Note*1
Gate-Source Voltage	V _{GS}	±30	V	
Repetitive and Non-Repetitive Maximum Avalanche Current	lar	6.6	А	Note *2
Non-Repetitive Maximum Avalanche Energy	Eas	849.2	mJ	Note *3
Maximum Drain-Source dV/dt	dV _{DS} /dt	50	kV/μs	V _{DS} ≤ 600V
Peak Diode Recovery dV/dt	dV/dt	30	kV/μs	Note *4
Peak Diode Recovery -di/dt	-di/dt	100	A/µs	Note *5
Manimum Damas Disaination		2.5	10/	T _a =25°C
Maximum Power Dissipation	P₀	220	W	Tc=25°C
Oncertion and Otenana Temperature and a	Tch	150	°C	
Operating and Storage Temperature range	T _{stg}	-55 to +150	°C	

■ Electrical Characteristics at T_c=25°C (unless otherwise specified) Static Ratings

Parameter	Symbol	Conditions		min.	typ.	max.	Unit
Drain-Source Breakdown Voltage	BV _{DSS}	I _D =250μA V _{GS} =0V		600	-	-	V
Gate Threshold Voltage	V _{GS(th)}	I _D =1mA V _{DS} =V _{GS}		3	4	5	V
Zero Gate Voltage Drain Current	Inss	V _{DS} =600V V _{GS} =0V	T _{ch} =25°C	-	-	25	μΑ
		V _{DS} =480V V _{GS} =0V	T _{ch} =125°C	-	150	-	
Gate-Source Leakage Current	Igss	V _{GS} = ± 30V V _{DS} =0V		-	10	100	nA
Drain-Source On-State Resistance	R _{DS(on)}	I _D =15A V _{GS} =10V		-	0.111	0.132	Ω
Gate resistance	R _G	f=1MHz, open drain		-	3.3	-	Ω

Note *1 : Limited by maximum channel temperature.
Note *2 : T_{ch}≤150°C, See Fig.1 and Fig.2
Note *3 : Starting T_{ch}=25°C, I_{AS}=4A, L=97.3mH, V_{DD}=60V, R_G=50Ω, See Fig.1 and Fig.2

Eas limited by maximum channel temperature and avalanche current. Note *4 : IF \le -ID, -di/dt=100A/ μ s, VDs peak \le 600V, Tch \le 150°C.

Note *5 : IF \leq -ID, dV/dt=30kV/ μ s, VDs peak \leq 600V, Tch \leq 150°C.

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Dynamic Ratings

Parameter	Symbol	Conditions	min.	typ.	max.	Unit
Forward Transconductance	g _{fs}	I _D =15A V _{DS} =25V	11	23	-	S
Input Capacitance	Ciss	V _{DS} =400V	-	2080	-	
Output Capacitance	Coss	V _{GS} =0V	-	60	-	
Reverse Transfer Capacitance	Crss	f=250kHz	-	4	-	
Effective output capacitance, energy related (Note *6)	C _{o(er)}	V _{GS} =0V V _{DS} =0400V	-	160	-	pF
Effective output capacitance, time related (Note *7)	C _{o(tr)}	V _{GS} =0V V _{DS} =0400V I _D =constant	-	535	-	
Turn-On Time	t _{d(on)}	V _{DD} =400V, V _{SS} =10V I _D =15A, R _G =27Ω See Fig.3 and Fig.4	-	119	-	
Turn-On Time	t r		-	32	-	ns
Turn-Off Time	t _{d(off)}		-	186	-	
Turn-Oπ Time	t _f		-	22	-	
Total Gate Charge	Q _G	V _{DD} =400V, I _D =30A V _{GS} =10V See Fig.5	-	73	-	
Gate-Source Charge	Q _{GS}		-	22	-	nC
Gate-Drain Charge	Q _{GD}		-	29	-	IIC IIC
Drain-Source crossover Charge	Qsw		-	11.5	-	

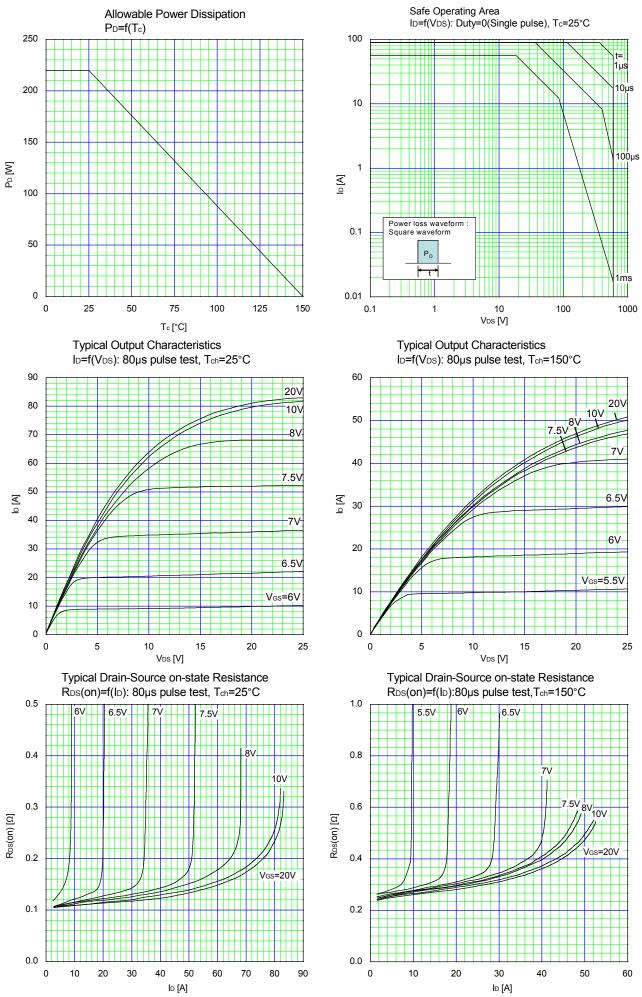
Note $^{\star}6$: $C_{O(er)}$ is a fixed capacitance that gives the same stored energy as C_{OSS} while V_{DS} is rising from 0 to 400V. Note $^{\star}7$: $C_{O(tr)}$ is a fixed capacitance that gives the same charging times as C_{OSS} while V_{DS} is rising from 0 to 400V.

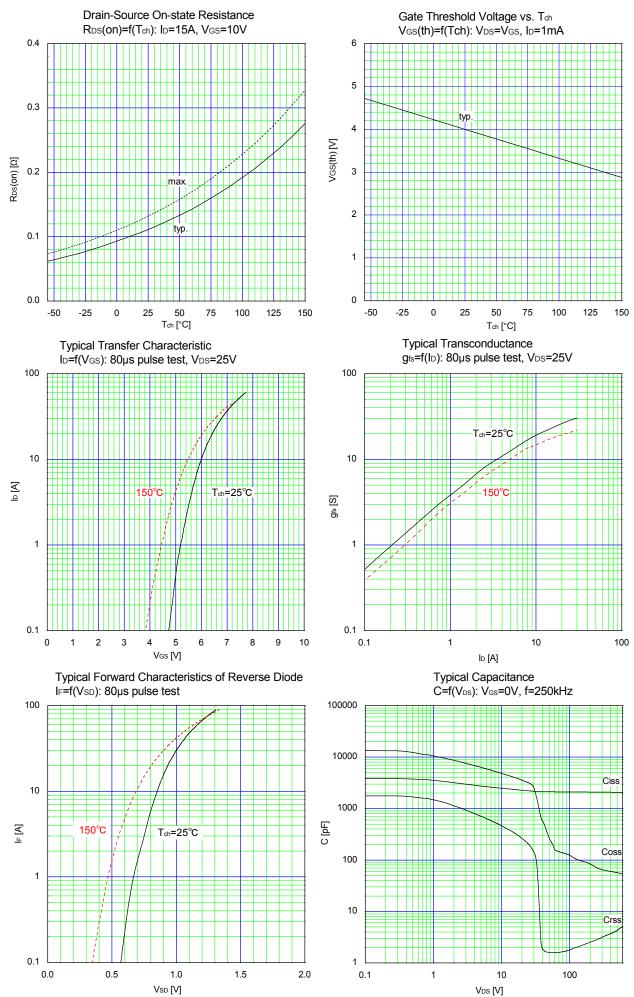
• Reverse Diode

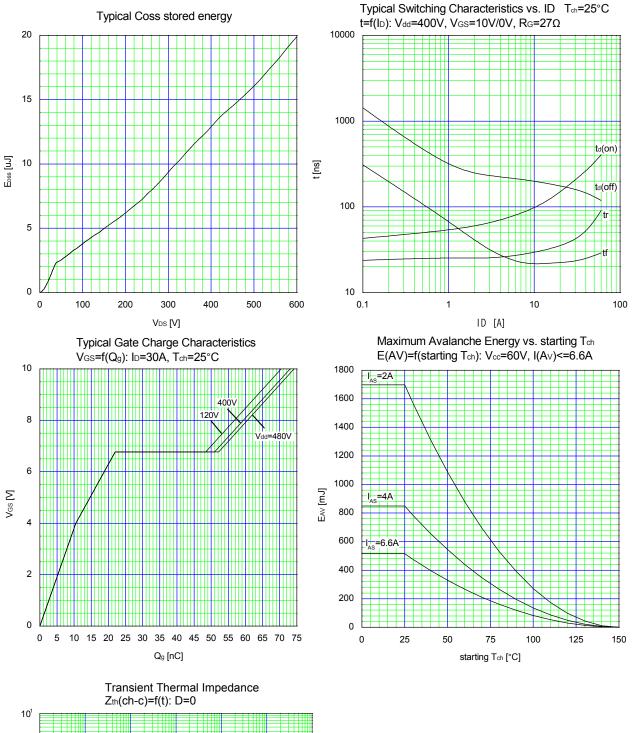
Parameter	Symbol	Conditions	min.	typ.	max.	Unit
Avalanche Capability	lav	L=21.7mH, T _{oh} =25°C See Fig.1 and Fig.2	6.6	-	-	Α
Diode Forward On-Voltage	V _{SD}	I _F =30A, V _{GS} =0V T _{Ch} =25°C	-	1	1.35	V
Reverse Recovery Time	trr	I _F =30A, V _{DD} =400V -di/dt=100A/µs T _{ch} =25°C See Fig.6 and Fig.7		180	-	ns
Reverse Recovery Charge	Qrr		-	1.2	1	μC
Peak Reverse Recovery Current	Irp		-	13.5	-	А

■ Thermal Resistance

Parameter	Symbol	min.	typ.	max.	Unit
Channel to Case	R _{th(ch-c)}	-	-	0.57	°C/W
Channel to Ambient	R _{th(ch-a)}	-	-	50	°C/W







Zth(ch-c)=f(t): D=0

10¹
10²
10³
10⁶
10⁵
10⁴
10³
10²
10¹
10¹
10²
10¹
10²
10¹
10²
10²
10³
10⁶
10⁵
10⁶
10⁶
10⁶
10⁶
10⁷
10⁸
10

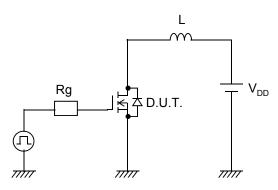


Fig.1 Avalanche Test circuit

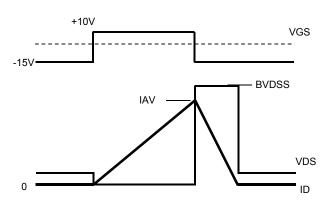


Fig.2 Operating waveforms of Avalanche Test

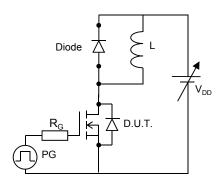


Fig.3 Switching Test circuit

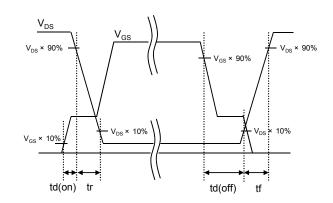


Fig.4 Operating waveform of Switching Test

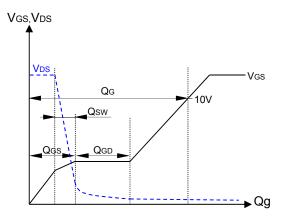


Fig.5 Operating waveform of Gate charge Test

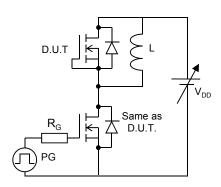


Fig.6 Reverse recovery Test circuit

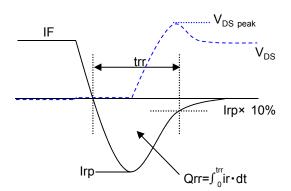
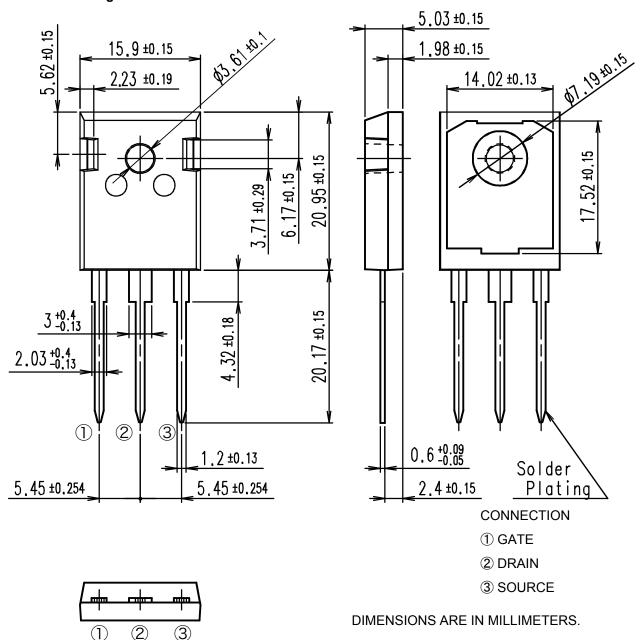
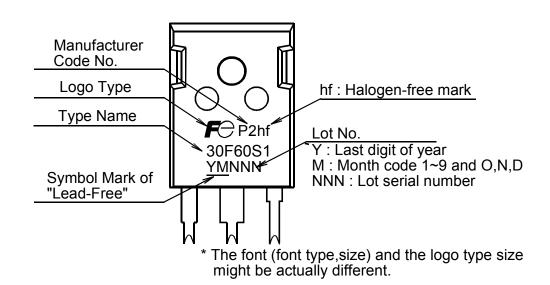


Fig.7 Operating waveform of Reverse recovery Test

Outview: TO-247 Package



Marking



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