

# FMW57N60S1FDHF

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**FUJI POWER MOSFET** 

## **Super J-MOS series**

### N-Channel enhancement mode power MOSFET

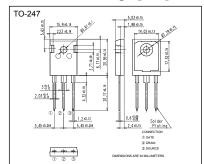
### Features

Pb-free lead terminal RoHS compliant uses Halogen-free molding compound

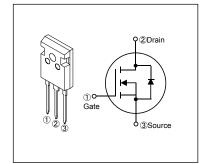
### Applications

For switching

### Outline Drawings [mm]



### Equivalent circuit schematic



### ■ Absolute Maximum Ratings at T<sub>c</sub>=25°C (unless otherwise specified)

Parameter	Symbol	Characteristics	Unit	Remarks
Drain Course Voltage	V <sub>DS</sub>	600	V	
Drain-Source Voltage	V <sub>DSX</sub>	600	V	V <sub>GS</sub> =-30V
Continuous Brain Current	ID	±57	Α	Tc=25°C Note*1
Continuous Drain Current		±36	Α	Tc=100°C Note*1
Pulsed Drain Current	I <sub>DP</sub>	±171	Α	Note*1
Gate-Source Voltage	V <sub>GS</sub>	±30	V	
Repetitive and Non-Repetitive Maximum Avalanche Current	lar	11.1	А	Note *2
Non-Repetitive Maximum Avalanche Energy	Eas	2109.8	mJ	Note *3
Maximum Drain-Source dV/dt	dV <sub>DS</sub> /dt	50	kV/μs	V <sub>DS</sub> ≤ 600V
Peak Diode Recovery dV/dt	dV/dt	30	kV/μs	Note *4
Peak Diode Recovery -di/dt	-di/dt	100	A/µs	Note *5
Manimum Danier Dissination	Po	2.5	10/	T <sub>a</sub> =25°C
Maximum Power Dissipation		445	W	Tc=25°C
O	Tch	150	°C	
Operating and Storage Temperature range	T <sub>stg</sub>	-55 to +150	°C	

### ■ Electrical Characteristics at T<sub>c</sub>=25°C (unless otherwise specified) Static Ratings

Parameter	Symbol	Conditions		min.	typ.	max.	Unit
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	I <sub>D</sub> =250µA V <sub>GS</sub> =0V		600	-	-	V
Gate Threshold Voltage	V <sub>GS(th)</sub>	I <sub>D</sub> =2.6mA V <sub>DS</sub> =V <sub>GS</sub>		3	4.0	5.0	V
Zero Gate Voltage Drain Current	Inss	V <sub>DS</sub> =600V V <sub>GS</sub> =0V	T <sub>ch</sub> =25°C	-	-	25	μΑ
		V <sub>DS</sub> =480V V <sub>GS</sub> =0V	T <sub>ch</sub> =125°C	-	300	-	
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> = ± 30V V <sub>DS</sub> =0V		-	10	100	nA
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	I <sub>D</sub> =28.5A V <sub>GS</sub> =10V		-	0.049	0.058	Ω
Gate resistance	R <sub>G</sub>	f=1MHz, open drain		-	1.2	-	Ω

Note \*1 : Limited by maximum channel temperature.

Note \*2 : Tch≤150°C, See Fig.1 and Fig.2

Note \*3 : Starting Tch=25°C, Ias=6.7A, L=86.2mH, Vdd=60V, Re=50Ω, See Fig.1 and Fig.2

Eas limited by maximum channel temperature and avalanche current. Note \*4 : IF  $\le$  -ID, -di/dt=100A/ $\mu$ s, VDs peak  $\le$  600V, Tch  $\le$  150°C.

Note \*5 : IF  $\leq$  -ID, dV/dt=30kV/ $\mu$ s, VDs peak  $\leq$  600V, Tch  $\leq$  150°C

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### Dynamic Ratings

Parameter	Symbol	Conditions	min.	typ.	max.	Unit
Forward Transconductance	g <sub>fs</sub>	I <sub>D</sub> =28.5A V <sub>DS</sub> =25V	22	44	-	S
Input Capacitance	Ciss	V <sub>DS</sub> =400V	-	4452	-	
Output Capacitance	Coss	V <sub>GS</sub> =0V	-	127	-	
Reverse Transfer Capacitance	Crss	f=250kHz	-	9	-	
Effective output capacitance, energy related (Note *6)	C <sub>o(er)</sub>	V <sub>GS</sub> =0V V <sub>DS</sub> =0400V	-	322	-	pF
Effective output capacitance, time related (Note *7)	C <sub>o(tr)</sub>	VGS=0V VDS=0400V ID=constant	-	1133	-	
Turn-On Time	t <sub>d(on)</sub>	V <sub>DD</sub> =400V, V <sub>GS</sub> =10V I <sub>D</sub> =28.5A, Rg=10Ω See Fig.3 and Fig.4	-	119	-	ns
Turn-On Time	<b>t</b> r		-	32	-	
Turn-Off Time	t <sub>d(off)</sub>		-	180	-	
Turni-On Time	tf		-	18	-	
Total Gate Charge	Q <sub>G</sub>	V <sub>DD</sub> =400V, I <sub>D</sub> =57A V <sub>GS</sub> =10V See Fig.5	-	158	-	
Gate-Source Charge	Q <sub>GS</sub>		-	40.5	-	
Gate-Drain Charge	Q <sub>GD</sub>		-	69	-	nC
Drain-Source crossover Charge	Qsw		-	20	-	

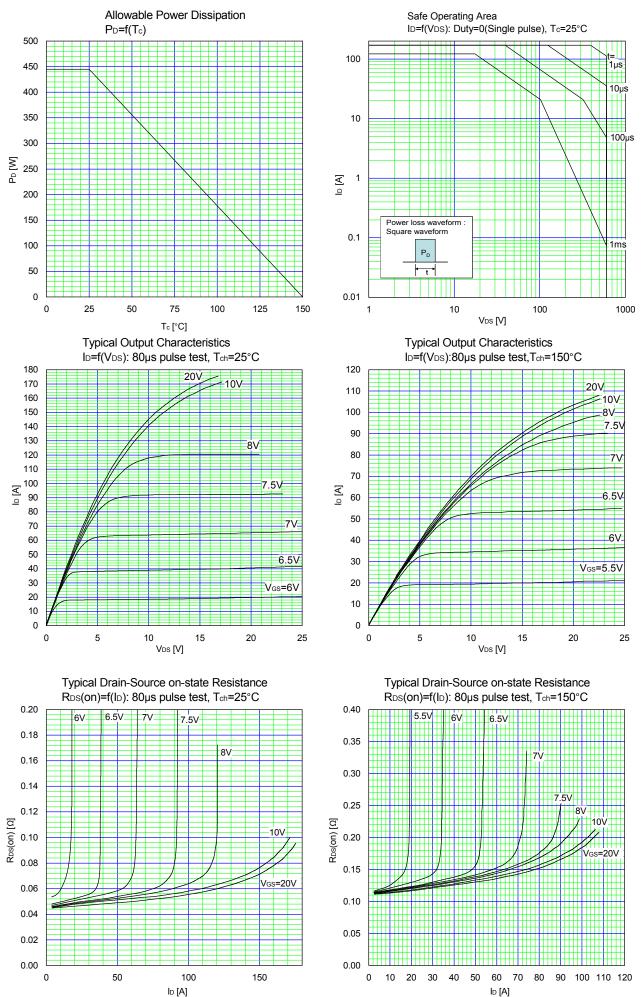
Note  $^{\star}6$ :  $C_{O(er)}$  is a fixed capacitance that gives the same stored energy as  $C_{OSS}$  while  $V_{DS}$  is rising from 0 to 400V. Note  $^{\star}7$ :  $C_{O(tr)}$  is a fixed capacitance that gives the same charging times as  $C_{OSS}$  while  $V_{DS}$  is rising from 0 to 400V.

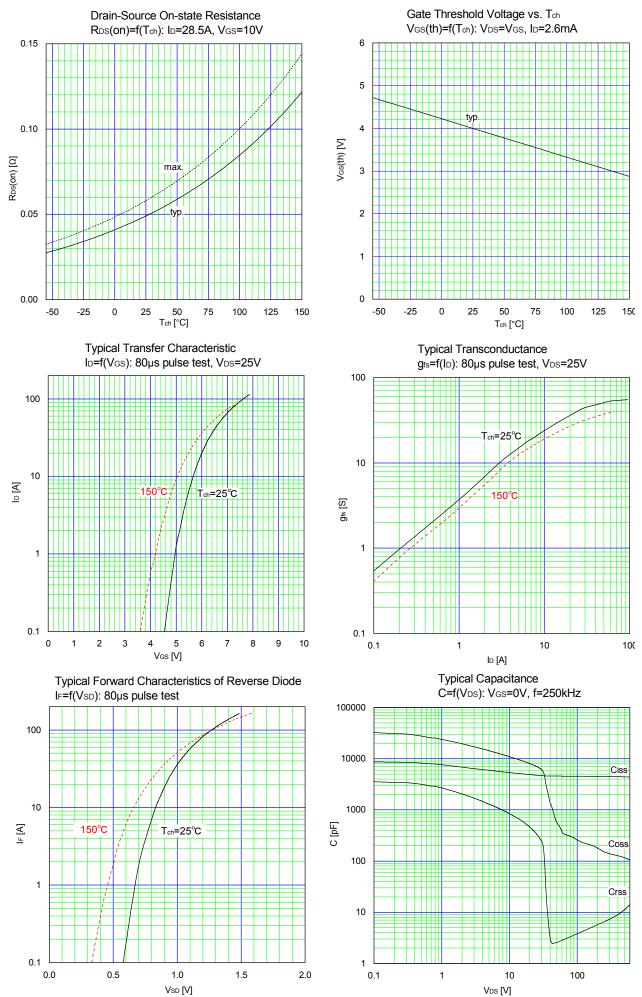
### • Reverse Diode

Parameter	Symbol	Conditions	min.	typ.	max.	Unit
Avalanche Capability	lav	L=18.9mH, T <sub>ch</sub> =25°C See Fig.1 and Fig.2	11.1	-	-	Α
Diode Forward On-Voltage	V <sub>SD</sub>	I <sub>F</sub> =57A, V <sub>GS</sub> =0V T <sub>ch</sub> =25°C	-	1.1	1.35	V
Reverse Recovery Time	trr	I <sub>F</sub> =57A, V <sub>DD</sub> =400V -di/dt=100A/µs T <sub>ch</sub> =25°C See Fig.6 and Fig.7		220	-	ns
Reverse Recovery Charge	Qrr		-	1.9	-	μC
Peak Reverse Recovery Current	Irp		-	17	-	Α

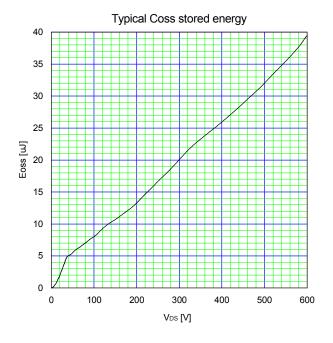
### ■ Thermal Resistance

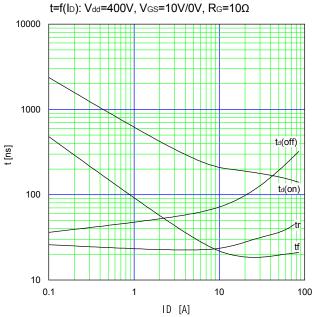
Parameter	Symbol	min.	typ.	max.	Unit
Channel to Case	R <sub>th(ch-c)</sub>	-	-	0.28	°C/W
Channel to Ambient	R <sub>th(ch-a)</sub>	-	-	50	°C/W

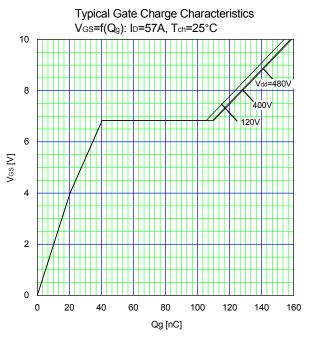


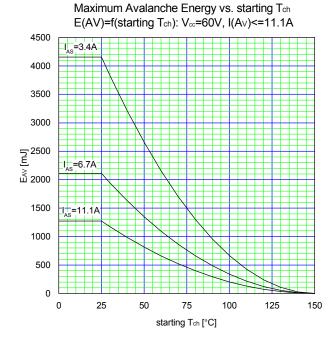


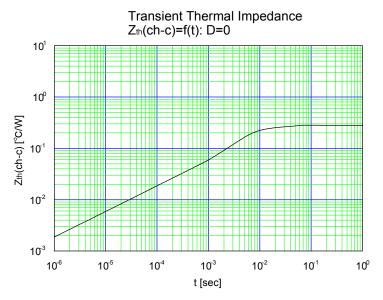
Typical Switching Characteristics vs. ID Tch=25°C











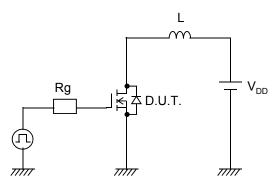


Fig.1 Avalanche Test circuit

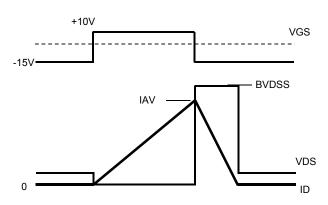


Fig.2 Operating waveforms of Avalanche Test

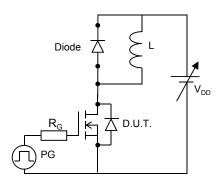


Fig.3 Switching Test circuit

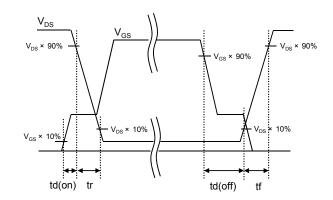


Fig.4 Operating waveform of Switching Test

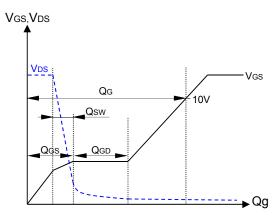


Fig.5 Operating waveform of Gate charge Test

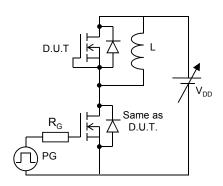


Fig.6 Reverse recovery Test circuit

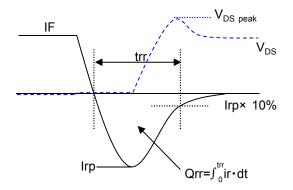
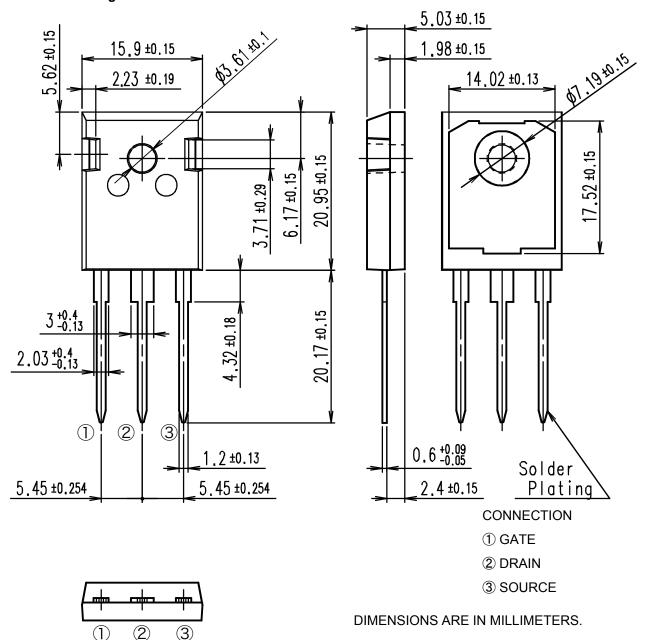
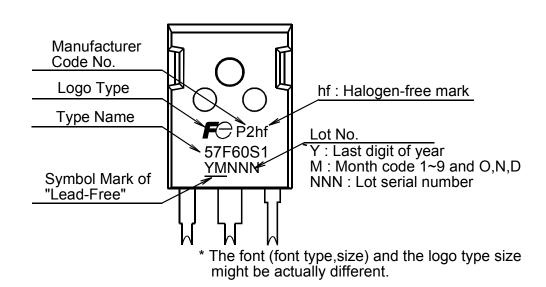


Fig.7 Operating waveform of Reverse recovery Test

### Outview: TO-247 Package



### Marking



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- Audiovisual equipment
- Electrical home appliances P
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