

IGBT Modules

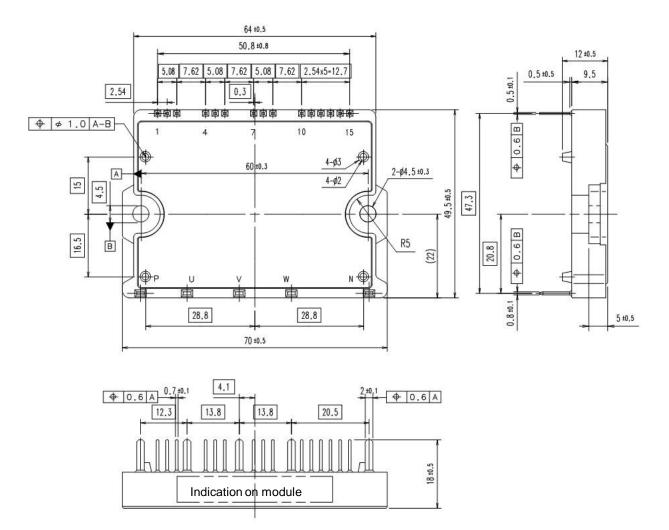
IGBT Module (X series) 1200V / 35A / IPM

■ Features

- Temperature protection provided by directly detecting the junction temperature of the IGBTs
- ·Low power loss and soft switching
- ·High performance and high reliability IGBT with overheating protection
- Higher reliability because of a big decrease in number of parts in built-in control circuit



■ Outline drawing (Unit:mm)



Weight:80g(typ.)



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■ Absolute maximum ratings

 $T_{\rm C}$ =25°C, $T_{\rm vi}$ =25°C, $V_{\rm CC}$ =15V unless otherwise specified

	Items	Symbol	Conditions	Min.	Max.	Units
Co	ollector-emitter voltage	$V_{\sf CES}$	*1	-	1200	V
Sh	ort circuit voltage	V_{SC}	*2	400	800	V
٦		I _C	DC	-	35	Α
Inverter	Collector current	I _{CP}	1ms	-	70	Α
Š		-1 _C	Duty=100% *3	-	35	Α
_	Total power dissipation	P_{tot}	IGBT 1 device *4	-	245	W
	Collector current	I _C	DC	-	-	Α
š		I _{CP}	1ms	-	-	Α
Brake	Forward current of diode	I _F		-	-	Α
_	Total power dissipation	P_{tot}	IGBT 1 device *4	-	-	W
Sι	ipply voltage of pre-driver	V _{cc}	*5	-0.5	20	V
Inp	out signal voltage	V_{in}	*6	-0.5	$V_{\rm CC} + 0.5$	V
Αla	arm signal voltage	V_{ALM}	*7	-0.5	$V_{\rm cc}$	V
Ala	arm signal current	I _{ALM}	*8	-	20	mΑ
Vii	tual junction temperature	T_{vj}		-	175	°C
Op	perating virtual junction temperature	$T_{\rm vjop}$		-	150	°C
Op	perating case temperature	T _c		-20	125	°C
Sto	orage temperature	$T_{ m stg}$		-40	125	°C
Sc	older temperature	T_{sol}	*9	-	260	°C
Isc	plating voltage	$V_{\rm isol}$	*10	-	2500	Vrms
Mo	ounting torque of screws to heat sink	Ms	Mounting(M4)	-	1.7	Nm
	ounting torque of screws to terminals	$M_{\rm t}$	Main terminals(M4)	-	-	Nm
No	otes					

- *1: V_{CES} shall be applied to the input voltage between terminal P-(U,V, W) and (U,V, W)-N.
- *2: In the case of the load inductance to be over 1µH.

- *3: Duty=150°C/R_{th(i-c)D}/(I_F×V_F Max.)×100
 *4: P_{tot}=150°C/R_{th(i-c)Q}
 *5: V_{CC} shall be applied to the input voltage between terminal No.3 and 1, 6 and 4, 9 and 7,11 and 10.
- *6: V_{in} shall be applied to the input voltage between terminal No.2 and 1, 5 and 4, 8 and 7,12~14 and 10.
- *7: V_{ALM} shall be applied to the voltage between terminal No.15 and 10.
- *8: I_{ALM} shall be applied to the input current to terminal No.15.
- *9: Immersion time 10±1sec. 1 time.
- *10: Terminal to base, 50/60Hz sine wave 1 min. All terminals should be connected together during the test.

■ Electrical characteristics

Main circuit

 T_{vi} =25°C, V_{CC} =15V unless otherwise specified

	Item	Symbol	Cor	nditions	Min.	Тур.	Max.	Units
<u>-</u>	Collector current at off signal input	I _{CES}	V _{CE} :1200V		-	-	1.0	mA
Inverter	Collector-emitter	$V_{\scriptscriptstyle{CE(sat)}}$	$I_{\rm c} = 35A$	Terminal	-	-	1.70	V
≥	saturation voltage	CE(sat)		Chip	-	1.40	-	V
<u>-</u>	Forward voltage of FWD	$V_{\scriptscriptstyle \sf F}$	I _F = 35A	Terminal	-	-	2.45	V
	I diward voltage of 1 WD	VF		Chip	-	2.00	•	V
a)	Collector current at off signal input	I _{CES}	V _{CE} = -V	•	-	-	ı	mA
Brake	Collector-emitter	$V_{\sf CE(sat)}$	I _C = -A	Terminal	-	-	-	V
32.	saturation voltage	CE(sat)		Chip	-	-	-	V
"	Forward voltage of FWD	$V_{\scriptscriptstyle F}$	/ _F = -A	Terminal	-	-	•	V
	I diward voltage of I WD	V F		Chip	-	-	•	V
Switc	hing time	t_{on}	$I_{\rm c} = 35A$	T_{vi} =150°C	0.5	-	·	μs
*11		$t_{\rm d(on)}$	$V_{DC} = 600 V$		0.5	-		μs
		$t_{ m off}$			-	-	2.0	μs
		$t_{\sf d(off)}$			-	-	1.7	μs
		$t_{\rm rr}$	$I_{\rm F}$: 35A $V_{\rm DC}$: 600V	T _{vi} =150°C	-	-	0.5	μs

^{*11:} Turn on time $(t_{on}) = t_{d(on)} + t_{r}$, Turn off time $(t_{off}) = t_{d(off)} + t_{f}$



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Control circuit

 T_{vi} =25°C, V_{CC} =15V unless otherwise specified

Item	Symbol	Conditions		Min.	Тур.	Max.	Units
Supply current of P-side	,	Switching frequency $(f_{SW}) = 0$	∼15kHz	-	-	10	mΑ
pre-driver (per one unit)	I ccp	$T_{\rm C} = -20 \sim 125^{\circ}{\rm C}$					
Supply current of N-side	,			-	-	26	mΑ
pre-driver	r ccn						
Input sinal threshold voltage	$V_{\text{inth(on)}}$	V _{in} -GND	ON	1.2	1.4	1.6	V
	$V_{\text{inth(off)}}$	V in-CIND	OFF	1.5	1.7	1.9	V

Protection circuit

 T_{vi} =25°C, V_{CC} =15V unless otherwise specified

Item		Symbol	Conditions		Min.	Тур.	Max.	Units
Over current Ir	nverter	1	T _{vi} =150°C		53	-	-	Α
protection level B	Brake	I _{oc}			-	-	-	Α
Over current protection	delay time	$t_{ ext{dOC}}$	T _{vj} =150°C		-	4.0	-	μs
Short circuit protection of	delay time	$t_{ t dSC}$	T _{vj} =150°C		-	1.0	-	μs
IGBT chips over heating		T_{jOH}	Surface of IGBT chips		175	-	-	°C
protection temperature I	evel	≠ joH						
Over heating protection	hysteresis	T_{jH}			-	20	-	ů
Under voltage protection	n level	$V_{\sf uv}$			11.0	-	12.5	V
Under voltage protection	n hysteresis	$V_{\scriptscriptstyle H}$			0.2	0.5	-	V
		$t_{\scriptscriptstyle ALM(OC)}$	ALM-GND		1.0	2.0	2.4	ms
Alarm signal hold time		$t_{\scriptscriptstyle ALM(UV)}$	$T_{\rm C}$ =-20~125°C	/ _{cc} ≧10V	3.5	4.0	4.5	ms
		$t_{\text{ALM(TjOH)}}$	_		7.0	8.0	9.0	ms
Alarm signal voltage		$V_{\scriptscriptstyle ALMH}$	ALM-GND, without protection	·	14.5	-	15.0	V
Resistance for current li	mit	$R_{\scriptscriptstyle{ALM}}$		·	960	-	1570	Ω

■ Thermal resistance characteristics ($T_c = 25$ °C)

Item			Symbol	Min.	Тур.	Max.	Units
Thermal resistance	Inverter	IGBT	$R_{th(j-c)Q}$	-	-	0.61	K/W
		FWD	$R_{th(j-c)D}$			0.97	K/W
junction to case *12	Brake	IGBT	$R_{th(j-c)Q}$	-	-	-	K/W
12		FWD	$R_{th(j-c)D}$	-	-	-	K/W
Thermal resistance case to heat sink *13			$R_{th(c-s)}$	-	0.05	-	K/W

^{*12:} For 1 device, the measurement point of the case is just under the chip.

■ Noise immunity (V_{DC} =600V, V_{CC} =15V)

Item	Conditions	Min.	Тур.	Max.	Units
Common mode	Pulse width 1µs,polarity ±,10min.	+ 2 ∩	_	_	kV
rectangular noise	Judge: no over-current, no miss operating	±2.0	_	_	ΝV

■ Recommended operating conditions

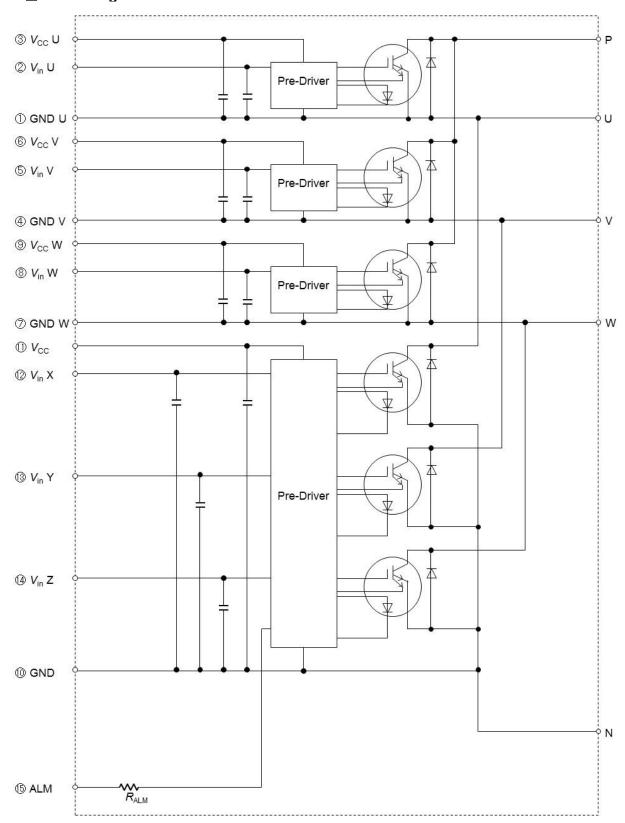
Item	Symbol	Min.	Тур.	Max.	Units
DC bus voltage	$V_{ exttt{DC}}$	-	-	800	V
Power supply voltage of pre-driver	V_{cc}	13.5	15.0	16.5	V
Switching frequency of IPM	f _{sw}	-	-	20.0	kHz
Arm shoot through blocking time for IPM's input signal *14	$t_{\sf dead}$	1.5	-	-	μs
Screw torque (M4)	-	1.3	-	1.7	Nm

^{*14:} $t_{\text{dead}} = t_{\text{off}} - t_{\text{d(on)}}$

^{*13:} This is the value which is defined mounting on the additional heat sink with 1 W/(m·K) thermal grease.

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■ Block diagram



Pre-drivers include following functions

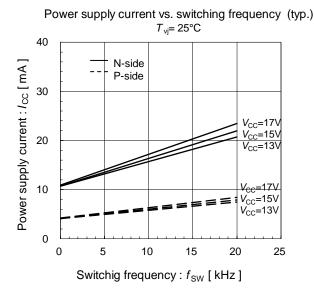
- 1. Amplifier for driver
- 2. Short circuit protection
- 3. Under voltage lockout circuit
- 4. Over current protection
- 5. IGBT chip over heating protection

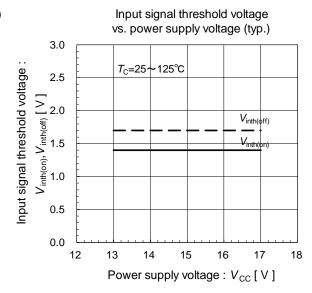


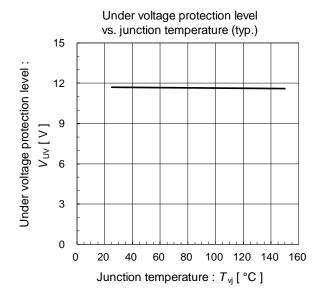
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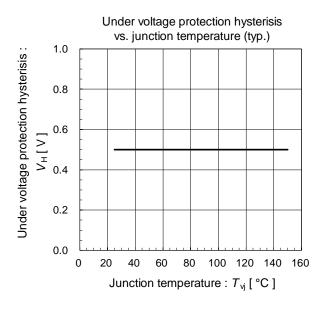
■ Characteristics (representative)

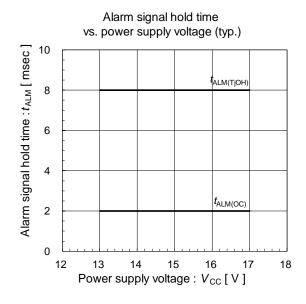
Control circuit

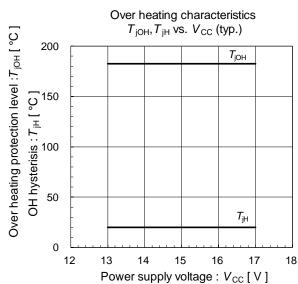






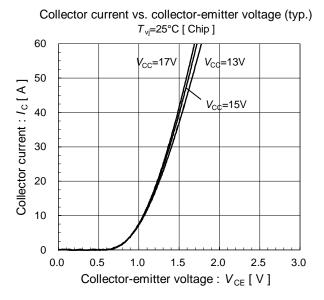


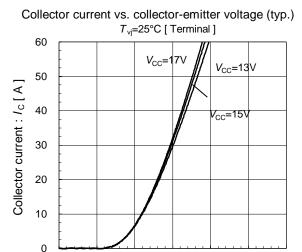




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Inverter





0.0

0.5

1.0

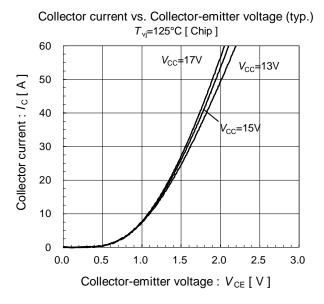
1.5

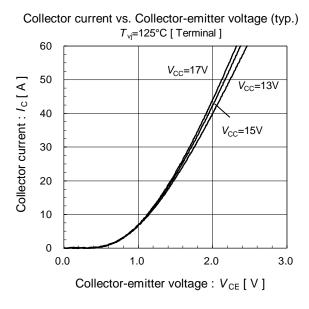
Collector-emitter voltage : V_{CE} [V]

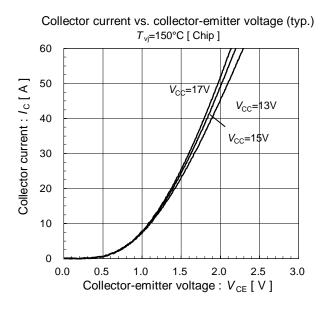
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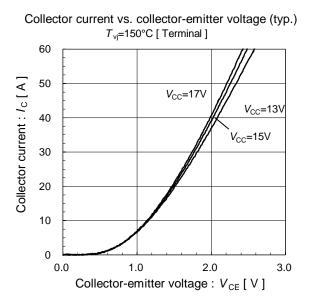
2.5

3.0

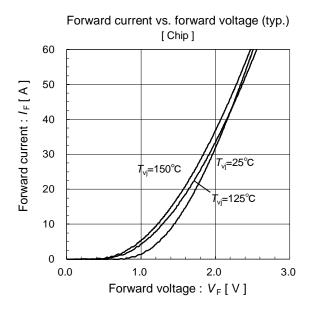


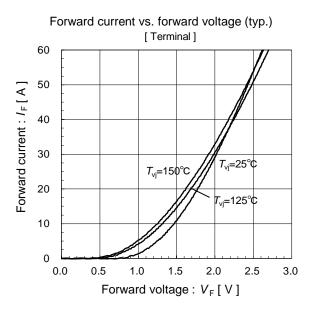


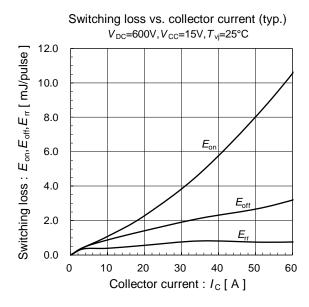


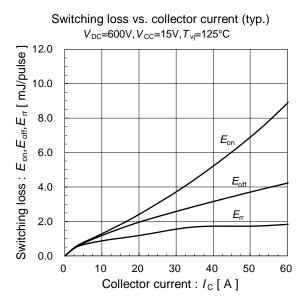


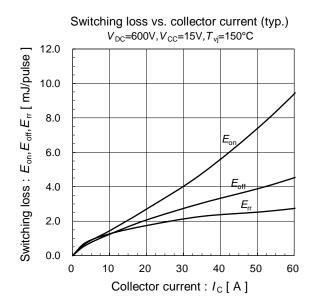




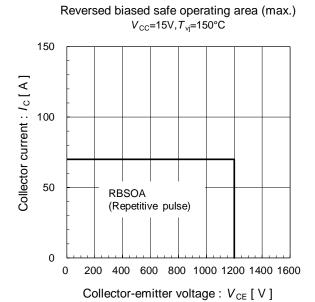


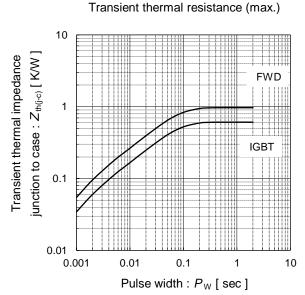


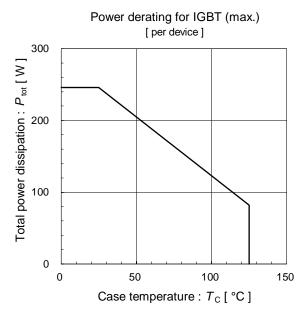


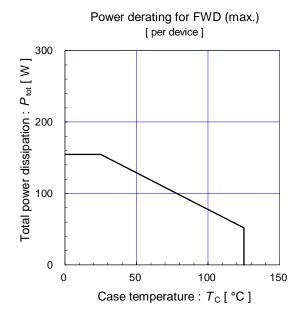




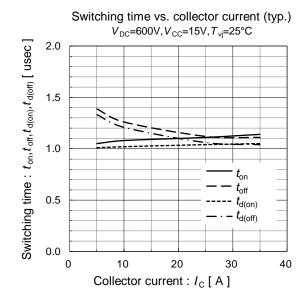


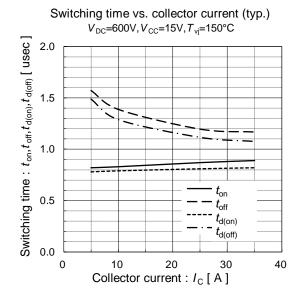


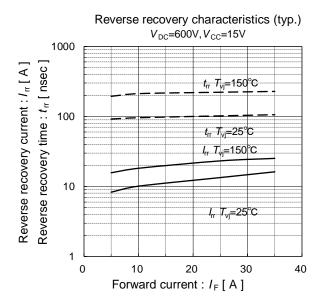


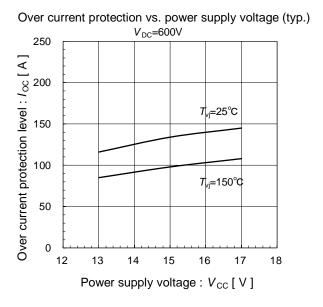














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