

FDC2AT10S65

SiC Schottky Barrier Diode

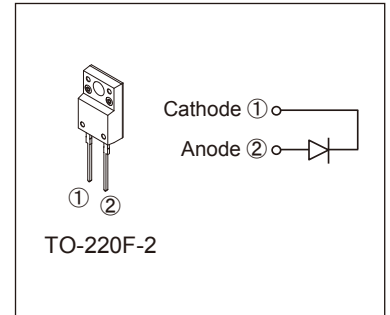
Features

- New 2nd Generation Technology
- Low forward voltage
- High surge current capability
- High speed switching
- Pb-free lead terminal; RoHS compliant
- Halogen-free molding compound

Applications

- Switch mode power supply
- Uninterruptible power supply
- PV Power Conditioner

Internal circuit chart



Maximum Ratings and Characteristics

Absolute Maximum Ratings at $T_{vj} = 25^{\circ}\text{C}$ (unless otherwise specified)

Parameter	Symbol	Value	Unit	Remarks
Repetitive peak reverse voltage	V_{RRM}	650	V	
Continuous forward current	I_F	10	A	$T_c < 115^{\circ}\text{C}$, $D = 1$
Surge non-repetitive forward current (half sine wave)	I_{FSM}	82	A	$T_c = 25^{\circ}\text{C}$, $t_b = 10\text{ ms}$
		61.5	A	$T_c = 150^{\circ}\text{C}$, $t_b = 10\text{ ms}$
$\rho^2 t$ value	$\int \rho^2 dt$	33.6	A^2s	$T_c = 25^{\circ}\text{C}$, $t_b = 10\text{ ms}$
		18.9	A^2s	$T_c = 150^{\circ}\text{C}$, $t_b = 10\text{ ms}$
Max. Power Dissipation	P_{tot}	54	W	$T_c = 25^{\circ}\text{C}$
Operating junction temperature	T_{vj}	175	$^{\circ}\text{C}$	
Storage temperature	T_{stg}	-55 ~ +175	$^{\circ}\text{C}$	
Isolation Voltage	V_{iso}	2	kVrms	$t = 60\text{sec}$, $f = 60\text{Hz}$

Electrical Characteristics at $T_{vj} = 25^{\circ}\text{C}$ (unless otherwise specified)

Static characteristics

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
DC blocking voltage	V_{DC}	$I_R = 1\text{ mA}$	650	-	-	V
Forward voltage	V_F	$I_F = 10\text{ A}$, $T_{vj} = 25^{\circ}\text{C}$	1.10	1.30	1.50	V
		$I_F = 10\text{ A}$, $T_{vj} = 150^{\circ}\text{C}$	-	1.48	1.99	V
Reverse current	I_R	$V_R = 650\text{ V}$, $T_{vj} = 25^{\circ}\text{C}$	-	0.4	50	μA
		$V_R = 650\text{ V}$, $T_{vj} = 150^{\circ}\text{C}$	-	2	200	μA

Dynamic characteristics

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Total Capacitive Charge	Q_C	$V_R = 400\text{ V}$, $I_F = 10\text{ A}$, $-di/dt = 200\text{ A}/\mu\text{s}$, $T_{vj} = 150^{\circ}\text{C}$	-	9.5	-	nC
Total Capacitance	C	$V_R = 400\text{ V}$, $f = 1\text{ MHz}$	-	44	-	pF

Thermal Resistance

Parameter	Symbol	Min.	Typ.	Max.	Unit
Thermal Resistance, Junction –Ambient	$R_{th(j-a)}$	-	-	58	$^{\circ}\text{C}/\text{W}$
Thermal Resistance, Junction –Case	$R_{th(j-c)}$	-	-	2.80	$^{\circ}\text{C}/\text{W}$

Figure 1. Allowable power dissipation

$$P_{tot} = f(T_c)$$

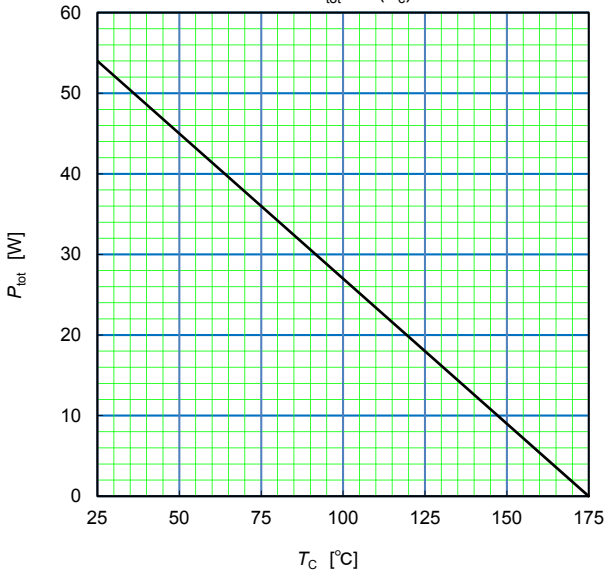


Figure 2. Peak forward current

$$I_F = f(T_c) : T_{vj} \leq 175 \text{ }^\circ\text{C}, R_{th(j-c)} \text{ max.}$$

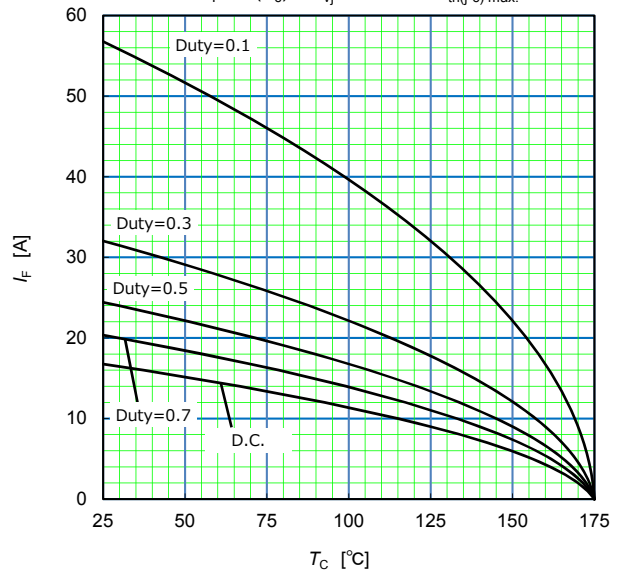


Figure 3. Typical forward characteristics

$$I_F = f(V_F)$$

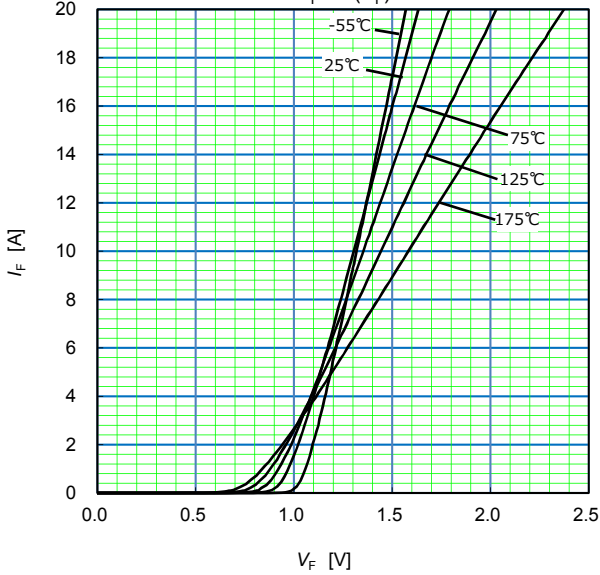


Figure 4. Typical forward characteristics

$$I_F = f(V_F)$$

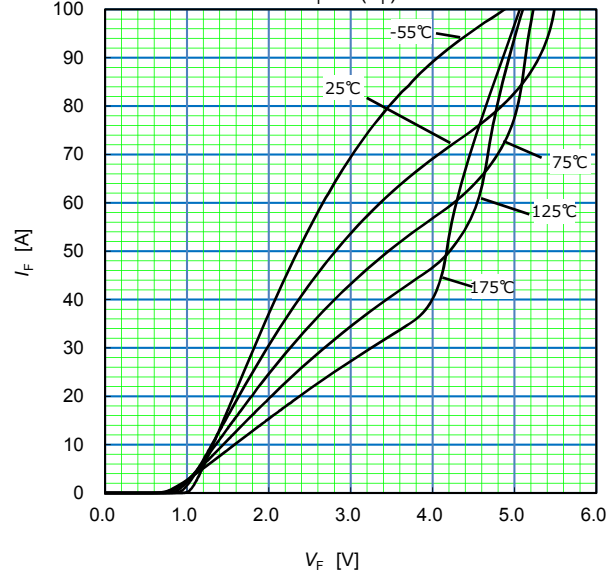


Figure 5. Typical reverse characteristics

$$I_R = f(V_R)$$

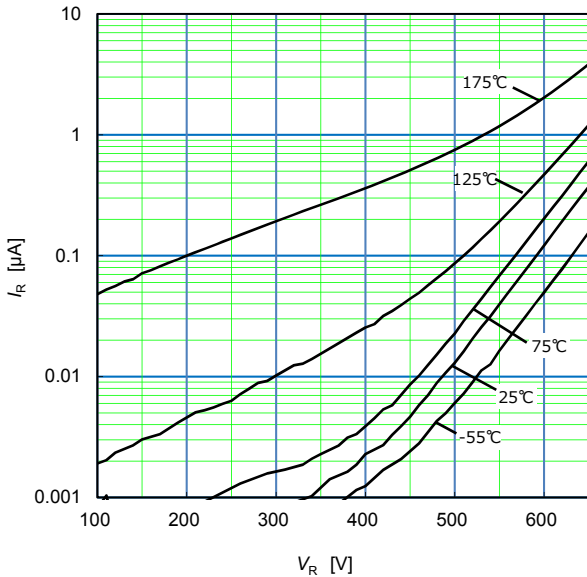


Figure 6. Typical capacitance

$$C = f(V_R) : f = 1 \text{ MHz}$$

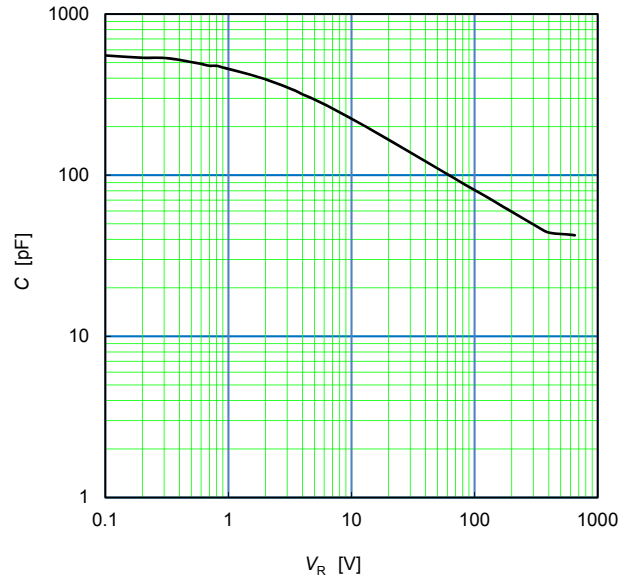


Figure 7. Typical capacitance stored energy

$$E_C = f(V_R)$$

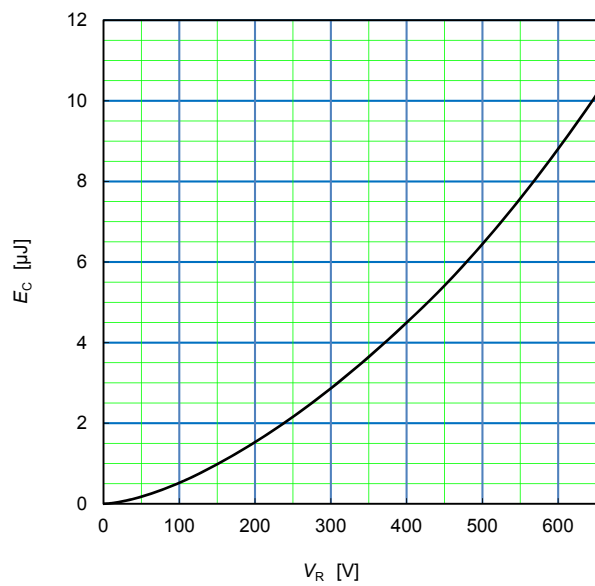
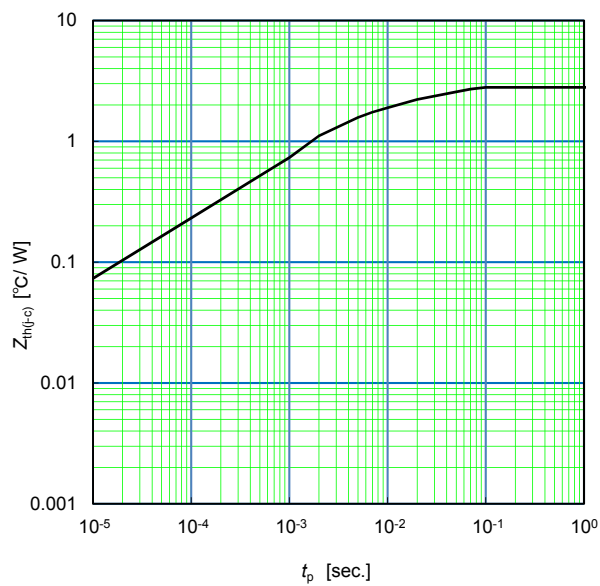
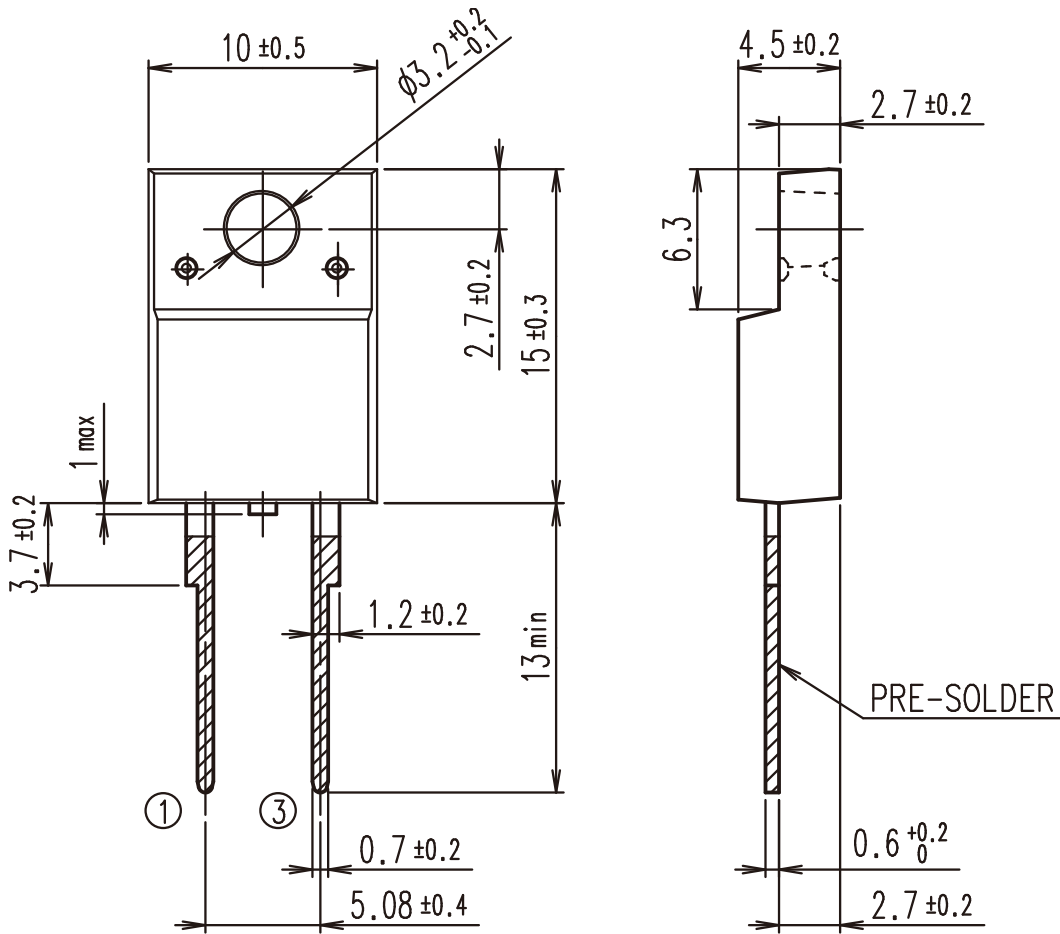


Figure 8. Transient Thermal Impedance

$$Z_{th(j-c)} = f(t) : D = 0$$



■ Outview: TO-220F-2 Package

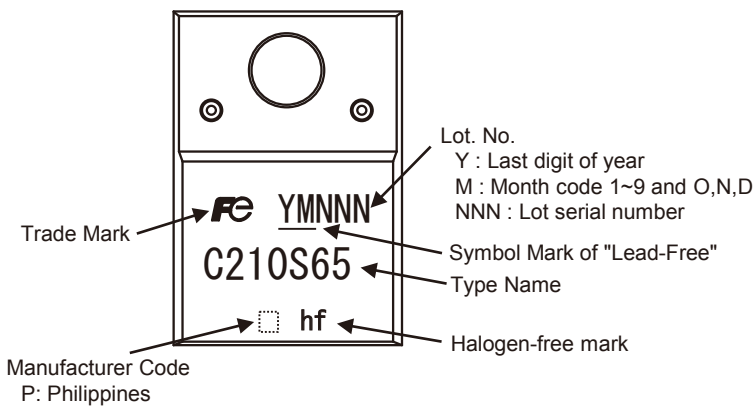


CONNECTION

- ① Cathode
- ③ Anode

DIMENSIONS ARE IN MILLIMETERS

■ Marking



* The font (font type, size) and the trademark-size might be actually different.

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