Fuji Electric

Innovating Energy Technology

FMP60N105S2FDHF

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FUJI POWER MOSFET

Super J MOS® S2 series

N-Channel enhancement mode power MOSFET

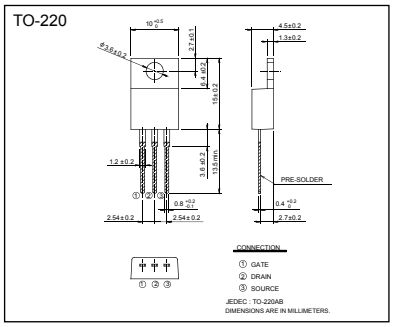
Features

Pb-free lead terminal
RoHS compliant
uses Halogen-free molding compound

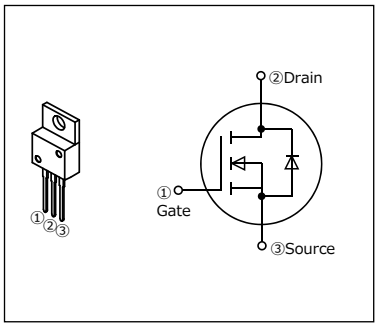
Applications

For switching

Outline Drawings [mm]



Equivalent circuit schematic



Absolute Maximum Ratings at T_{vj}=25°C (unless otherwise specified)

Parameter	Symbol	Characteristics	Unit	Remarks
Drain-Source Voltage	V_{DS}	600	V	
	V_{DSX}	600	V	V_{GS}=-30V
Continuous Drain Current	I_D	38.1	A	T_{vj}=25°C Note*1,2
		24.1	A	T_{vj}=100°C Note*1,2
Pulsed Drain Current	I_{DP}	117	A	Note *2
Gate-Source Voltage	V_{GS}	±30	V	
Non-Repetitive Maximum Avalanche Current	I_{AS}	4.4	A	Note *3
Non-Repetitive Maximum Avalanche Energy	E_{AS}	930	mJ	Note *4
Maximum Drain-Source dV/dt	dV_{DS}/dt	50	V/ns	V_{DS}≤ 600V
Continuous Diode Forward Current	I_{SD}	38.1	A	T_{vj}=25°C Note*1,2
		24.1	A	T_{vj}=100°C Note*1,2
Pulsed Diode Forward Current	I_{SDP}	117	A	Note *2
Peak Diode Recovery dV/dt	dV/dt	30	V/ns	Note *5
Peak Diode Recovery -di/dt	-di/dt	100	A/μs	Note *6
Maximum Power Dissipation	P_D	2.02	W	T_a=25°C
		210		T_{vj}=25°C
Operating and Storage Temperature range	T_{ch}	150	°C	
	T_{stg}	-55 to +150	°C	

Note *1 : Maximum duty cycle D=0.59
Note *2 : Limited by maximum channel temperature.
Note *3 : T_{ch}≤150°C, See Fig.1 and Fig.2
Note *4 : Starting T_{ch}=25°C, I_{AS}=2.7A, L=234mH, V_{DD}=60V, R_G=50Ω, See Fig.1 and Fig.2
E_{AS} limited by maximum channel temperature and avalanche current.
Note *5 : I_{SD}≤29.2A, -di/dt≤100A/μs, $V_{DS\ peak}$≤ 600V, T_{ch}≤150°C.
Note *6 : I_{SD}≤29.2A, dV/dt≤30V/ns, $V_{DS\ peak}$≤ 600V, T_{ch}≤150°C.

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MARCH 2017

Electrical Characteristics at T_{vj}=25°C (unless otherwise specified)

• Static Ratings

Parameter	Symbol	Conditions		Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage	BV_{DSS}	V_{GS}=0V I_D=250μA		600	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS}=V_{GS} I_D=4.4mA		3.0	4.0	5.0	V
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS}=600V V_{GS}=0V	T_{ch}=25°C	-	-	25	μA
		V_{DS}=480V V_{GS}=0V	T_{ch}=125°C	-	44	-	
Gate-Source Leakage Current	I_{GSS}	V_{DS}=0V V_{GS}= ± 30V		-	10	100	nA
Drain-Source On-State Resistance	$R_{DS(on)}$	V_{GS}=10V I_D=14.6A		-	0.093	0.105	Ω
Gate resistance	R_G	f=1MHz, open drain		-	7.8	-	Ω

• Dynamic Ratings

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Forward Transconductance	g_{fs}	V_{DS}=25V I_D=14.6A	10	20	-	S
Input Capacitance	C_{iss}	V_{DS}=400V V_{GS}=0V f=250kHz	-	1540	-	pF
Output Capacitance	C_{oss}		-	55	-	
Reverse Transfer Capacitance	C_{rss}		-	7.4	-	
Effective output capacitance, energy related (Note *7)	$C_{o(er)}$	V_{DS}=0…400V V_{GS}=0V	-	126	-	
Effective output capacitance, time related (Note *8)	$C_{o(tr)}$	V_{DS}=0…400V V_{GS}=0V I_D=constant	-	511	-	
Turn-On Time	$t_{d(on)}$	V_{DD}=400V, V_{GS}=10V I_D=14.6A, R_G=15Ω See Fig.3 and Fig.4	-	29	-	ns
	t_r		-	108	-	
Turn-Off Time	$t_{d(off)}$		-	146	-	
	t_f		-	27	-	
Total Gate Charge	Q_G	V_{DD}=400V, V_{GS}=10V I_D=29.2A See Fig.5	-	75	-	nC
Gate-Source Charge	Q_{GS}		-	31	-	
Gate-Drain Charge	Q_{GD}		-	35	-	
Drain-Source crossover Charge	Q_{SW}		-	22	-	

Note *7 : $C_{o(er)}$ is a fixed capacitance that gives the same stored energy as C_{oss} while V_{DS} is rising from 0 to 400V.
Note *8 : $C_{o(tr)}$ is a fixed capacitance that gives the same charging times as C_{oss} while V_{DS} is rising from 0 to 400V.

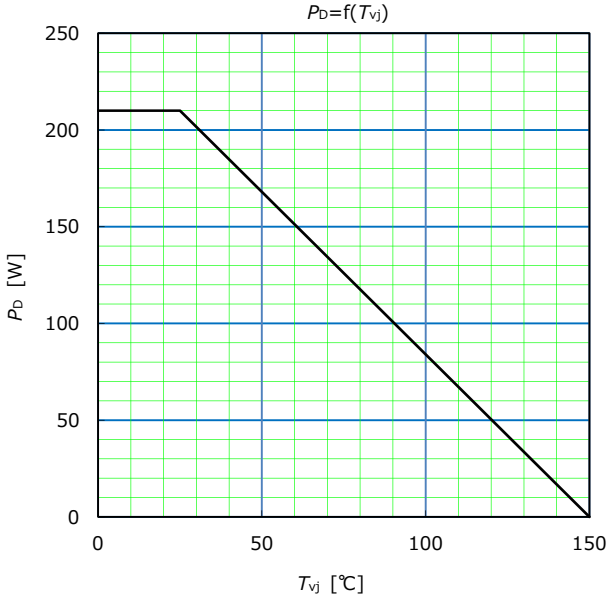
• Reverse Diode

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Diode Forward On-Voltage	V_{SD}	I_{SD}=29.2A, V_{GS}=0V T_{ch}=25°C	-	0.95	1.35	V
Reverse Recovery Time	t_{rr}	V_{DD}=400V, I_{SD}=29.2A -di/dt=100A/μs T_{ch}=25°C See Fig.6 and Fig.7	-	174	-	ns
Reverse Recovery Charge	Q_{rr}		-	1.4	-	μC
Peak Reverse Recovery Current	I_{rp}		-	14.9	-	A

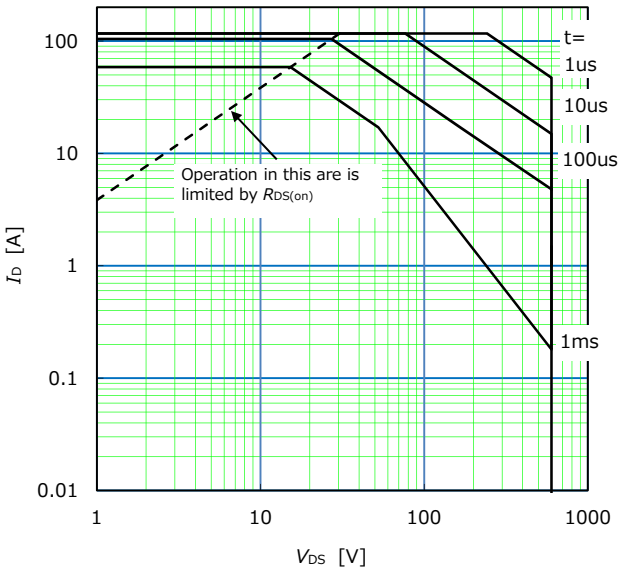
Thermal Resistance

Parameter	Symbol	Min.	Typ.	Max.	Unit
Channel to Case	$R_{th(ch-c)}$	-	-	0.60	°C/W
Channel to Ambient	$R_{th(ch-a)}$	-	-	62	°C/W

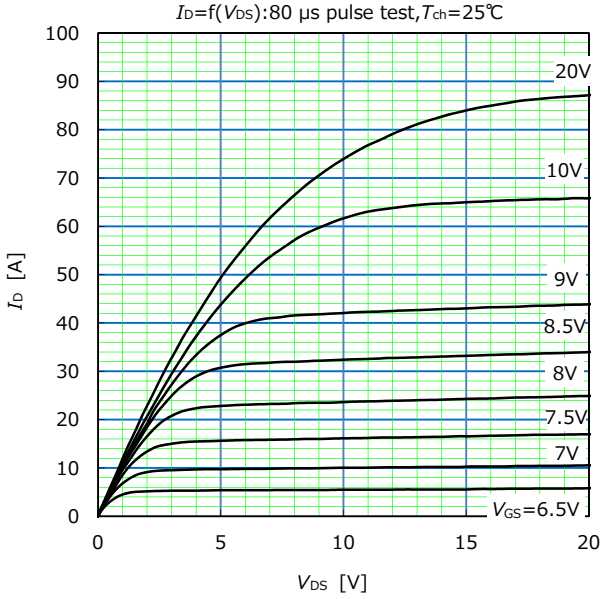
Allowable Power Dissipation
P_D=f(T_{vj})



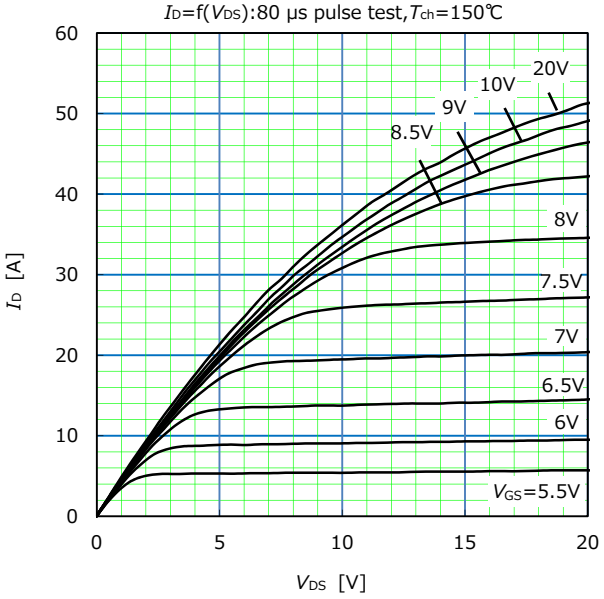
Safe Operating Area
I_D=f(V_{DS}):Duty=0(Single pulse),T_{vj}=25℃



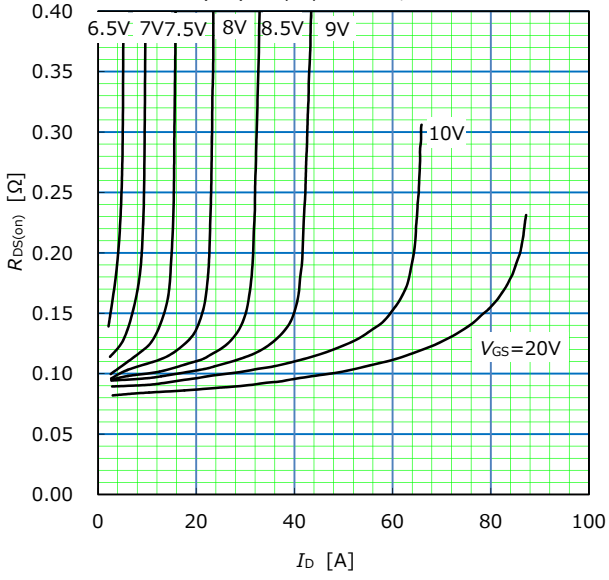
Typical Output Characteristics
I_D=f(V_{DS}):80 μs pulse test,T_{ch}=25℃



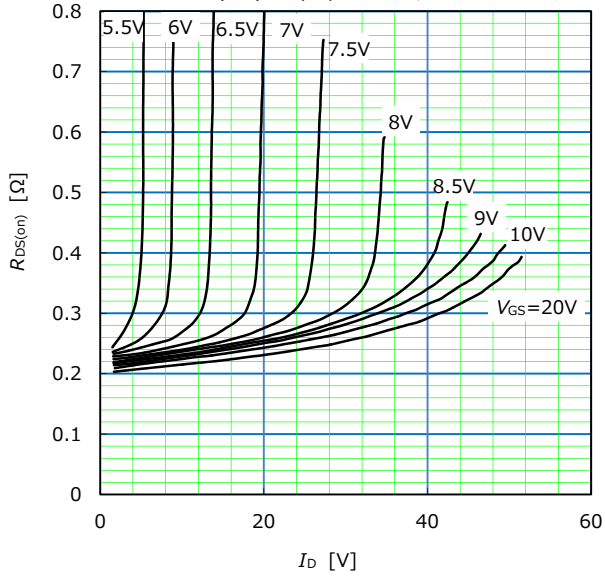
Typical Output Characteristics
I_D=f(V_{DS}):80 μs pulse test,T_{ch}=150℃



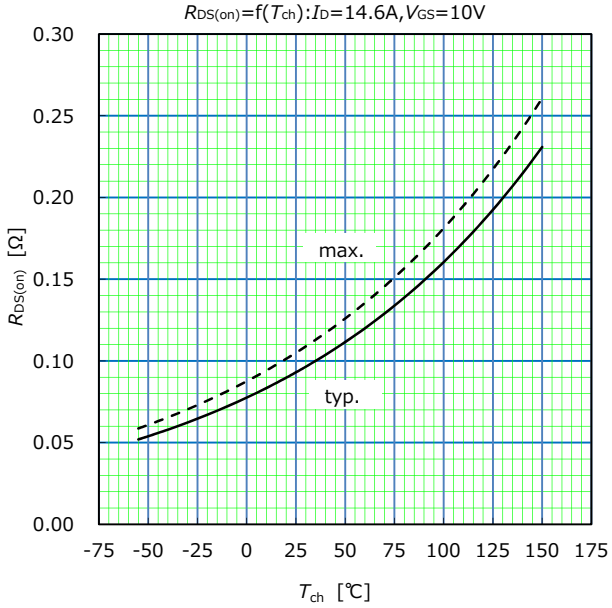
Typical Output Characteristics
I_D=f(V_{DS}):80 μs pulse test,T_{ch}=25℃



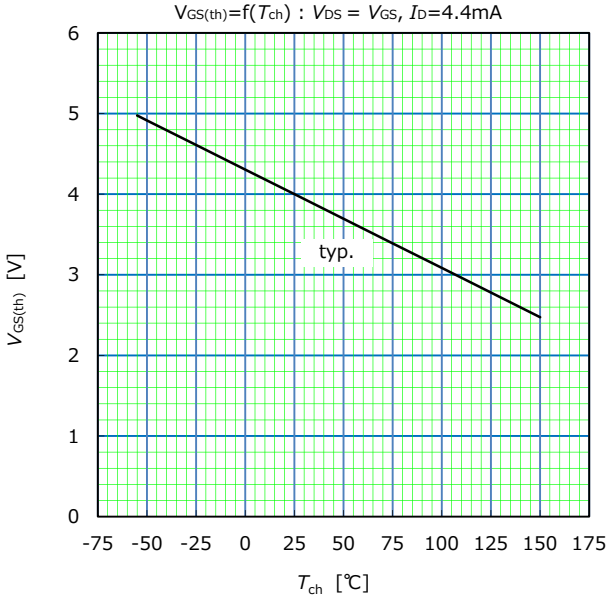
Typical Output Characteristics
I_D=f(V_{DS}):80 μs pulse test,T_{ch}=150℃



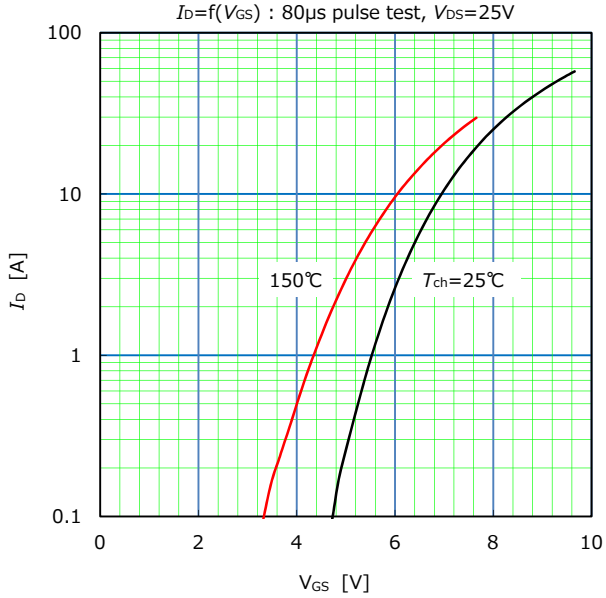
Drain-Source On-state Resistance
$R_{DS(on)}$=f(T_{ch}):I_D=14.6A,V_{GS}=10V



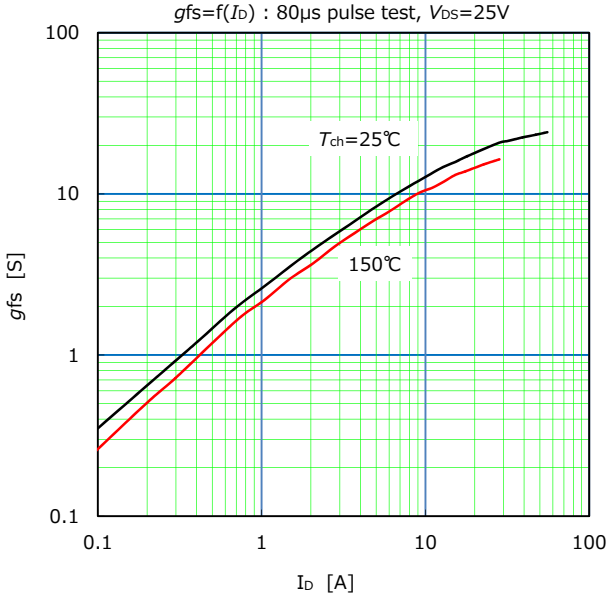
Gate Threshold Voltage vs. T_{ch}
$V_{GS(th)}$=f(T_{ch}) : V_{DS} = V_{GS}, I_D=4.4mA



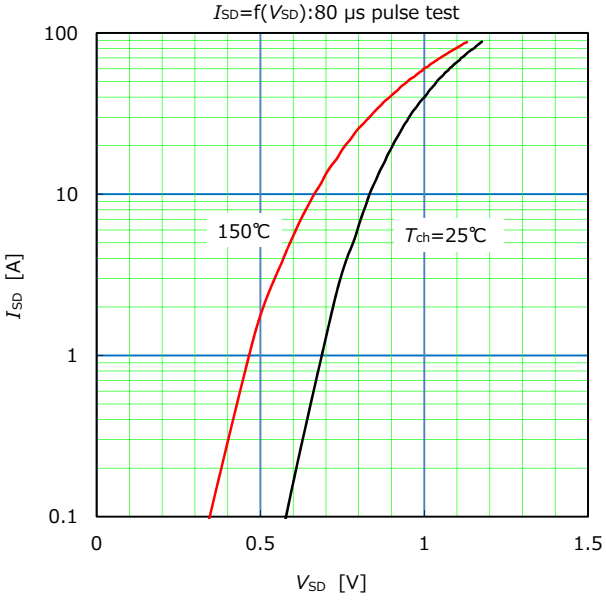
Typical Transfer Characteristic
I_D=f(V_{GS}) : 80μs pulse test, V_{DS}=25V



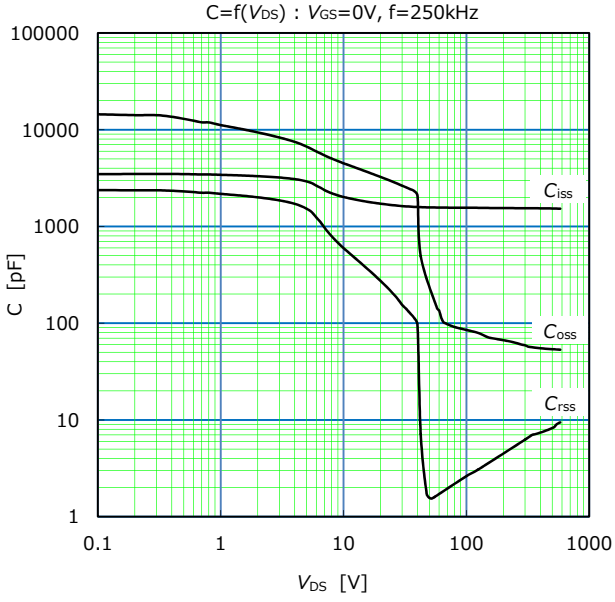
Typical Transconductance
g_{fs}=f(I_D) : 80μs pulse test, V_{DS}=25V



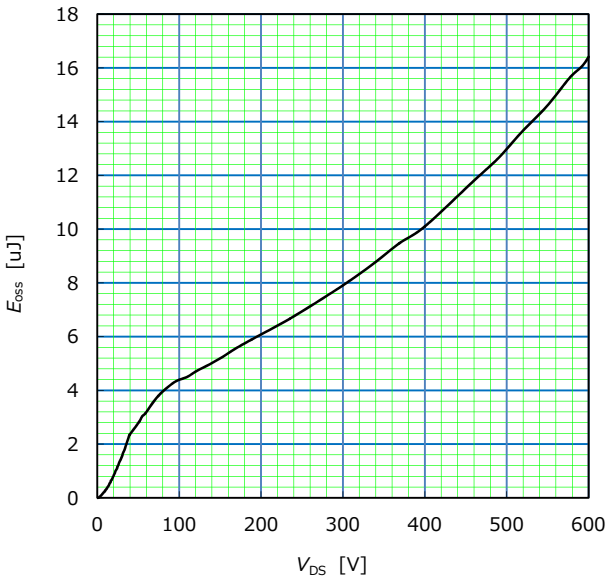
Typical Forward Characteristics of Reverse Diode
I_{SD}=f(V_{SD}):80 μs pulse test



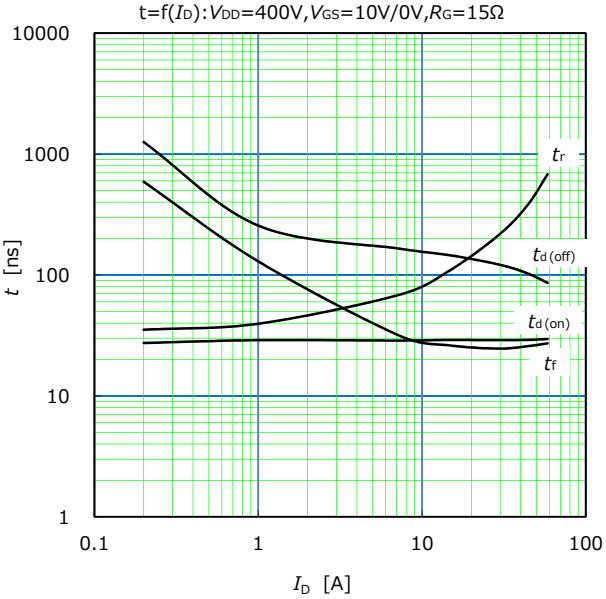
Typical Capacitance
C=f(V_{DS}) : V_{GS}=0V, f=250kHz



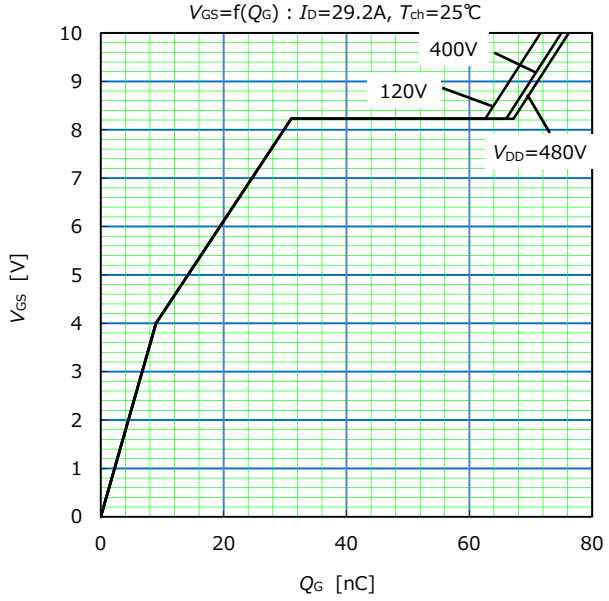
Typical C_{oss} stored energy



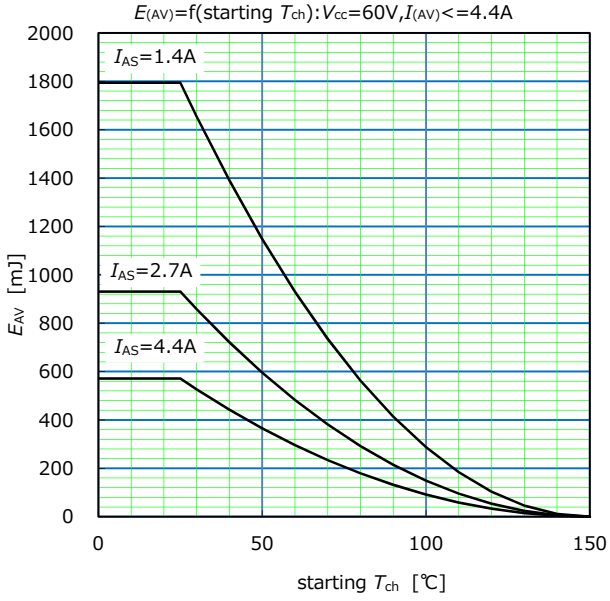
Typical Switching Characteristics vs. I_D T_{ch}=25℃
t=f(I_D):V_{DD}=400V,V_{GS}=10V/0V,R_G=15Ω



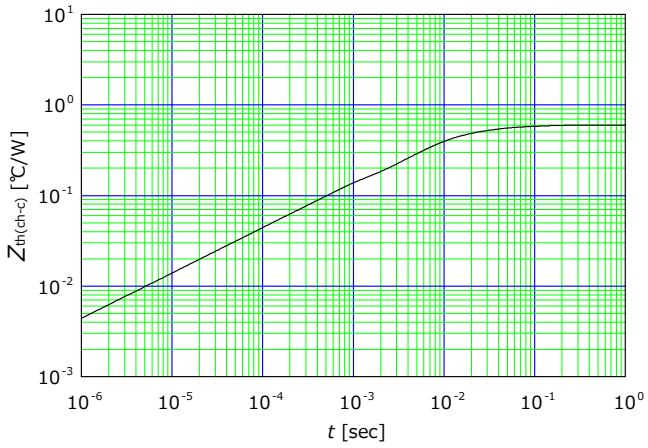
Typical Gate Charge Characteristics
V_{GS}=f(Q_G) : I_D=29.2A, T_{ch}=25℃



Maximum Avalanche Energy vs. starting T_{ch}
$E_{(AV)}$=f(starting T_{ch}):V_{cc}=60V,$I_{(AV)}$<=4.4A



Transient Thermal Impedance
$Z_{th(ch-c)}$=f(t):D=0



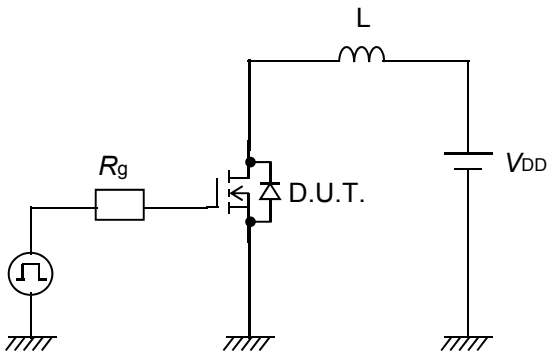


Fig.1 Avalanche Test circuit

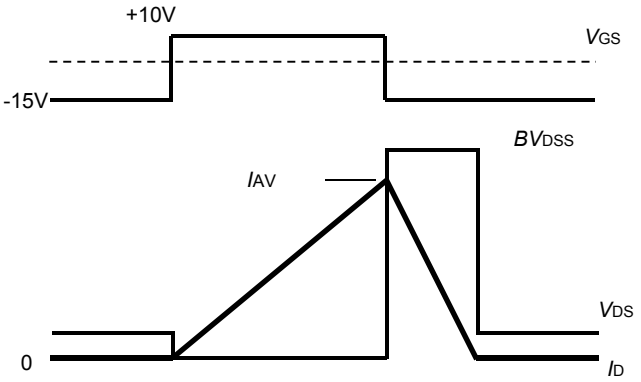


Fig.2 Operating waveforms of Avalanche Test

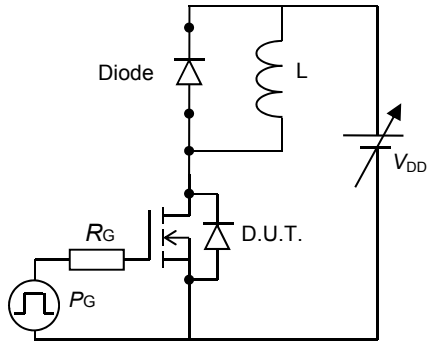


Fig.3 Switching Test circuit

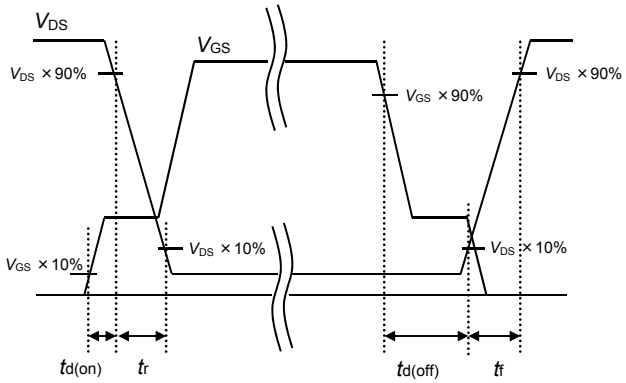


Fig.4 Operating waveform of Switching Test

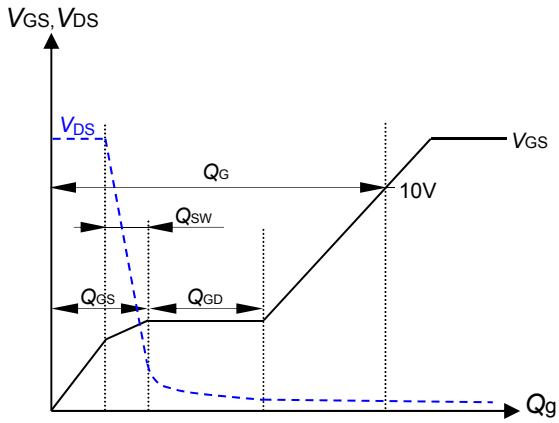


Fig.5 Operating waveform of Gate charge Test

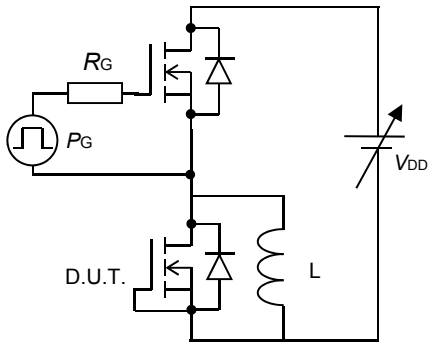


Fig.6 Reverse recovery Test circuit

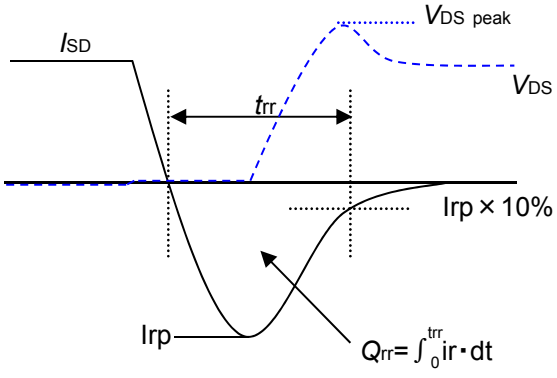
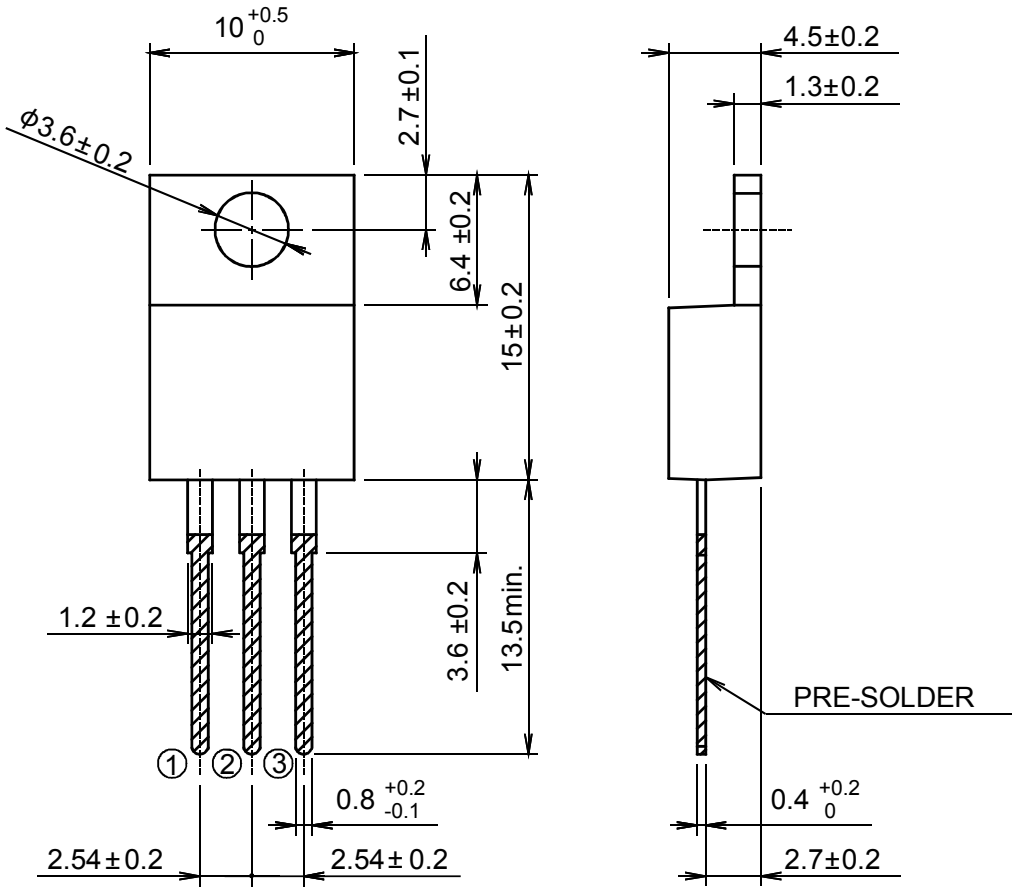
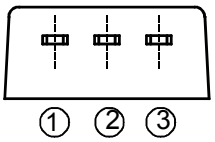


Fig.7 Operating waveform of Reverse recovery Test

Outview: TO-220 Package



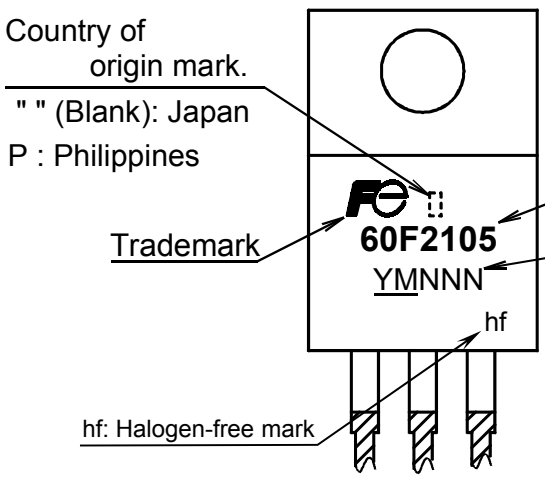


CONNECTION

① GATE
② DRAIN
③ SOURCE

JEDEC : TO-220AB
DIMENSIONS ARE IN MILLIMETERS.

Marking



Type name
Date code & Lot No.
Y: Last digit of year
M: Month code 1~9 and O,N,D
NNN: Lot. serial number
Under bar of date code
: means lead-free mark

* The font (font type,size) and the trademark-size might be actually different.

WARNING

1. This Catalog contains the product specifications, characteristics, data, materials, and structures as of March 2017.
The contents are subject to change without notice for specification changes or other reasons. When using a product listed in this Catalog, be sur to obtain the latest specifications.

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4. The products introduced in this Catalog are intended for use in the following electronic and electrical equipment which has normal reliability requirements.
- Computers
- OA equipment
- Communications equipment (terminal devices)
- Measurement equipment
- Machine tools
- Audiovisual equipment
- Electrical home appliances
- Personal equipment
- Industrial robots etc.

5. If you need to use a product in this Catalog for equipment requiring higher reliability than normal, such as for the equipment listed below, it is imperative to contact Fuji Electric Co., Ltd. to obtain prior approval. When using these products for such equipment, take adequate measures such as a backup system to prevent the equipment from malfunctioning even if a Fuji's product incorporated in the equipment becomes faulty.
- Transportation equipment (mounted on cars and ships)
- Trunk communications equipment
- Traffic-signal control equipment
- Gas leakage detectors with an auto-shut-off feature
- Emergency equipment for responding to disasters and anti-burglary devices
- Safety devices
- Medical equipment

6. Do not use products in this Catalog for the equipment requiring strict reliability such as the following and equivalents to strategic equipment (without limitation).
- Space equipment
- Aeronautic equipment
- Nuclear control equipment
- Submarine repeater equipment

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