Innovating Energy Technology

FMV60N094S2FDHF

http://www.fujielectric.com/products/semiconductor/

FUJI POWER MOSFET

Super J MOS® S2 series

N-Channel enhancement mode power MOSFET

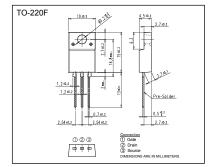
Features

Pb-free lead terminal RoHS compliant uses Halogen-free molding compound

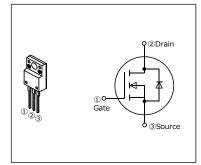
Applications

For switching

Outline Drawings [mm]



Equivalent circuit schematic



■ Absolute Maximum Ratings at T_{vi}=25°C (unless otherwise specified)

Parameter	Symbol	Characteristics	Unit	Remarks
Duain Sauras Valtana	V _{DS}	600	V	
Drain-Source Voltage	V _{DSX}	600	V	V _{GS} =-30V
Continuous Drain Current	ID	42.3	Α	<i>T</i> _{vj} =25°C Note*1,2
Continuous Drain Current		26.8	Α	T _{vj} =100°C Note*1,2
Pulsed Drain Current	I DP	131	Α	Note *2
Gate-Source Voltage	V _{GS}	±30	V	
Non-Repetitive Maximum Avalanche Current	I AS	4.9	Α	Note *3
Non-Repetitive Maximum Avalanche Energy	E AS	1018	mJ	Note *4
Maximum Drain-Source dV/dt	dV⊳s/dt	50	V/ns	V _{DS} ≤ 600V
Continuous	I sp	42.3	Α	T _{vj} =25°C Note*1,2
Diode Forward Current	I SD	26.8	Α	T _{vj} =100°C Note*1,2
Pulsed Diode Forward Current	I _{SDP}	131	Α	Note *2
Peak Diode Recovery dV/dt	dV/dt	30	V/ns	Note *5
Peak Diode Recovery -di/dt	-di/dt	100	A/µs	Note *6
Maximum Bower Discination	P□	2.16	W	T _a =25°C
Maximum Power Dissipation	7 D	85	VV	T _{vj} =25°C
Operating and Storage Temperature range	T _{ch}	150	°C	
	T _{stg}	-55 to +150	°C	
Isolation Voltage (TO-220F)	Viso	2	kVrms	t=60sec,f=60Hz

Note *1 : Maximum duty cycle D=0.65

Note *1: Maximum duty cycle D=0.55
Note *2: Limited by maximum channel temperature.
Note *3: T_{ch}≤150°C, See Fig.1 and Fig.2
Note *4: Starting T_{ch}=25°C, I_{AS}=3A, L=207mH, V_{DD}=60V, R_G=50Ω, See Fig.1 and Fig.2
E_{AS} limited by maximum channel temperature and avalanche current.
Note *5: I_{SD}≤32.8A, -di/dt≤100A/µs, V_{DS} peak≤ 600V, T_{ch}≤150°C.
Note *6: I_{SD}≤32.8A, dV/dt≤30V/ns, V_{DS} peak≤ 600V, T_{ch}≤150°C.

http://www.fujielectric.com/products/semiconductor/

■ Electrical Characteristics at *T*_{vj}=25°C (unless otherwise specified) • Static Ratings

Parameter	Symbol	Conditions		Min.	Тур.	Max.	Unit
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250μA		600	-	-	V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} I _D =4.9mA		3.0	4.0	5.0	V
Zero Gate Voltage Drain Current	l _{oss}	V _{DS} =600V V _{GS} =0V	T _{ch} =25°C	-	-	25	μΑ
		V _{DS} =480V V _{GS} =0V	<i>T</i> _{ch} =125°C	-	48	-	
Gate-Source Leakage Current	I GSS	V _{DS} =0V V _{GS} = ± 30V		-	10	100	nA
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =10V I _D =16.4A		-	0.084	0.094	Ω
Gate resistance	R G	f=1MHz, open drain		-	7.5	-	Ω

Dynamic Ratings

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Forward Transconductance	g fs	V _{DS} =25V I _D =16.4A	11	22	-	S
Input Capacitance	Ciss	V _{DS} =400V	-	1720	-	
Output Capacitance	Coss	V _{GS} =0V	-	60	-	
Reverse Transfer Capacitance	Crss	f=250kHz	-	7.9	-	
Effective output capacitance, energy related (Note *7)	C _{o(er)}	V _{DS} =0400V V _{GS} =0V	-	139	-	pF
Effective output capacitance, time related (Note *8)	C _{o(tr)}	V _{DS} =0400V V _{GS} =0V J _D =constant	-	569	-	
Town On Time	t _{d(on)}	V _{DD} =400V, V _{GS} =10V	-	32	-	
Turn-On Time	t r	I₀=16.4A,	-	119	-	
T 0# Tl	t _{d(off)}	R_{o} =15 Ω See Fig.3 and Fig.4	-	158	-	ns -
Turn-Off Time	t f		-	27	-	
Total Gate Charge	Q _G	V _{DD} =400V, V _{GS} =10V I _D =32.8A See Fig.5	-	83	-	
Gate-Source Charge	Q _{GS}		-	35	-	7
Gate-Drain Charge	Q _{GD}		-	39	-	nC
Drain-Source crossover Charge	Q sw		-	25	-	

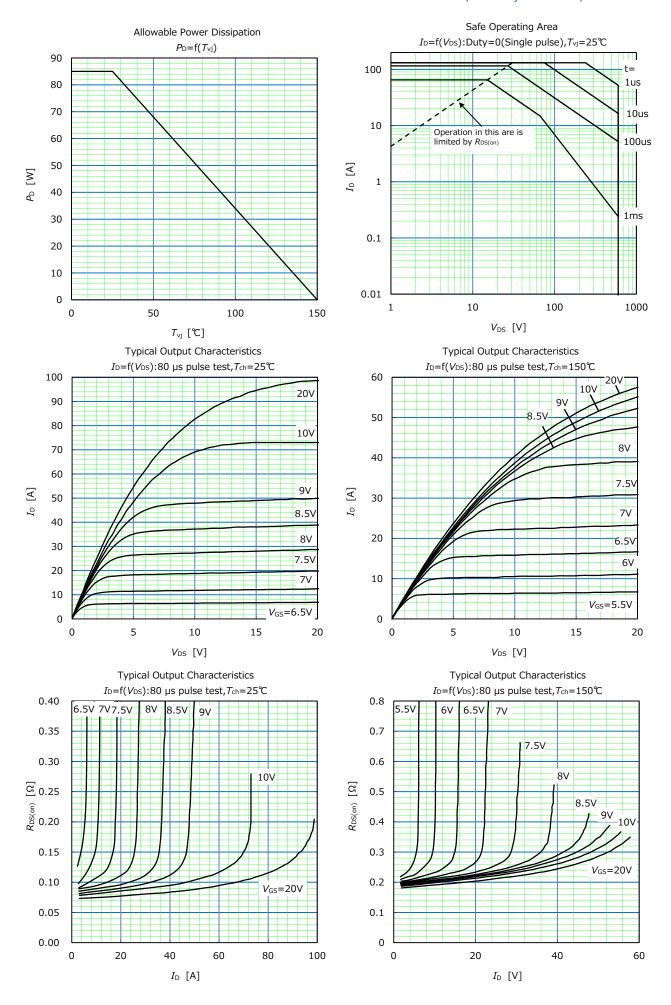
Note *7 : $C_{0(er)}$ is a fixed capacitance that gives the same stored energy as C_{oss} while V_{Ds} is rising from 0 to 400V. Note *8 : $C_{0(fr)}$ is a fixed capacitance that gives the same charging times as C_{oss} while V_{Ds} is rising from 0 to 400V.

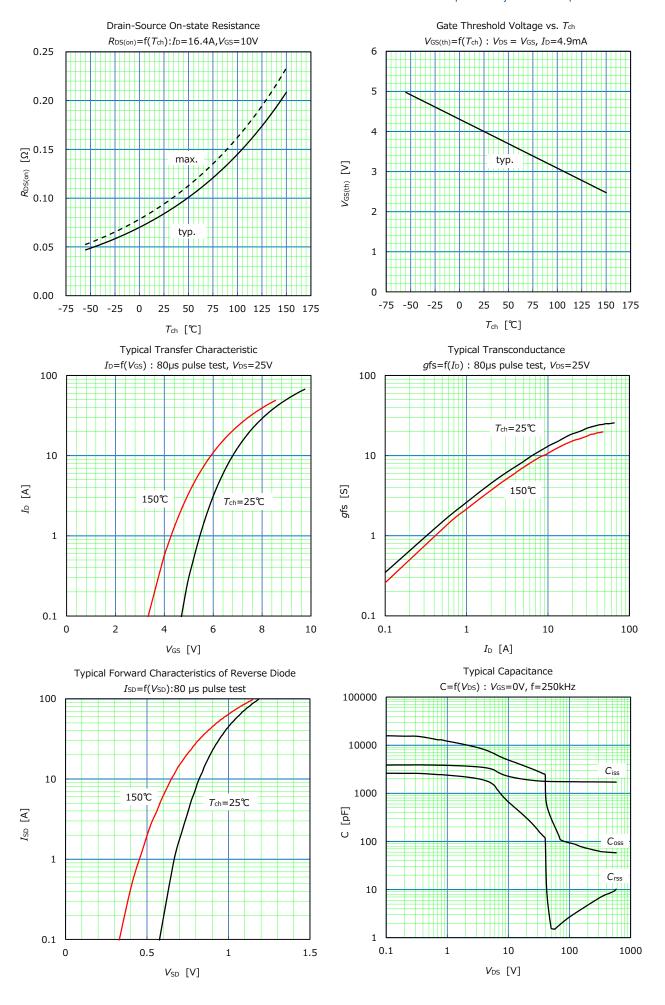
• Reverse Diode

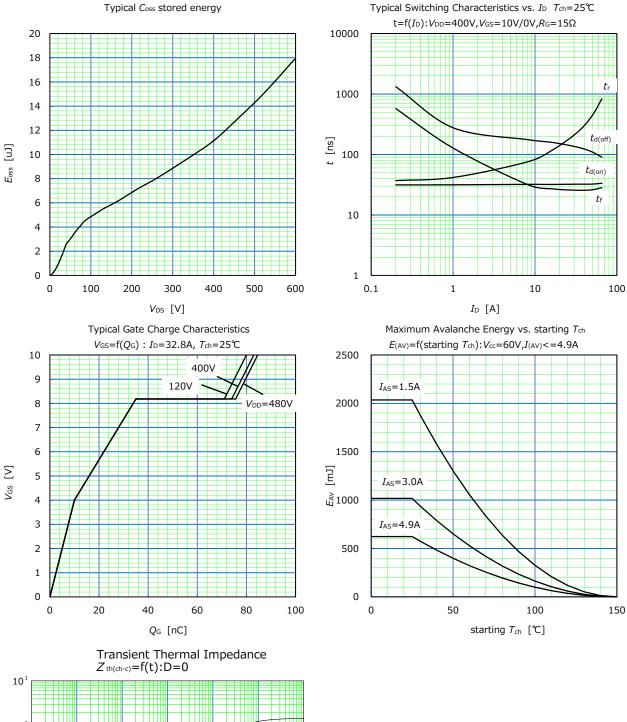
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Diode Forward On-Voltage	V _{SD}	I _{SD} =32.8A, V _{GS} =0V T _{ch} =25°C	-	0.95	1.35	V
Reverse Recovery Time	t rr	$V_{\rm DD}$ =400V, $I_{\rm SD}$ =32.8A -di/dt=100A/ μ s $T_{\rm ch}$ =25°C See Fig.6 and Fig.7	-	185	-	ns
Reverse Recovery Charge	Qrr		-	1.6	-	μC
Peak Reverse Recovery Current	I rp		-	15.8	-	А

■ Thermal Resistance

Parameter	Symbol	Min.	Тур.	Max.	Unit
Channel to Case	R _{th(ch-c)}	-	-	1.47	°C/W
Channel to Ambient	R _{th(ch-a)}	-	-	58	°C/W







 $Z_{\text{th(ch-c)}} = f(t):D=0$ 10^{0} 10^{0} 10^{-2} 10^{-3} 10^{-6} 10^{-5} 10^{-4} 10^{-3} 10^{-2} 10^{-1} 10^{0} t [sec]

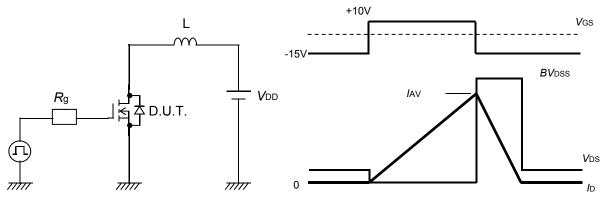


Fig.1 Avalanche Test circuit

Fig.2 Operating waveforms of Avalanche Test

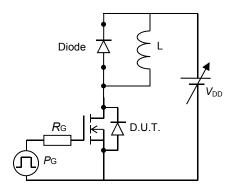


Fig.3 Switching Test circuit

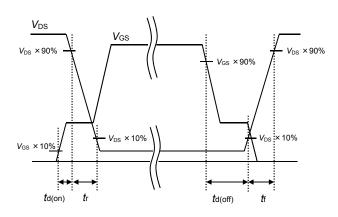


Fig.4 Operating waveform of Switching Test

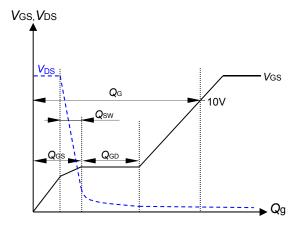


Fig.5 Operating waveform of Gate charge Test

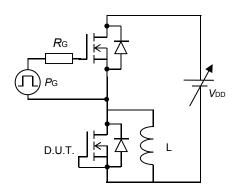


Fig.6 Reverse recovery Test circuit

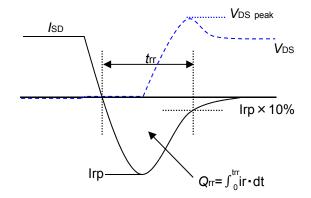
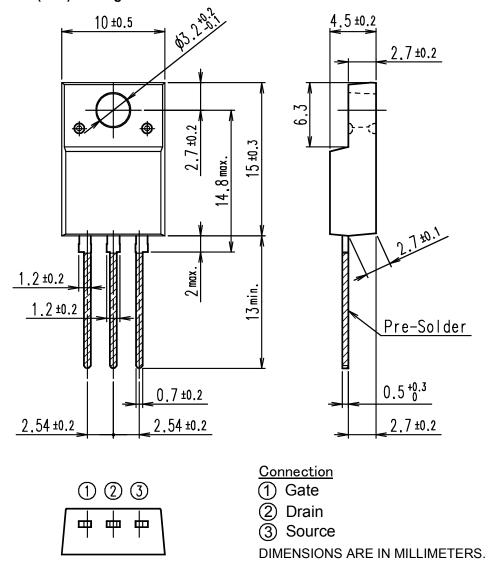
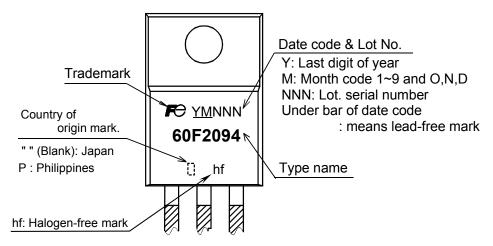


Fig.7 Operating waveform of Reverse recovery Test

Outview: TO-220F(SLS) Package



Marking



* The font (font type,size) and the trademark-size might be actually different.

http://www.fujielectric.com/products/semiconductor/

WARNING

- 1. This Catalog contains the product specifications, characteristics, data, materials, and structures as of March 2017. The contents are subject to change without notice for specification changes or other reasons. When using a product listed in this Catalog, be sur to obtain the latest specifications.
- 2. All applications described in this Catalog exemplify the use of Fuji's products for your reference only. No right or license, either express or implied, under any patent, copyright, trade secret or other intellectual property right owned by Fuji Electric Co., Ltd. is (or shall be deemed) granted. Fuji Electric Co., Ltd. makes no representation or warranty, whether express or implied, relating to the infringement or alleged infringement of other's intellectual property rights which may arise from the use of the applications described herein.
- 3. Although Fuji Electric Co., Ltd. is enhancing product quality and reliability, a small percentage of semiconductor products may become faulty. When using Fuji Electric semiconductor products in your equipment, you are requested to take adequate safety measures to prevent the equipment from causing a physical injury, fire, or other problem if any of the products become faulty. It is recommended to make your design failsafe, flame retardant, and free of malfunction.
- 4. The products introduced in this Catalog are intended for use in the following electronic and electrical equipment which has normal reliability requirements.
 - Computers
- OA equipment
- Communications equipment (terminal devices)
- · Measurement equipment

- · Machine tools
- Audiovisual equipment
- Electrical home appliances
- Personal equipment Industrial robots etc.
- 5. If you need to use a product in this Catalog for equipment requiring higher reliability than normal, such as for the equipment listed below, it is imperative to contact Fuji Electric Co., Ltd. to obtain prior approval. When using these products for such equipment, take adequate measures such as a backup system to prevent the equipment from malfunctioning even if a Fuji's product incorporated in the equipment becomes faulty.
- Transportation equipment (mounted on cars and ships)
- Traffic-signal control equipment
- Emergency equipment for responding to disasters and anti-burglary devices
- · Medical equipment

- Trunk communications equipment
- · Gas leakage detectors with an auto-shut-off feature
- · Safety devices
- 6. Do not use products in this Catalog for the equipment requiring strict reliability such as the following and equivalents to strategic equipment (without limitation).
 - Space equipment

- Aeronautic equipment
- · Nuclear control equipment

- Submarine repeater equipment
- 7. Copyright ©1996-2017 by Fuji Electric Co., Ltd. All rights reserved. No part of this Catalog may be reproduced in any form or by any means without the express permission of Fuji Electric Co., Ltd.
- 8. If you have any question about any portion in this Catalog, ask Fuji Electric Co., Ltd. or its sales agents before using the product. Neither Fuji Electric Co., Ltd. nor its agents shall be liable for any injury caused by any use of the products not in accordance with instructions set forth herein.