

2MBI900VXA-120P-50

IGBT Modules

IGBT MODULE (V series) 1200V / 900A / 2 in one package

Features

High speed switching Voltage drive Low Inductance module structure

F Fuji Electric

Applications

Inverter for Motor Drive AC and DC Servo Drive Amplifier Uninterruptible Power Supply Industrial machines, such as Welding machines

Maximum Ratings and Characteristics

Absolute Maximum Ratings (at Tc=25°C unless otherwise specified)

Items		Symbols	Conditions		Maximum ratings	Units
Collector-Emitter voltage		VCES			1200	V
Gate-Emitter voltage		Vges			±20	V
Collector current		Ic	Continuous	Tc=25°C	1200	
			Continuous	Tc=100°C	900	
		lc pulse	1ms		1800	А
		-lc			900	
		-lc pulse	1ms		1800	
Collector power dissipation		Pc	1 device		5100	W
Junction temperature		Tj			175	
Operating junction temperature (under switching conditions)		Tjop			150	°C
Case temperature		Tc			150	C
Storage temperature		Tstg			-40 ~ +150	
solation voltage	between terminal and copper base (*1)	-Viso	AC : 1min.		4000	VAC
solation voltage	between thermistor and others (*2)	Viso	AC . IIIIII.	AC . IIIIII.		VAC
	Mounting	-	M5		6.0	N m
Screw torque (*3)	Main Terminals		M8	M8		
	Sense Terminals		M4		2.1	

 Note *1: All terminals should be connected together during the test.

 Note *2: Two thermistor terminals should be connected together, other terminals should be connected together and shorted to base plate during the test.

 Note *3: Recommendable Value : Mounting
 3.0 ~ 6.0 Nm (M5)

 Recommendable Value : Sense Terminals 1.8 ~ 2.1 Nm (M4)

Electrical characteristics (at Ti= 25°C unless otherwise specified)

	Symbolo	Conditions	O and it is an		Characteristics		
ems	Symbols	Conditions		min.	typ.	max.	Units
Zero gate voltage collector current	ICES	V _{GE} = 0V, V _{CE} = 1200V		-	-	8.0	mA
Gate-Emitter leakage current	IGES	$V_{CE} = 0V, V_{GE} = \pm 20V$		-	-	1600	nA
Gate-Emitter threshold voltage	V _{GE (th)}	V _{CE} = 20V, I _C = 900mA		6.0	6.5	7.0	V
	V _{CE (sat)}		Tj=25°C	-	1.75	2.20	V
	(terminal)		Tj=125°C	-	2.10	-	
Collector Emitter esturation valtage	(*4)	V _{GE} = 15V	Tj=150°C	-	2.15	-	
Collector-Emitter saturation voltage	N/	Ic = 900A	Tj=25°C	-	1.65	2.10	
	V _{CE (sat)}		Tj=125°C	-	2.00	-	
	(chip)		Tj=150°C	-	2.05	-	
Internal gate resistance	Rg(int)	-		-	1.19	-	Ω
Input capacitance	Cies	V _{CE} = 10V, V _{GE} = 0V, f = 1N	1Hz	-	83	-	nF
Input capacitance	ton	V _{cc} = 600V		-	1.00	-	
	tr	Ic = 900A	-	0.40	-	µsec	
	tr (i)	$V_{GE} = \pm 15V$	-	0.15	-		
Turn-off time	toff	R _g = 1.6Ω		-	1.20	-	
	tf	Ls = 70nH		-	0.15	-	
	VF		Tj=25°C	-	1.90	2.35	
	(terminal) (*4)	V _{GE} = 0V _F = 900A	Tj=125°C	-	2.05	-	- V
Famuend an evoltance			Tj=150°C	-	2.00	-	
Forward on voltage			Tj=25°C	-	1.80	2.25	
	V _F		Tj=125°C	-	1.95	-	
	(chip)		Tj=150°C	-	1.90	-	
Reverse recovery time	trr	IF = 900A		-	0.20	-	µseo
Desistance	Б	T=25°C		-	5000	-	
Resistance B value	R	T=100°C		465	495	520	Ω
B value	В	T=25/50°C		3305	3375	3450	K

Note *4: Please refer to page 6, there is definition of on-state voltage at terminal.

Thermal resistance characteristics

Items	Symbols	Conditions	Characteristics			Units
items	Symbols	Conditions	min.	typ.	max.	Units
Thermal registeres (tdevice)	Dth/i a)	Inverter IGBT	-	-	0.030	
Thermal resistance (1device) Rth(j-c)		Inverter FWD	-	-	0.054	°C/W
Contact thermal resistance (1device) (*5)	Rth(c-f)	with Thermal Compound	-	0.00625	-	1

Note *5: This is the value which is defined mounting on the additional cooling fin with thermal compound.



Characteristics (Representative)





[INVERTER] Collector-Emitter voltage vs. Gate-Emitter voltage (typ.) Tj= 25°C / chip



[INVERTER] Dynamic Gate Charge (typ.) Vcc=600V, Ic=900A, Tj= 25°C





[INVERTER] Switching time vs. Gate resistance (typ.) Vcc=600V, Ic=900A, VGE=±15V, Tj=125°C, 150°C



[INVERTER] Switching loss vs. Gate resistance (typ.) Vcc=600V, Ic=900A, VGE=±15V, Tj=125°C, 150°C



[INVERTER] Switching time vs. Collector current (typ.) Vcc=600V, Vge=±15V, Rg=1.6Ω, Tj=125°C, 150°C



Collector current: Ic [A]

[INVERTER] Switching loss vs. Collector current (typ.) Vcc=600V, Vge=±15V, Rg=1.6Ω, Tj=125°C, 150°C



[INVERTER]

Reverse bias safe operating area (max.) +VGE=15V, -VGE=15V, RG=1.6Ω, Tj=150°C



Collector current: Ic [A]



[INVERTER] Reverse Recovery Characteristics (typ.) Vcc=600V, Vge=±15V, Rg=1.6Ω, Tj=125°C, 150°C



[THERMISTOR]

Temperature characteristic (typ.)





[INVERTER]

Transient Thermal Resistance (max.)



FWD safe operating area (max.)





Outline Drawings (Unit: mm)

Equivalent Circuit





Definition of on-state voltage at terminal and switching characteristics



Fuji defined VCE value of terminal by using Sense C1 and Sense C2E1 for Upper arm and Sense C2E1 and Sense E2 for Lower arm .

Switching characteristics of VCE also is defined between Sense C1 and Sense C2E1 for Upper arm and Sense C2E1 and Sense E2 for Lower arm .

Please use these terminals whenever measure spike voltage and on-state voltage .

WARNING

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