

IGBT Modules

IGBT Power Module (V series) 1200V/600A/IGBT, 4-in-1 package

■ Features

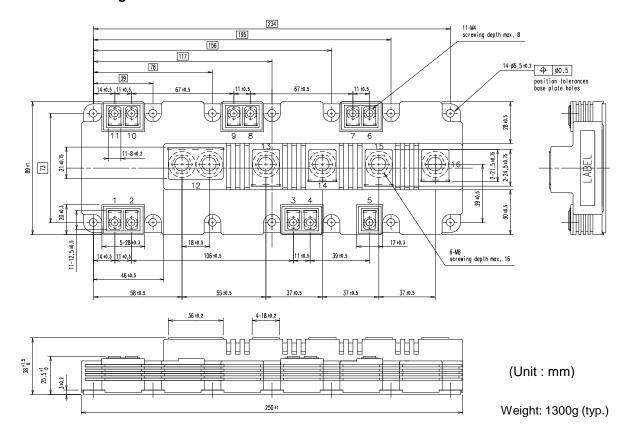
Higher efficiency Standard I-type 3-level circuit Low inductance module structure

■ Applications

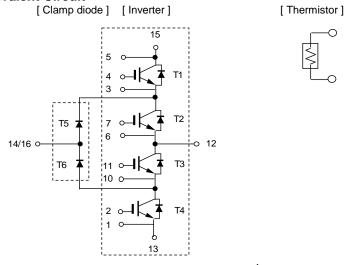
Inverter for motor drive Uninterruptible power supply Power conditioner for PV, Wind turbine

■ Outline drawing





■ Equivalent Circuit





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■ Absolute Maximum Ratings (at T_c = 25°C unless otherwise specified)

		Item		Symbol	Condi	tion	Maximum Rating	Unit
Collector-Emitter voltage		V _{CES}			1200	V		
	Gate-Emitter voltage		V_{GES}			±20	V	
			IGBT	I _C	Continuous	T_c =25°C	800	
_						<i>T</i> _c =80°C	600	Α
) H				I _C pulse	1ms		1200	
Inverter	Collec	Collector current	FWD	-1 _C			600	
			I WD	-/ _C pulse	1ms		1200	
	Collec	tor power dissipation	ı.	Pc	1 device		2460	W
ge	Repeti	tive peak reverse voltage		V _R			1200	
p diode			FWD	I _F			600	Α
Clamp	Forwa	rd current		I _F pulse	1ms		1200	
Jur	nction t	emperature	ı.	$T_{\rm vj}$			175	
	Operating junction temperature (under switching conditions)			T _{vjop}			150	°C
Case temperature		T _c			125			
Sto	Storage temperature			$T_{\rm stg}$			-40 ~ 125	
	Isolation between terminal and copper base between thermistor and others (*2)		V _{iso}	AC: 1min.		4000	Vrms	
	Screw Mounting		_	M5		6.0		
	rque	Main terminals		-	M8		10.0	N m
(*3	•	Sense terminals		-	M4		2.1	

^(*1) All terminals should be connected together during the test.

(*3) Recommended value : Mounting 3.0 ~ 6.0 Nm (M5) Recommended value : Main Terminals 8.0 ~ 10.0 Nm (M8) Recommended value : Sense Terminals 1.8 ~ 2.1 Nm (M4)

^(*2) Two thermistor terminals should be connected together, other terminals should be connected together and shorted to base plate during the test.

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■ Electrical characteristics (at T_{vj} = 25°C unless otherwise specified)

Item		Symbol	Condition		Ch	aracterist	ics	Units		
			Syllibol			min.	typ.	max.	Ullits	
	Zero gate Collector	•	ICES	$V_{\text{GE}} = 0V$ $V_{\text{CE}} = 1200V$		-	-	3.0	mA	
	Gate-Emitter leakage current		I _{GES}	$V_{CE} = 0V$ $V_{GE} = \pm 20V$		-	-	600	nA	
	Gate-Emit threshold		$V_{GE(th)}$	$V_{\text{CE}} = 20V$ $I_{\text{C}} = 600\text{mA}$		6.0	6.5	7.0	V	
			17	$V_{GE} = 15V$	T _{vi} =25°C	-	1.85	2.10		
			V _{CE(sat)}	$I_{\rm C} = 600A$	T _{vj} =125°C	-	2.20	-	1	
	Collector-	Emitter	(chip)		T _{vj} =150°C	-	2.25	-	V	
	saturation	voltage	V	$V_{\rm GE}$ = 15V	T _{vj} =25°C	-	1.95	-	V	
			V _{CE(sat)}	$I_{\rm C} = 600A$	T _{vj} =125°C	-	2.30	-		
			(terminal)		T _{vj} =150°C	-	2.35	-		
_	Internal ga	ate	$R_{G(int)}$	-		-	1.25	-	Ω	
Inverter	Input capa	acitance	C _{ies}	V_{CE} =10V, V_{GE} =	0V, <i>f</i> =1MHz	-	50	-	nF	
×			$t_{\sf on}$	SW mode: A, E	3	-	0.45	-		
=	Turn-on ti	me	t_{r}	$V_{\rm CC} = 750 \rm{V}$		-	0.15	-	μs	
			$t_{\rm r(i)}$	$I_{\rm C} = 600A$		-	0.05	-		
	Turn off ti		toff	V_{GE} = ±15V		-	0.70	-		
	Turn-off ti	me	t_{f}	$R_{\rm G} = +1.2/-0.5$	L_{s} =60nH -	0.10	-	1		
	Forward on voltage		V_{F}	$I_{\rm F} = 600A$	T _{vi} =25°C	-	1.70	1.95		
					T _{vi} =125°C	-	1.85	-		
			(chip)		T _{vi} =150°C	-	1.80	-	V	
	Forward o	n voitage	V _F	$I_{\rm F} = 600A$	T _{vj} =25°C	-	1.75	-	7 V	
					T _{vj} =125°C	-	1.90	-		
			(terminal)		T _{vi} =150°C	-	1.85	-		
	Reverse recovery time		t _{rr}	Switching mode $V_{CC} = 750V I_F$ $V_{GE} = \pm 15V R_O$	= 600A	-	0.15	-	μs	
	Reverse le	akage current	I_{R}	V _R =1200V		-	-	3.0	mA	
		1/	V _F	$I_{F} = 600A$	T _{vj} =25°C	-	1.70	1.95		
e					T _{vi} =125°C	-	1.85	-		
Clamp diode			(chip)		T _{vi} =150°C	-	1.80	-		
b d	Forward o	n voitage	V_{F}	$I_{\rm F} = 600A$	T _{vi} =25°C	-	1.80	-	V	
E E	<u> </u>				T _{vi} =125°C	-	1.95	-	7	
ပြ			(terminal)		T _{vi} =150°C	-	1.90	-		
	Reverse recovery time		t _{rr}	Switching mode $V_{CC} = 750V I_F$ $V_{GE} = \pm 15V R_O$: A = 600A	-	0.15	-	μs	
		Resistance	R	T=25°C		-	5000	-	Ω	
Th	ermistor	stor	T=100°C		465	495	520			
		B Value	В	T=25/50°C		3305	3375	3450	K	

■ Thermal resistance characteristics

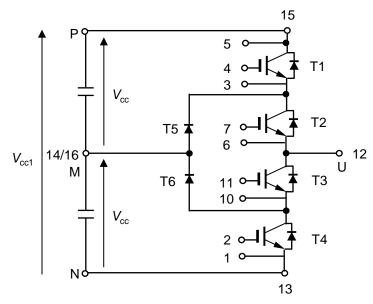
Item	Symbol	Condition	Characteristics			Units
item	Syllibol	Condition	min.	typ.	max.	Ullits
Thermal resistance		Inverter-IGBT	-	-	0.061	
	$R_{th(j-c)}$	Inverter-FWD	-	-	0.092	
(1device)		Clamp diode-FWD	-	-	0.092	0000
Contact thermal						°C/W
resistance	$R_{\text{th(c-f)}}$	with Thermal Compound	-	0.0167	-	
(1device) (*1)	, ,					

^(*1) This is the value which is defined mounting on the additional cooling fin with thermal compound. (thermal conductivity = 1W/m · k)



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■ Definitions of on-stage voltage at terminal and switching characteristics



 $V_{\rm CE(sat)}/V_{\rm F}$ values are measured between sense 5 and sense 3 for T1 arm, sense 3 and sense 6 for T2 arm, sense 6 and sense 10 for T3 arm, sense 10 and sense 1 for T4 arm, sense 14 and sense 3 for T5 arm and sense 10 and sense 14 for T6 arm.

SW mode	Load L	T1	T2	T3	T4
Λ	M-U	SW	ON	OFF	OFF
Α	M-U	OFF	OFF	ON	SW
В	U-N	OFF	SW	OFF	OFF
В	P-U	OFF	OFF	SW	OFF

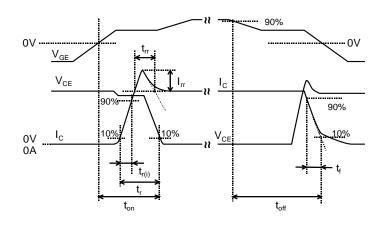
SW: Connect to drive circuit and input gate signal

ON: Bias voltage of gate +15V

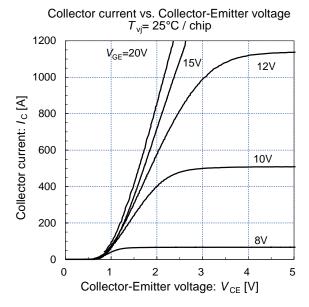
OFF: Reverse bias voltage of gate -15V

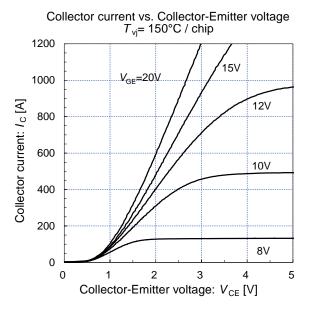
 $V_{cc1}=2\times V_{cc}$

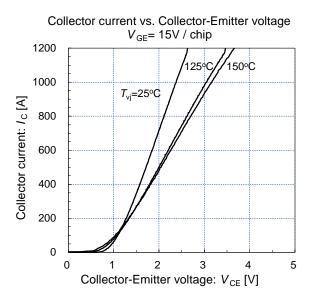
■ Definitions of switching time

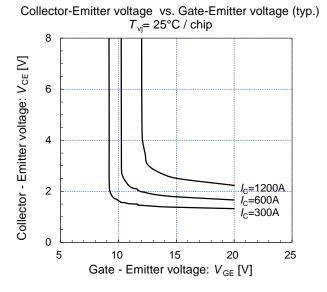


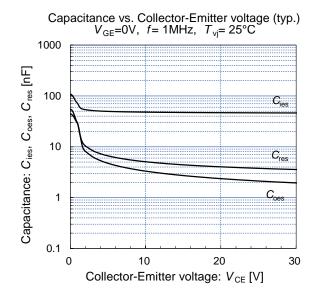
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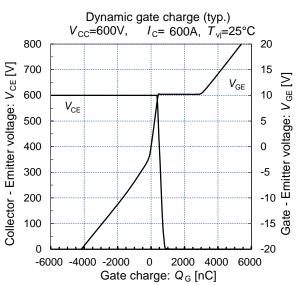




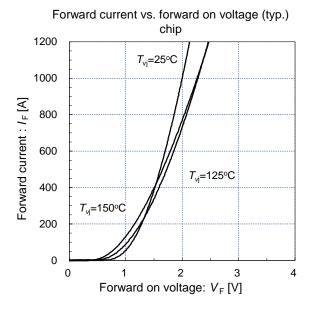


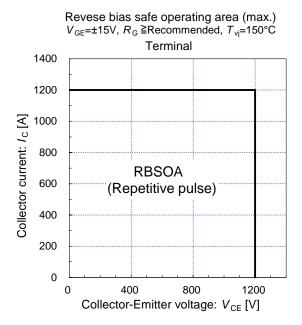


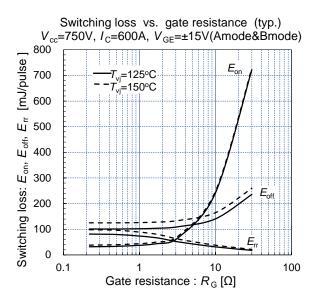


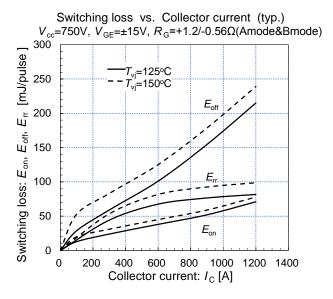


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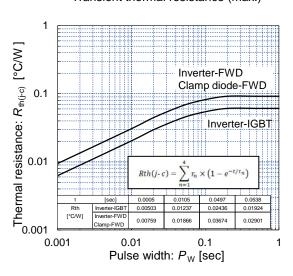


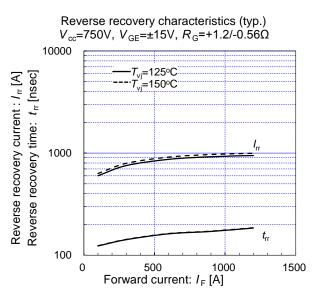






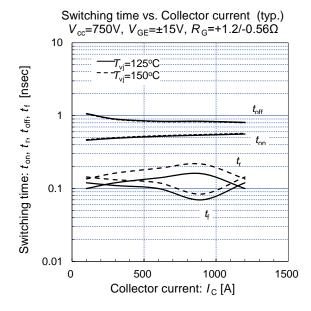
Transient thermal resistance (max.)

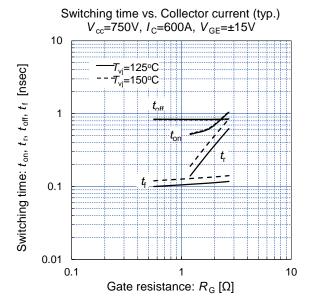


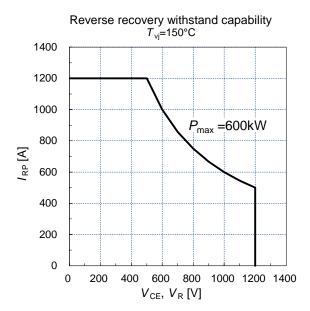




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 $V_{\rm CE}, V_{\rm R}$ are measured between :

sense 5 and 6 for T1&T2 sense 6 and 1 for T3&T4 sense 14 and 3 for T5 sense 10 and 14 for T6

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