

Innovating Energy Technology

http://www.fujielectric.com/products/semiconductor/ **FUJI POWER MOSFET**

Super J MOS[®] S2 series

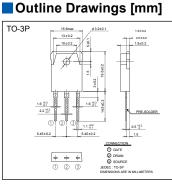
N-Channel enhancement mode power MOSFET

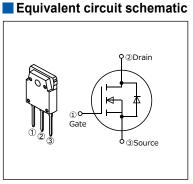
Features

Pb-free lead terminal **RoHS** compliant uses Halogen-free molding compound

Applications

For switching





Absolute Maximum Ratings at T_{vi}=25°C (unless otherwise specified)

Parameter	Symbol	Characteristics	Unit	Remarks
Duraine Courses Vialtering	VDS	600	V	
Drain-Source Voltage	VDSX	600	V	V _{GS} =-30V
Continuous Drain Current	· · · · · · · · · · · · · · · · · · ·	20	А	Tvj=25°C Note*1,2
Continuous Drain Current	I _D	12.6	А	T _{vj} =100°C Note*1,2
Pulsed Drain Current	I _{DP}	62	А	Note *2
Gate-Source Voltage	Vgs	±30	V	
Non-Repetitive Maximum Avalanche Current	las	2.3	А	Note *3
Non-Repetitive Maximum Avalanche Energy	Eas	559	mJ	Note *4
Maximum Drain-Source dV/dt	dV₀s/dt	50	V/ns	V _{DS} ≤ 600V
Continuous	,	20	А	Tvj=25°C Note*1,2
Diode Forward Current	Isd	12.6	А	T _{vj} =100°C Note*1,2
Pulsed Diode Forward Current	Isdp	62	А	Note *2
Peak Diode Recovery dV/dt	dV/dt	15	V/ns	Note *5
Peak Diode Recovery -di/dt	-di/dt	100	A/µs	Note *6
Maximum Dawar Disaination	P	2.5	14/	<i>T</i> _a =25°C
Maximum Power Dissipation	PD	94	W	<i>T</i> _{vj} =25°C
One verting and Stevens Temperature verses	Tch	150	°C	
Operating and Storage Temperature range	T _{stg}	-55 to +150	°C	

Note *1 : Maximum duty cycle D=0.6 Note *1 : Limited by maximum channel temperature. Note *2 : *T*_{ch}<150°C, See Fig.1 and Fig.2 Note *3 : Starting *T*_{ch}=25°C, *I*_{AS}=1.4A, L=559mH, *V*_{DD}=60V, *R*_G=50Ω, See Fig.1 and Fig.2 EAs limited by maximum channel temperature and avalanche current. Note *4 : *I*_{SD}≤15.5A, -di/dt≤100A/µs, *V*_{DS peak}≤ 600V, *T*_{ch}≤150°C. Note *5 : *I*_{SD}≤15.5A, dV/dt≤15V/ns, *V*_{DS peak}≤ 600V, *T*_{ch}≤150°C.

Electrical Characteristics at T_{vi}=25°C (unless otherwise specified) Static Ratings

Parameter	Symbol	Conditions		min.	typ.	max.	Unit
Drain-Source Breakdown Voltage	BV _{DSS}	V₅₅=0V /₀=250µA		600	-	-	V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} I₀=250µA		2.5	3.0	3.5	V
Zero Gate Voltage Drain Current	Ioss	V _{DS} =600V V _{GS} =0V	T _{ch} =25°C	-	-	25	μA
		V _{DS} =480V V _{GS} =0V	<i>T</i> _{ch} =125°C	-	-	250	
Gate-Source Leakage Current	Igss	V _{DS} =0V V _{GS} = ± 30V		-	10	100	nA
Drain-Source On-State Resistance	RDS(on)	V _{GS} =10V I _D =7.8A		-	0.169	0.190	Ω
Gate resistance	RG	f=1MHz, open drain		-	10.9	-	Ω

Dynamic Ratings

Parameter	Symbol	Conditions	min.	typ.	max.	Unit
Forward Transconductance	g _{fs}	V _{DS} =25V I _D =7.8A	7.2	14.5	-	S
Input Capacitance	Ciss	V _{DS} =400V	-	1130	-	
Output Capacitance	Coss	V _{GS} =0V	-	30	-	
Reverse Transfer Capacitance	Crss	f=250kHz	-	4.4	-	
Effective output capacitance, energy related (Note *7)	C _{o(er)}	V _{DS} =0400V V _{GS} =0V	-	69	-	pF
Effective output capacitance, time related (Note *8)	Co(tr)	V _{DS} =0400V V _{GS} =0V I _D =constant	-	251	-	
Turn-On Time	t _{d(on)}	- V _{DD} =400V, V _{GS} =10V I₀=7.8A,	-	18	-	_
Turn-On Time	tr		-	30	-	
Turn-Off Time	t _{d(off)}	$R_{\rm G}=18\Omega$	-	143	-	ns
Turn-Off Time	ti	See Fig.3 and Fig.4	-	22	-]
Total Gate Charge	QG		-	46	-	nC
Gate-Source Charge	Q _{GS}	V₀=400V, V₀s=10V /₀=15.5A See Fig.5	-	12	-	
Gate-Drain Charge	QGD		-	14	-	
Drain-Source crossover Charge	Qsw		-	7	-	

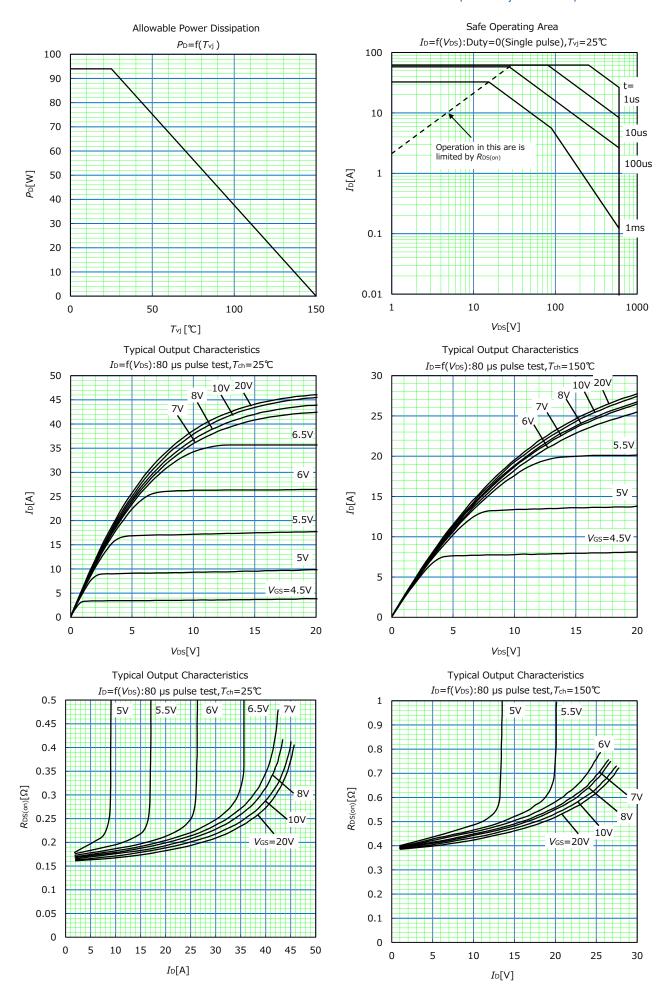
Note *7 : $C_{0(er)}$ is a fixed capacitance that gives the same stored energy as C_{oss} while V_{DS} is rising from 0 to 400V. Note *8 : $C_{0(er)}$ is a fixed capacitance that gives the same charging times as C_{oss} while V_{DS} is rising from 0 to 400V.

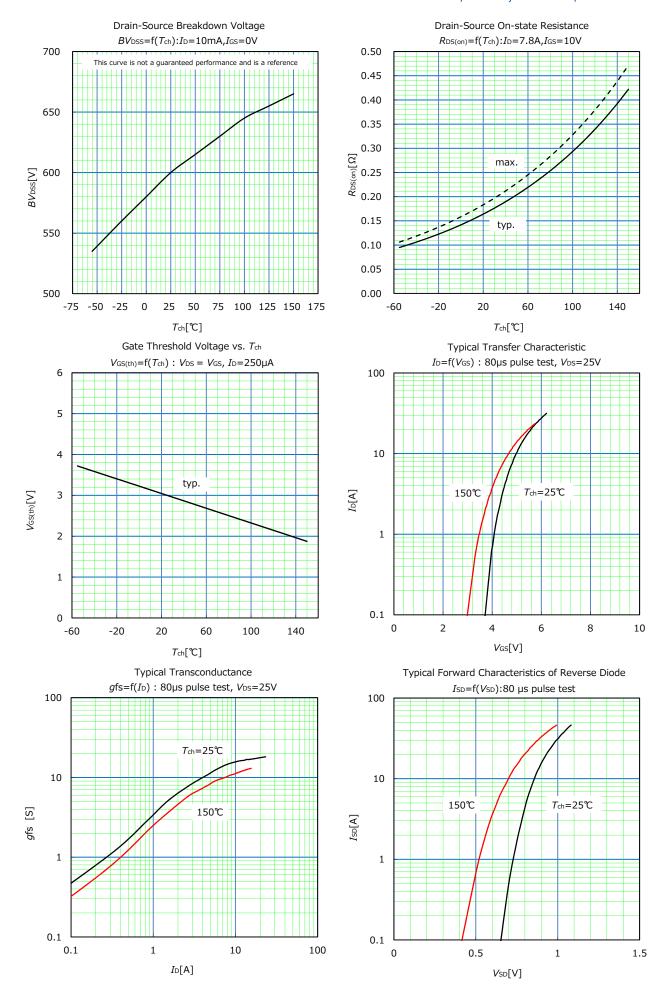
Reverse Diode

Parameter	Symbol	Conditions	min.	typ.	max.	Unit
Diode Forward On-Voltage	V _{SD}	I _{SD} =15.5A, V _{GS} =0V T _{ch} =25°C	-	0.90	1.35	V
Reverse Recovery Time	trr	- V₀₀=400V, /₅₀=15.5A -di/dt=100A/μs T₅h=25°C See Fig.6 and Fig.7	-	328	-	ns
Reverse Recovery Charge	Qrr		-	4.2	-	μC
Peak Reverse Recovery Current	I rp		-	25	-	А

Thermal Resistance

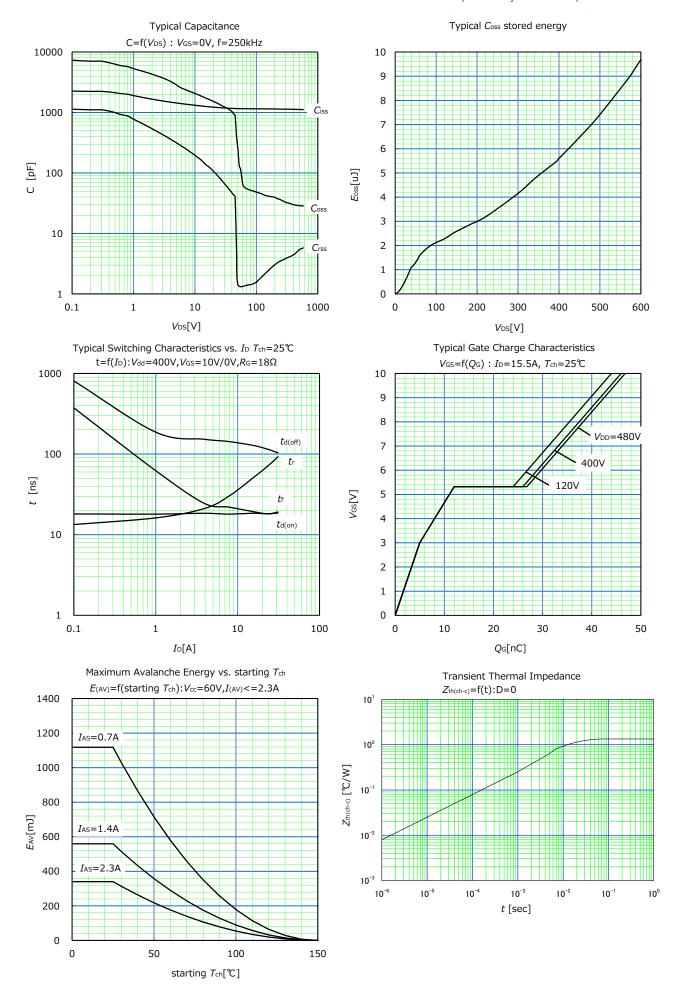
Parameter	Symbol	min.	typ.	max.	Unit
Channel to Case	Rth(ch-c)	-	-	1.33	°C/W
Channel to Ambient	R _{th(ch-a)}	-	-	50	°C/W

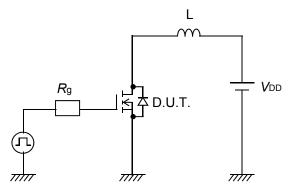


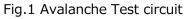


FMH60N190S2HF

http://www.fujielectric.com/products/semiconductor/







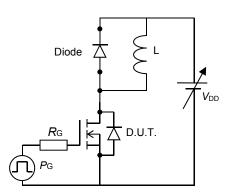


Fig.3 Switching Test circuit

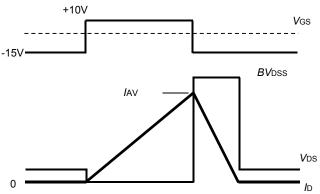


Fig.2 Operating waveforms of Avalanche Test

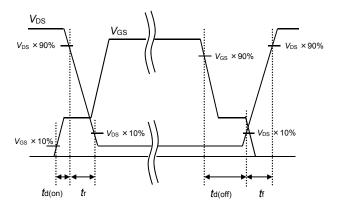


Fig.4 Operating waveform of Switching Test

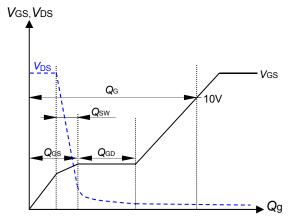
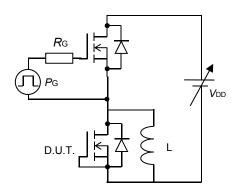


Fig.5 Operating waveform of Gate charge Test



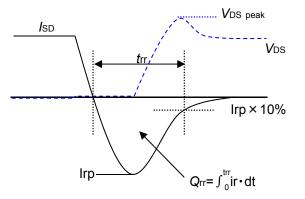


Fig.6 Reverse recovery Test circuit

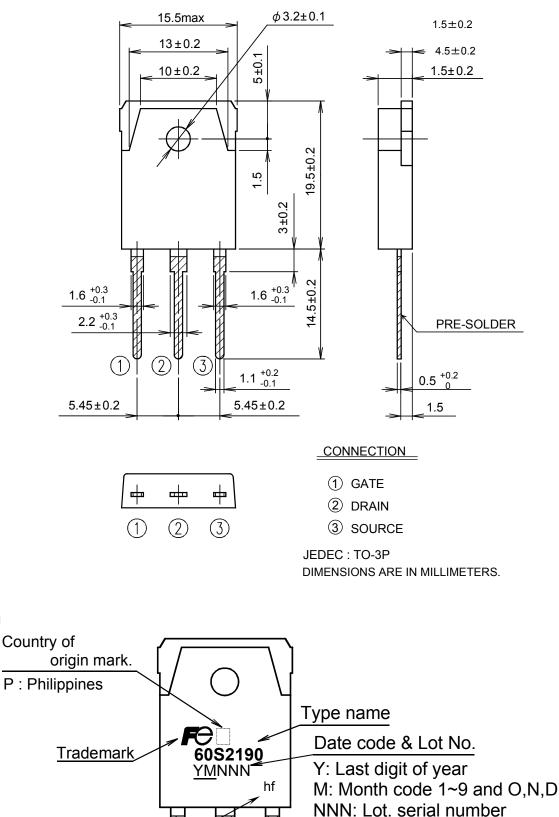
Fig.7 Operating waveform of Reverse recovery Test

Marking

hf: Halogen-free mark

http://www.fujielectric.com/products/semiconductor/

Outview: TO-3P Package



* The font (font type,size) and the trademark-size might be actually different.

WARNING

- 1. This Catalog contains the product specifications, characteristics, data, materials, and structures as of March 2017. The contents are subject to change without notice for specification changes or other reasons. When using a product listed in this Catalog, be sur to obtain the latest specifications. 2. All applications described in this Catalog exemplify the use of Fuji's products for your reference only. No right or license, either express or implied, under any patent, copyright, trade secret or other intellectual property right owned by Fuji Electric Co., Ltd. is (or shall be deemed) granted. Fuji Electric Co., Ltd. makes no representation or warranty, whether express or implied, relating to the infringement or alleged infringement of other's intellectual property rights which may arise from the use of the applications described herein. 3. Although Fuji Electric Co., Ltd. is enhancing product quality and reliability, a small percentage of semiconductor products may become faulty. When using Fuji Electric semiconductor products in your equipment, you are requested to take adequate safety measures to prevent the equipment from causing a physical injury, fire, or other problem if any of the products become faulty. It is recommended to make your design failsafe, flame retardant, and free of malfunction. 4. The products introduced in this Catalog are intended for use in the following electronic and electrical equipment which has normal reliability requirements. Computers OA equipment Communications equipment (terminal devices) Measurement equipment Electrical home appliances • Personal equipment • Industrial robots etc. Machine tools Audiovisual equipment 5. If you need to use a product in this Catalog for equipment requiring higher reliability than normal, such as for the equipment listed below, it is imperative to contact Fuji Electric Co., Ltd. to obtain prior approval. When using these products for such equipment, take adequate measures such as a backup system to prevent the equipment from malfunctioning even if a Fuji's product incorporated in the equipment becomes faulty. • Transportation equipment (mounted on cars and ships) Trunk communications equipment • Traffic-signal control equipment · Gas leakage detectors with an auto-shut-off feature · Emergency equipment for responding to disasters and anti-burglary devices · Safety devices Medical equipment 6. Do not use products in this Catalog for the equipment requiring strict reliability such as the following and equivalents to strategic equipment (without limitation). Space equipment · Aeronautic equipment Nuclear control equipment Submarine repeater equipment 7. Copyright ©1996-2017 by Fuji Electric Co., Ltd. All rights reserved. No part of this Catalog may be reproduced in any form or by any means without the express permission of Fuji Electric Co., Ltd. 8. If you have any question about any portion in this Catalog, ask Fuji Electric Co., Ltd. or its sales agents before using the product.
 - Neither Fuji Electric Co., Ltd. nor its agents shall be liable for any injury caused by any use of the products not in accordance with instructions set forth herein.