

Innovating Energy Technology

http://www.fujielectric.com/products/semiconductor/ **FUJI POWER MOSFET**

Super J MOS[®] S2 series

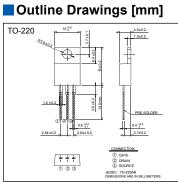
N-Channel enhancement mode power MOSFET

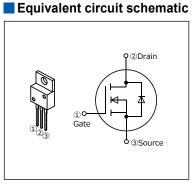
Features

Pb-free lead terminal **RoHS** compliant uses Halogen-free molding compound

Applications

For switching





Absolute Maximum Ratings at T_{vi}=25°C (unless otherwise specified)

Parameter	Symbol	Characteristics	Unit	Remarks
Drain Source Veltere	VDS	600	V	
Drain-Source Voltage	VDSX	600	V	V _{GS} =-30V
Continuous Brain Current		13	А	T _{vj} =25°C Note*1,2
Continuous Drain Current	I _D	8.2	А	T _{vj} =100°C Note*1,2
Pulsed Drain Current	I _{DP}	41.6	А	Note *2
Gate-Source Voltage	Vgs	±30	V	
Non-Repetitive Maximum Avalanche Current	las	1.5	А	Note *3
Non-Repetitive Maximum Avalanche Energy	Eas	468	mJ	Note *4
Maximum Drain-Source dV/dt	dV _{DS} /dt	50	V/ns	V _{DS} ≤ 600V
Continuous	,	13	А	T _{vj} =25°C Note*1,2
Diode Forward Current	Isd	8.2	А	T _{vj} =100°C Note*1,2
Pulsed Diode Forward Current	Isdp	41.6	А	Note *2
Peak Diode Recovery dV/dt	dV/dt	15	V/ns	Note *5
Peak Diode Recovery -di/dt	-di/dt	100	A/µs	Note *6
Maximum Dawar Disaination	PD	2.02	W	<i>T</i> ₂=25°C
Maximum Power Dissipation	F D	75	vv	<i>T</i> _{vj} =25°C
Operating and Storage Temperature range	T ch	150	°C	
Operating and Storage Temperature range	T _{stg}	-55 to +150	°C	

Note *1 : Maximum duty cycle D=0.64

Note *1: Imited by maximum channel temperature. Note *3: Imited by maximum channel temperature. Note *3: T_{ch}≤150°C, See Fig.1 and Fig.2 Note *4: Starting T_{ch}=25°C, I_{As}=0.9A, L=1.06H, V_{DD}=60V, R_G=50Ω, See Fig.1 and Fig.2 E_{As} limited by maximum channel temperature and avalanche current. Note *5: I_{SD}≤10.4A, -di/dt≤100A/µs, V_{DS} peak≤ 600V, T_{ch}≤150°C. Note *6: I_{SD}≤10.4A, dV/dt≤15V/ns, V_{DS} peak≤ 600V, T_{ch}≤150°C.

Electrical Characteristics at T_{vj}=25°C (unless otherwise specified) Static Ratings

Parameter	Symbol	Conditions		min.	typ.	max.	Unit
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I₀=250µA		600	-	-	V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} I₀=150µA		2.5	3.0	3.5	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =600V V _{GS} =0V	T _{ch} =25°C	-	-	25	μΑ
		V _{DS} =480V V _{GS} =0V	<i>T</i> _{ch} =125°C	-	-	250	
Gate-Source Leakage Current	Igss	V _{DS} =0V V _{GS} = ± 30V		-	10	100	nA
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =10V I⊳=5.2A		-	0.248	0.280	Ω
Gate resistance	RG	f=1MHz, open drain		-	12.7	-	Ω

Dynamic Ratings

Parameter	Symbol	Conditions	min.	typ.	max.	Unit
Forward Transconductance	g fs	V _{DS} =25V I _D =5.2A	4.7	9.5	-	S
Input Capacitance	Ciss	V _{DS} =400V	-	790	-	
Output Capacitance	Coss	V _{GS} =0V	-	22	-	
Reverse Transfer Capacitance	Crss	f=250kHz	-	3.5	-	
Effective output capacitance, energy related (Note *7)	C _{o(er)}	V _{DS} =0400V V _{GS} =0V	-	53	-	pF
Effective output capacitance, time related (Note *8)	C _{o(tr)}	V _{DS} =0400V V _{GS} =0V I _D =constant	- 183	183	-	
Turn-On Time	t _{d(on)}	V _{DD} =400V, V _{GS} =10V	-	15	-	- ns
Turn-On Time	tr	$I_{D}=5.2A,$	-	28	-	
td(off)	t _{d(off)}	R₀=18Ω See Fig.3 and Fig.4	-	95	-	
Turn-Off Time	<i>t</i> r		-	21	-	
Total Gate Charge	QG	V₀₀=400V, V₀₅=10V /₀=10.4A See Fig.5	-	33	-	nC
Gate-Source Charge	QGS		-	11	-	
Gate-Drain Charge	QGD		-	9	-	
Drain-Source crossover Charge	Qsw		-	7	-	

Note *7 : $C_{0(er)}$ is a fixed capacitance that gives the same stored energy as C_{oss} while V_{DS} is rising from 0 to 400V. Note *8 : $C_{0(er)}$ is a fixed capacitance that gives the same charging times as C_{oss} while V_{DS} is rising from 0 to 400V.

Reverse Diode

Parameter	Symbol	Conditions	min.	typ.	max.	Unit
Diode Forward On-Voltage	V _{SD}	I _{sD} =10.4A, V _{GS} =0V T _{ch} =25°C	-	0.95	1.35	V
Reverse Recovery Time	trr	- V₀₀=400V, /₅₀=10.4A -di/dt=100A/µs T₅h=25°C See Fig.6 and Fig.7	-	290	-	ns
Reverse Recovery Charge	Qrr		-	2.9	-	μC
Peak Reverse Recovery Current	Ігр		-	20.5	-	А

Thermal Resistance

Parameter	Symbol	min.	typ.	max.	Unit
Channel to Case	Rth(ch-c)	-	-	1.667	°C/W
Channel to Ambient	R _{th(ch-a)}	-	-	62	°C/W

t= 1us

10us

100us

1ms

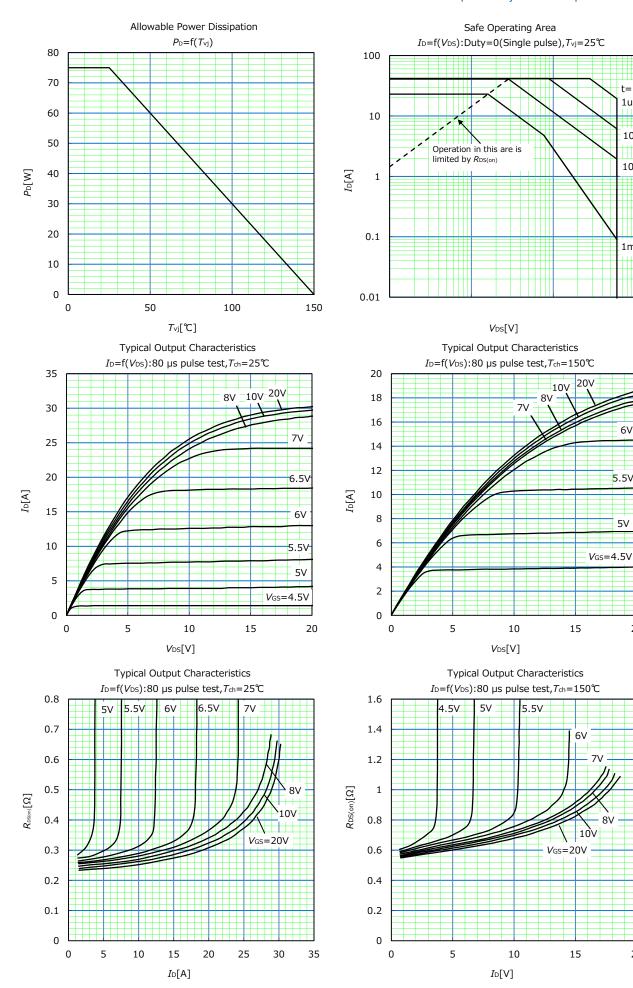
6V

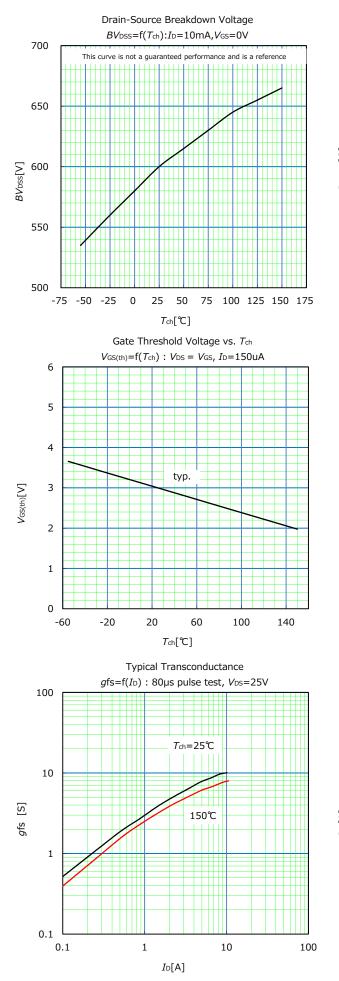
5V

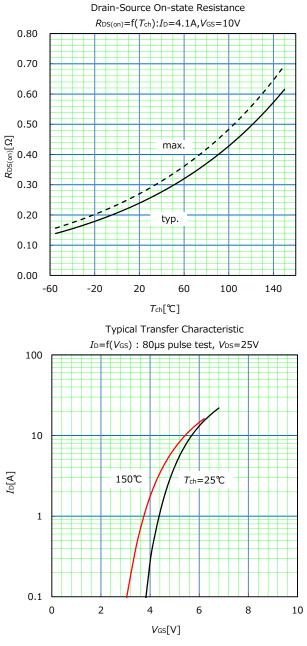
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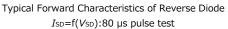
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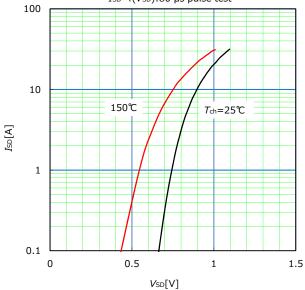
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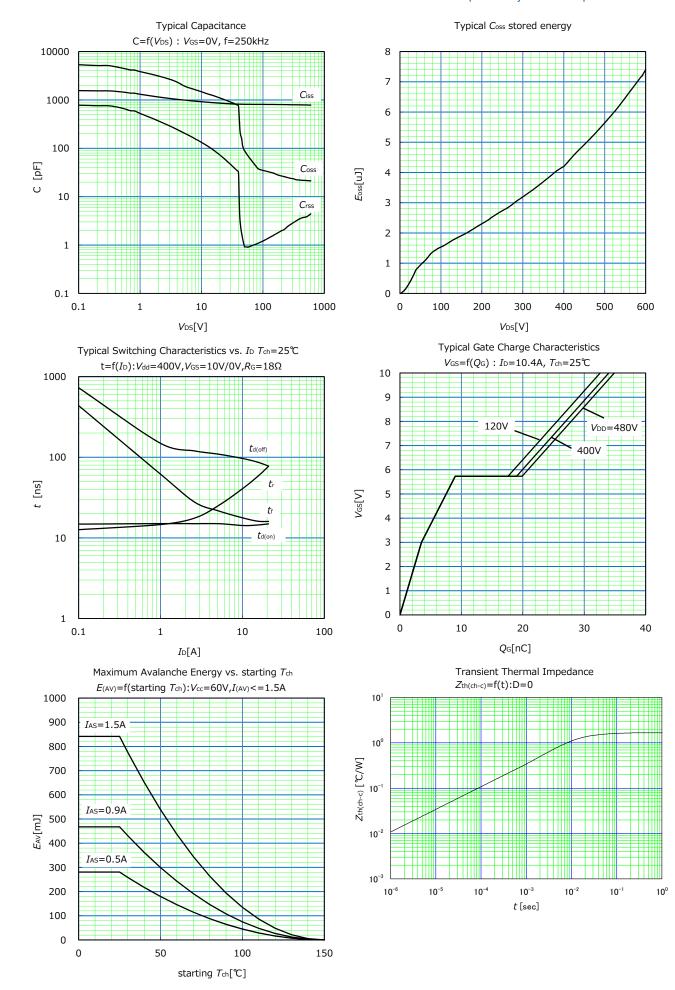


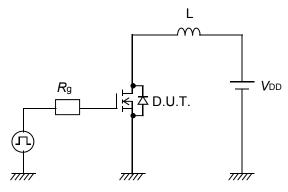


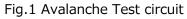


FMP60N280S2HF

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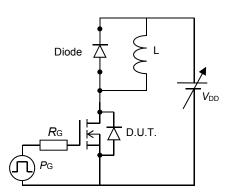


Fig.3 Switching Test circuit

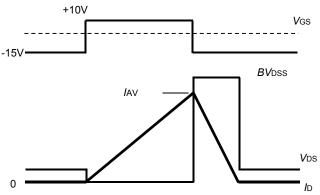


Fig.2 Operating waveforms of Avalanche Test

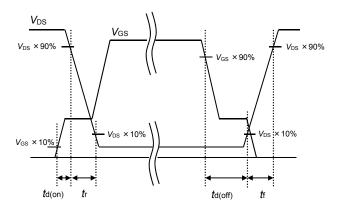


Fig.4 Operating waveform of Switching Test

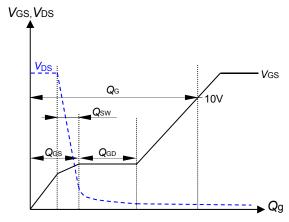
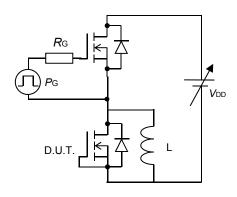


Fig.5 Operating waveform of Gate charge Test



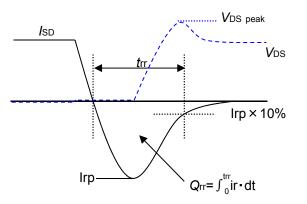
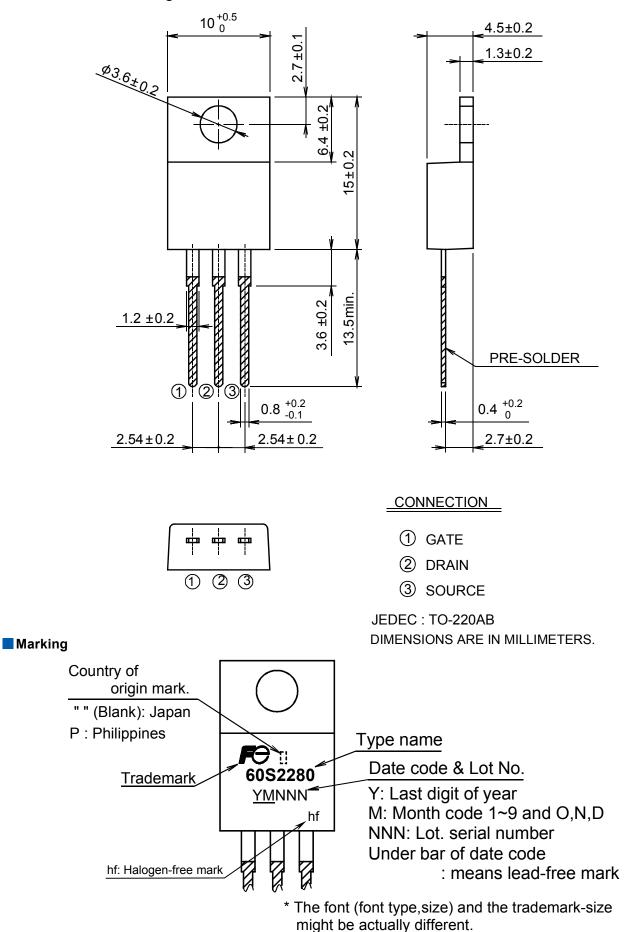


Fig.6 Reverse recovery Test circuit

Fig.7 Operating waveform of Reverse recovery Test

Outview: TO-220 Package



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