

# Innovating Energy Technology

http://www.fujielectric.com/products/semiconductor/ **FUJI POWER MOSFET** 

# Super J MOS<sup>®</sup> S2 series

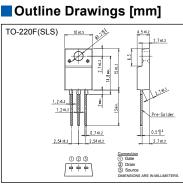
## N-Channel enhancement mode power MOSFET

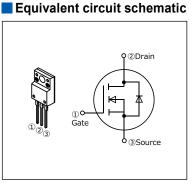
#### Features

Pb-free lead terminal **RoHS** compliant uses Halogen-free molding compound

#### Applications

For switching





### Absolute Maximum Ratings at T<sub>vi</sub>=25°C (unless otherwise specified)

Parameter	Symbol	Characteristics	Unit	Remarks
Drain Source Veltere	V <sub>DS</sub>	600	V	
Drain-Source Voltage	V <sub>DSX</sub>	600	V	V <sub>GS</sub> =-30V
Continuous Drain Current	I <sub>D</sub>	42.3	А	T <sub>vj</sub> =25°C Note*1,2
Continuous Drain Current		26.8	А	T <sub>vj</sub> =100°C Note*1,2
Pulsed Drain Current	I <sub>DP</sub>	131	А	Note *2
Gate-Source Voltage	V <sub>GS</sub>	±30	V	
Non-Repetitive Maximum Avalanche Current	las	4.9	А	Note *3
Non-Repetitive Maximum Avalanche Energy	Eas	1018	mJ	Note *4
Maximum Drain-Source dV/dt	dV <sub>DS</sub> /dt	50	V/ns	<i>V</i> <sub>DS</sub> ≤ 600V
Continuous	1	42.3	А	T <sub>vj</sub> =25°C Note*1,2
Diode Forward Current	Isd	26.8	А	T <sub>vj</sub> =100°C Note*1,2
Pulsed Diode Forward Current	ISDP	131	А	Note *2
Peak Diode Recovery dV/dt	dV/dt	15	V/ns	Note *5
Peak Diode Recovery -di/dt	-di/dt	100	A/µs	Note *6
	_	2.16		<i>T</i> <sub>a</sub> =25°C
Maximum Power Dissipation	PD	85	W	<i>T</i> <sub>vj</sub> =25°C
One sections and Otensons Tensons from sectors	Tch	150	°C	
Operating and Storage Temperature range	T <sub>stg</sub>	-55 to +150	°C	
Isolation Voltage (TO-220F)	Viso	2	kVrms	t=60sec, f=60Hz

Note \*1 : Maximum duty cycle D=0.60

Note \*1 : Maximum duty cycle D=0.00 Note \*1 : Limited by maximum channel temperature. Note \*2 : Tris150°C, See Fig.1 and Fig.2 Note \*3 : Starting Tris=25°C, IAs=3A, L=207mH, Vbb=60V, Rs=50 $\Omega$ , See Fig.1 and Fig.2 EAs limited by maximum channel temperature and avalanche current.

Note \*1 : Iso≤32.8A, -di/dts100A/µs, Vos peak≤ 600V, 7ch≤150°C. Note \*5 : Iso≤32.8A, dV/dt≤15V/ns, Vos peak≤ 600V, 7ch≤150°C.

# Electrical Characteristics at T<sub>vi</sub>=25°C (unless otherwise specified) Static Ratings

Parameter	Symbol	Conditions		min.	typ.	max.	Unit
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I⊳=250µA		600	-	-	V
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> I₀=1.72mA		3.5	4.0	4.5	V
Zero Gate Voltage Drain Current	loss	V <sub>DS</sub> =600V V <sub>GS</sub> =0V	T <sub>ch</sub> =25°C	-	-	25	μA
		V <sub>DS</sub> =480V V <sub>GS</sub> =0V	<i>T</i> <sub>ch</sub> =125°C	-	-	250	
Gate-Source Leakage Current	Igss	V <sub>DS</sub> =0V V <sub>GS</sub> = ± 30V		-	10	100	nA
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =10V I₀=16.4A		-	0.083	0.088	Ω
Gate resistance	RG	f=1MHz, open drain		-	7.5	-	Ω

#### Dynamic Ratings

Parameter	Symbol	Conditions	min.	typ.	max.	Unit
Forward Transconductance	<b>g</b> <sub>fs</sub>	V <sub>DS</sub> =25V I <sub>D</sub> =16.4A	10	20	-	S
Input Capacitance	Ciss	V <sub>DS</sub> =400V	-	1830	-	
Output Capacitance	Coss	V <sub>GS</sub> =0V	-	59	-	
Reverse Transfer Capacitance	Crss	f=250kHz	-	7.5	-	
Effective output capacitance, energy related (Note *7)	C <sub>o(er)</sub>	V <sub>DS</sub> =0400V V <sub>GS</sub> =0V	-	138	-	pF
Effective output capacitance, time related (Note *8)	Co(tr)	V₀s=0400V V₀s=0V I₀=constant	-	546	-	
	t <sub>d(on)</sub>	$V_{DD}=400V, V_{GS}=10V$ $I_{D}=16.4A, R_{G}=15\Omega$ See Fig.3 and Fig.4	-	31	-	- ns
Turn-On Time	tr		-	92	-	
Turn-Off Time $\frac{t_{doff}}{t_{t}}$	t <sub>d(off)</sub>		-	129	-	
	<i>t</i> r		-	24	-	
Total Gate Charge	QG		-	72	-	nC
Gate-Source Charge	Q <sub>GS</sub>	$V_{DD}$ =400V, $V_{GS}$ =10V	-	30	-	
Gate-Drain Charge	QGD	— /₀=32.8A See Fig.5	-	29	-	
Drain-Source crossover Charge	Qsw		-	21	-	

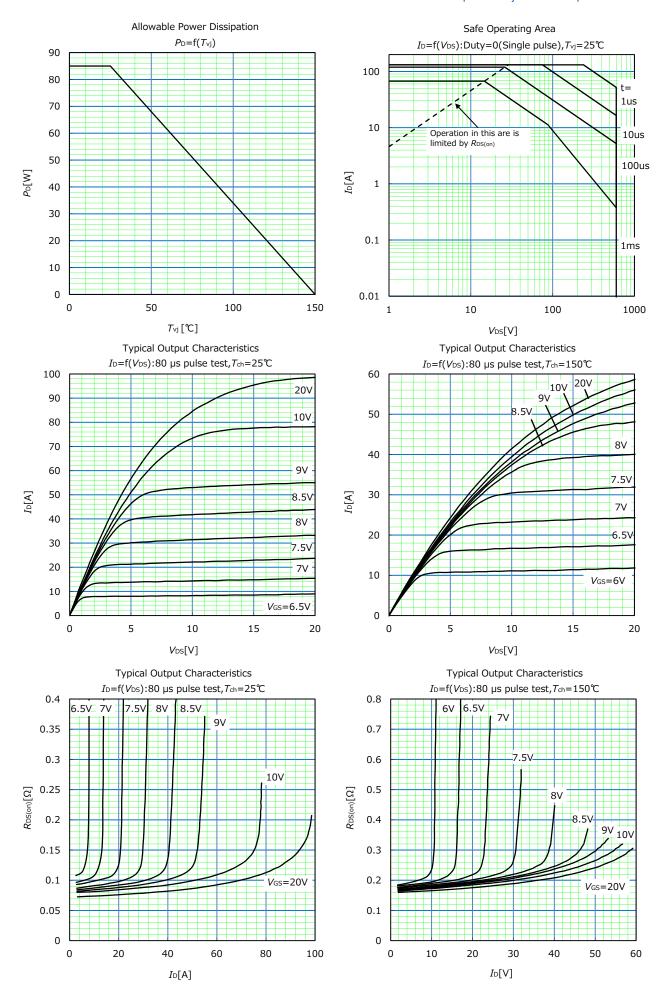
Note \*7 :  $C_{0(er)}$  is a fixed capacitance that gives the same stored energy as  $C_{oss}$  while  $V_{DS}$  is rising from 0 to 400V. Note \*8 :  $C_{0(er)}$  is a fixed capacitance that gives the same charging times as  $C_{oss}$  while  $V_{DS}$  is rising from 0 to 400V.

#### Reverse Diode

Parameter	Symbol	Conditions	min.	typ.	max.	Unit
Diode Forward On-Voltage	Vsd	I <sub>SD</sub> =32.8A, V <sub>GS</sub> =0V T <sub>ch</sub> =25°C	-	0.95	1.35	V
Reverse Recovery Time	t.r	- V₀₀=400V, /₅₀=32.8A -di/dt=100A/µs 7₅h=25°C See Fig.6 and Fig.7	-	370	-	ns
Reverse Recovery Charge	Qrr		-	6.5	-	μC
Peak Reverse Recovery Current	Ігр		-	33	-	А

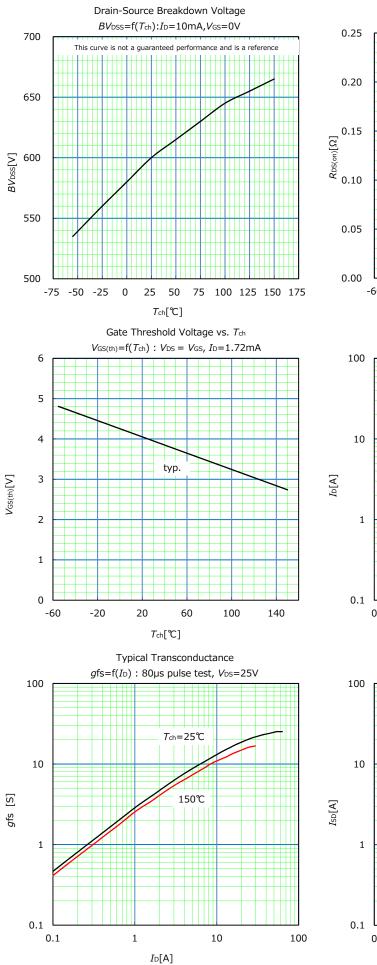
#### Thermal Resistance

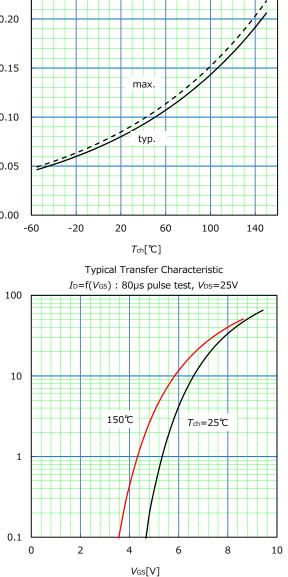
Parameter	Symbol	min.	typ.	max.	Unit
Channel to Case	Rth(ch-c)	-	-	1.47	°C/W
Channel to Ambient	R <sub>th(ch-a)</sub>	-	-	58	°C/W

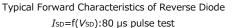


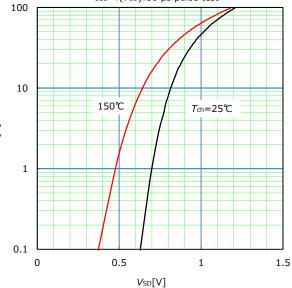
Drain-Source On-state Resistance

RDS(on)=f(Tch):ID=16.4A,VGS=10V



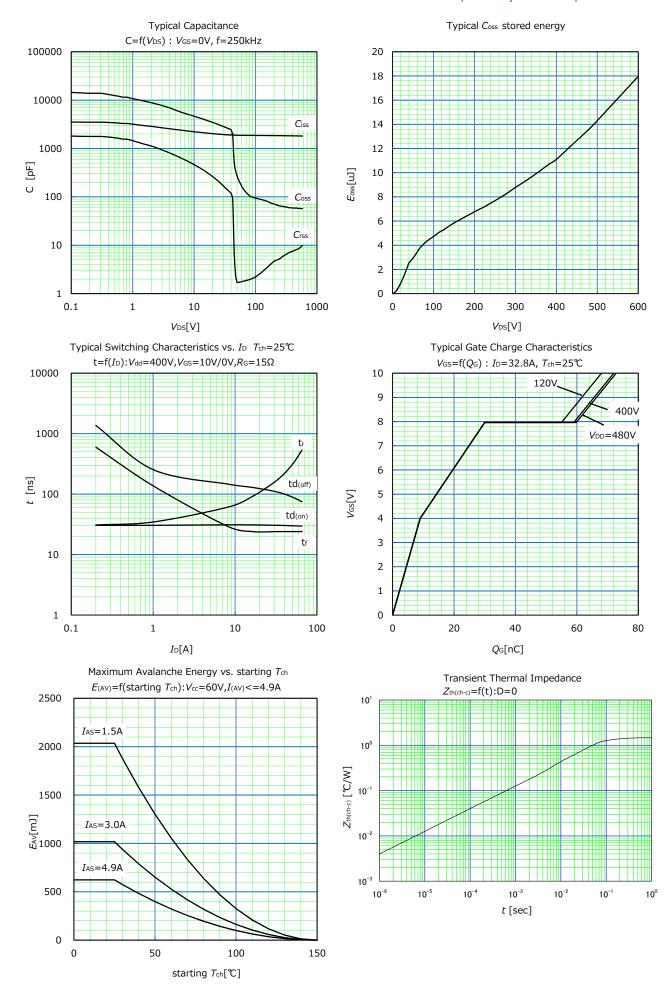


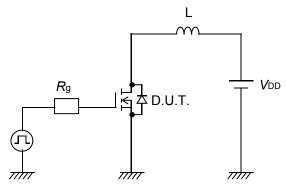


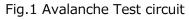


## FMV60N088S2HF

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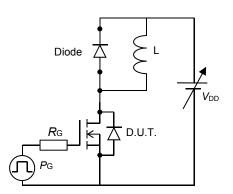


Fig.3 Switching Test circuit

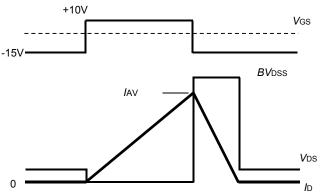


Fig.2 Operating waveforms of Avalanche Test

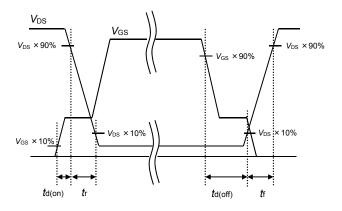


Fig.4 Operating waveform of Switching Test

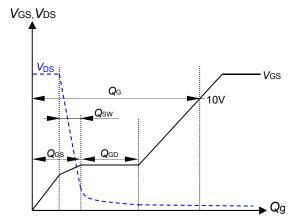
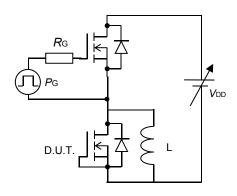


Fig.5 Operating waveform of Gate charge Test



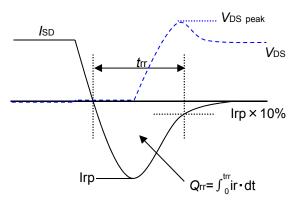


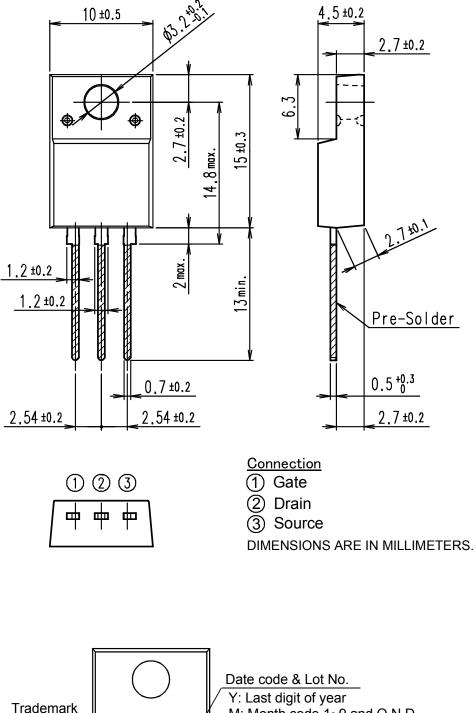
Fig.6 Reverse recovery Test circuit

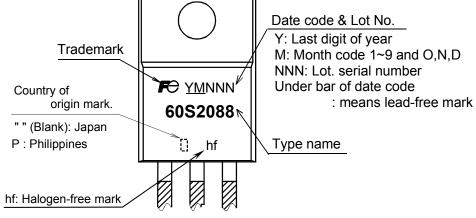
Fig.7 Operating waveform of Reverse recovery Test

Marking

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#### Outview: TO-220F(SLS) Package





\* The font (font type,size) and the trademark-size might be actually different.

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