

4MBI900VB-120RA-50

IGBT Modules

IGBT Power Module (V series)

1200V/900A/IGBT, ±600V/900A/RB-IGBT, 4-in-1 package

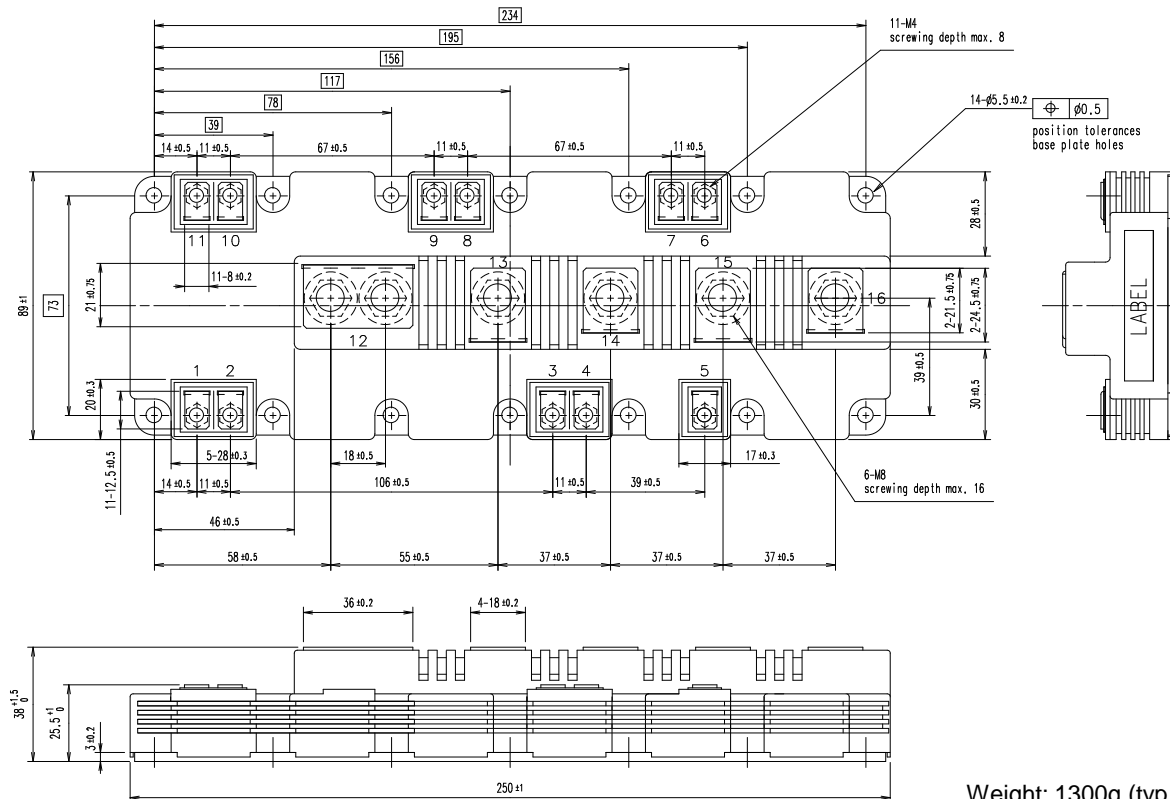
■ Features

- Higher efficiency
- Optimized Advanced T-type circuit
- Reverse-Blocking IGBT as for AC Switch
- Low inductance module structure

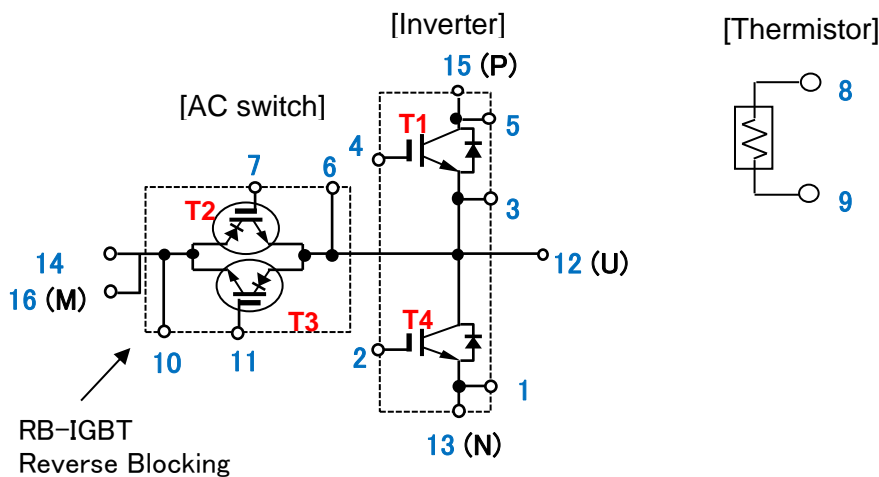
■ Applications

- Inverter for motor drive
- Uninterruptible power supply
- Power conditioner for PV, Wind turbine

■ Outline drawing (Unit : mm)



■ Equivalent Circuit



4MBI900VB-120RA-50

IGBT Modules

■ Absolute Maximum Ratings (at $T_c = 25^\circ\text{C}$ unless otherwise specified)

Item		Symbol	Condition	Maximum Rating	Unit	
Inverter	Collector-Emitter voltage	V_{CES}		1200	V	
	Gate-Emitter voltage	V_{GES}		± 20	V	
	Collector current	IGBT	I_C	Continuous	$T_c = 25^\circ\text{C}$	1200
					$T_c = 100^\circ\text{C}$ Duty=88%	900
		FWD	$-I_C$	1ms		1800
					$-I_C$ pulse	1ms
	Collector power dissipation	P_C	1 device	3950	W	
	Junction temperature	T_{vj}		175	°C	
	Operating junction temperature (under switching conditions)	T_{vjop}		150		
	Clamp diode	Collector-Emitter voltage	V_{CES}		± 600	V
Gate-Emitter voltage		V_{GES}		± 20	V	
Collector current		RB-IGBT	I_C	Continuous	$T_c = 25^\circ\text{C}$	1200
					$T_c = 88^\circ\text{C}$ Duty=56%	900
			I_C pulse	1ms	1800	A
Collector power dissipation		P_C	1 device	2660	W	
Junction temperature		T_{vj}		150	°C	
Operating junction temperature (under switching conditions)		T_{vjop}		125		
Case temperature	T_c		125			
Storage temperature	T_{stg}		-40 ~ 125			
Isolation voltage	between terminal and copper base (*1)	V_{iso}	AC: 1min.	4000	Vrms	
	between thermistor and others (*2)					
Screw Torque (*3)	Mounting	-	M5	6.0	N m	
	Main terminals	-	M8	10.0		
	Sense terminals	-	M4	2.1		

(*1) All terminals should be connected together during the test.

(*2) Two thermistor terminals should be connected together, other terminals should be connected together and shorted to base plate during the test.

(*3) Recommended value : Mounting 3.0 ~ 6.0 Nm (M5)
 Recommended value : Main Terminals 8.0 ~ 10.0 Nm (M8)
 Recommended value : Sense Terminals 1.8 ~ 2.1 Nm (M4)

4MBI900VB-120RA-50

IGBT Modules

■ Electrical characteristics (at $T_{vj}= 25^{\circ}\text{C}$ unless otherwise specified)

Item	Symbol	Condition	Characteristics			Units		
			min.	typ.	max.			
Inverter	Zero gate voltage Collector current	I_{CES}	$V_{GE} = 0\text{V}$ $V_{CE} = 1200\text{V}$	-	-	6.0	mA	
	Gate-Emitter leakage current	I_{GES}	$V_{CE} = 0\text{V}$ $V_{GE} = \pm 20\text{V}$	-	-	1200	nA	
	Gate-Emitter threshold voltage	$V_{GE(th)}$	$V_{CE} = 20\text{V}$ $I_C = 900\text{mA}$	6.0	6.5	7.0	V	
	Collector-Emitter saturation voltage	$V_{CE(sat)}$ (chip)	$V_{GE} = 15\text{V}$ $I_C = 900\text{A}$	$T_{vj}=25^{\circ}\text{C}$	-	1.85	2.35	V
				$T_{vj}=125^{\circ}\text{C}$	-	2.20	-	
				$T_{vj}=150^{\circ}\text{C}$	-	2.25	-	
		$V_{CE(sat)}$ (terminal)	$V_{GE} = 15\text{V}$ $I_C = 900\text{A}$	$T_{vj}=25^{\circ}\text{C}$	-	1.95	2.45	
				$T_{vj}=125^{\circ}\text{C}$	-	2.30	-	
				$T_{vj}=150^{\circ}\text{C}$	-	2.35	-	
	Internal gate resistance	$r_{g(int)}$	-	-	0.80	-	Ω	
	Input capacitance	C_{ies}	$V_{CE}=10\text{V}, V_{GE}=0\text{V}, f=1\text{MHz}$	-	75.5	-	nF	
	Turn-on time	t_{on}	SW mode : A (*1) $V_{CC} = 300\text{V}$ $I_C = 900\text{A}$	-	0.60	-	μs	
		t_r		-	0.26	-		
		$t_{r(l)}$		-	0.14	-		
	Turn-off time	t_{off}	$V_{GE} = \pm 15\text{V}$ $R_G = +3.9/-1.0\Omega$	-	0.85	-	μs	
t_f		-		0.09	-			
Forward voltage	V_F (chip)	$I_F = 900\text{A}$	$T_{vj}=25^{\circ}\text{C}$	-	1.70	2.20	V	
			$T_{vj}=125^{\circ}\text{C}$	-	1.85	-		
			$T_{vj}=150^{\circ}\text{C}$	-	1.80	-		
	V_F (terminal)	$I_F = 900\text{A}$	$T_{vj}=25^{\circ}\text{C}$	-	1.85	2.35		
			$T_{vj}=125^{\circ}\text{C}$	-	2.00	-		
			$T_{vj}=150^{\circ}\text{C}$	-	1.95	-		
Reverse recovery time	t_{rr}	SW mode: B (*1) $V_{CC} = 300\text{V}$ $I_F = 900\text{A}$ $V_{GE} = \pm 15\text{V}$ $R_G = +2.7/-15\Omega$	-	0.18	-	μs		
AC switch	Zero gate voltage Collector current	I_{CES}	$V_{GE} = 0\text{V}$ $V_{CE} = 600\text{V}$	-	-	9	mA	
	Gate-Emitter leakage current	I_{GES}	$V_{CE} = 0\text{V}$ $V_{GE} = \pm 20\text{V}$	-	-	1800	nA	
	Gate-Emitter threshold voltage	$V_{GE(th)}$	$V_{CE} = 20\text{V}$ $I_C = 900\text{mA}$	5.5	6.5	7.5	V	
	Collector-Emitter saturation voltage	$V_{CE(sat)}$ (chip)	$V_{GE} = 15\text{V}$ $I_C = 900\text{A}$	$T_{vj}=25^{\circ}\text{C}$	-	2.45	2.80	V
				$T_{vj}=125^{\circ}\text{C}$	-	2.60	-	
		$V_{CE(sat)}$ (terminal)	$V_{GE} = 15\text{V}$ $I_C = 900\text{A}$	$T_{vj}=25^{\circ}\text{C}$	-	2.55	3.20	
				$T_{vj}=125^{\circ}\text{C}$	-	2.70	-	
	Internal gate resistance	$r_{g(int)}$	-	-	2.80	-	Ω	
	Input capacitance	C_{ies}	$V_{CE}=10\text{V}, V_{GE}=0\text{V}, f=1\text{MHz}$	-	58.5	-	nF	
	Turn-on time	t_{on}	SW mode : B (*1) $V_{CC} = 300\text{V}$ $I_C = 900\text{A}$	-	0.66	-	μs	
t_r		-		0.27	-			
$t_{r(l)}$		-		0.18	-			
Turn-off time	t_{off}	$V_{GE} = \pm 15\text{V}$ $R_G = +2.7/-15\Omega$	-	1.69	-	μs		
	t_f		-	0.16	-			
Reverse recovery time	t_{rr}	SW mode: A (*1) $V_{CC} = 300\text{V}$ $I_F = 900\text{A}$ $V_{GE} = \pm 15\text{V}$ $R_G = +3.9/-1.0\Omega$	-	0.13	-	μs		
Thermistor	Resistance	R	$T=25^{\circ}\text{C}$	-	5000	-	Ω	
			$T=100^{\circ}\text{C}$	465	495	520		
	B Value	B	$T=25/50^{\circ}\text{C}$	3305	3375	3450	K	

(*1) Please refer to Page4, there is definition of A mode and B mode.

■ Thermal resistance characteristics

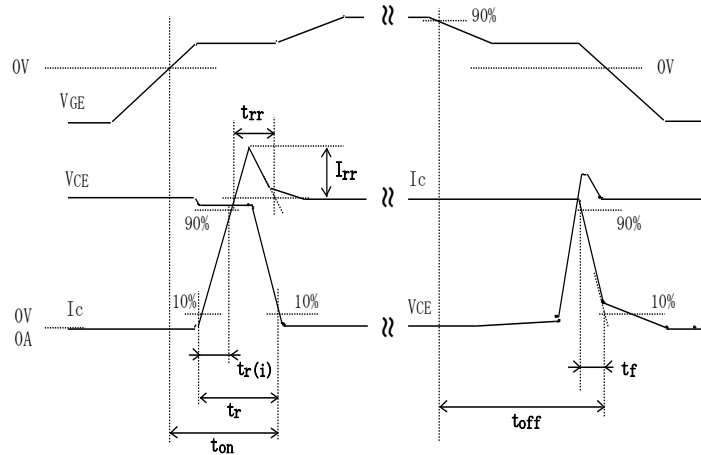
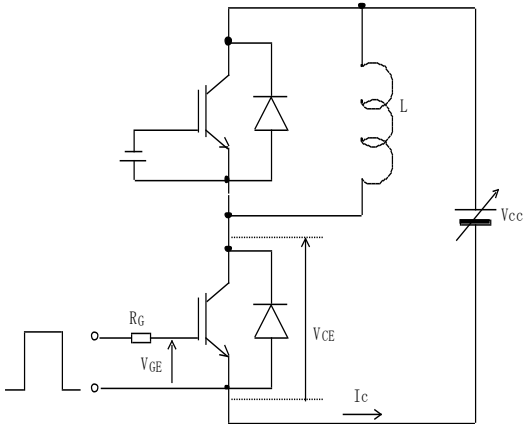
Item	Symbol	Condition	Characteristics			Units
			min.	typ.	max.	
Thermal resistance (1device)	$R_{th(j-c)}$	T1,T4 IGBT	-	-	0.038	$^{\circ}\text{C/W}$
		T1,T4 FWD	-	-	0.054	
		T2,T3 RB-IGBT	-	-	0.047	
Contact thermal resistance (1device) (*2)	$R_{th(c-f)}$	T1,T4	with Thermal Compound	-	0.0083	-
		T2,T3		-	0.0056	-

(*2) This is the value which is defined mounting on the additional cooling fin with thermal compound.

4MBI900VB-120RA-50

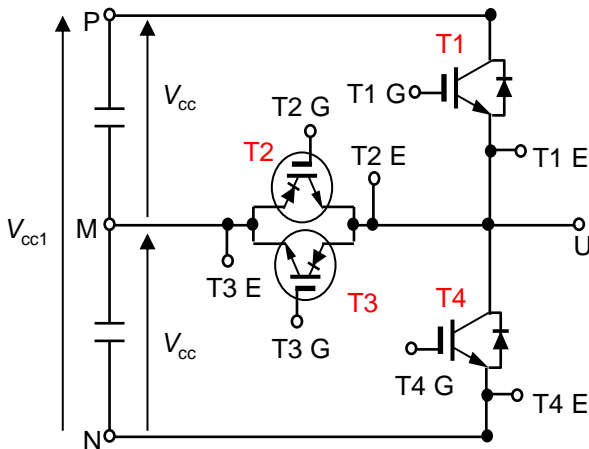
IGBT Modules

Definitions of switching time



Definitions of switching mode

Definition on-state voltage at terminal and switching characteristics



Fuji defined V_{CE} value of terminal by using Sense P and Sense T1E for T1 arm , Sense T3E and Sense T2E for T2 arm , Sense T2E and Sense T3E for T3 arm and Sense T1E and Sense T4E for T4 arm .

Switching characteristics of V_{CE} also is defined between Sense P and Sense T1E for T1 arm , Sense T3E and Sense T2E for T2 arm , Sense T2E and Sense T3E for T3 arm and Sense T1E and Sense T4E for T4 arm .

Please use these terminals whenever measure spike voltage and on-state voltage .

Definition of switching mode

	Load L	T1	T2	T3	T4
A	M-U	SW	ON	OFF	OFF
	M-U	OFF	OFF	ON	SW
B	U-N	OFF	SW	ON	OFF
	P-U	OFF	ON	SW	OFF

SW: Connect to drive circuit and input gate signal

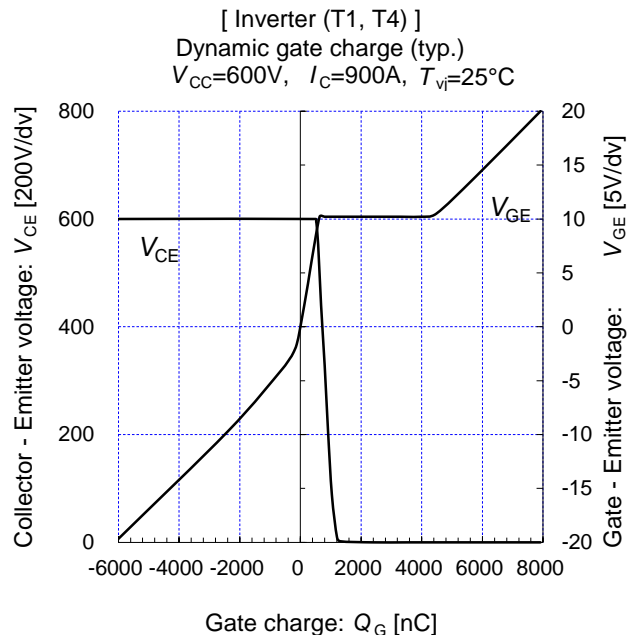
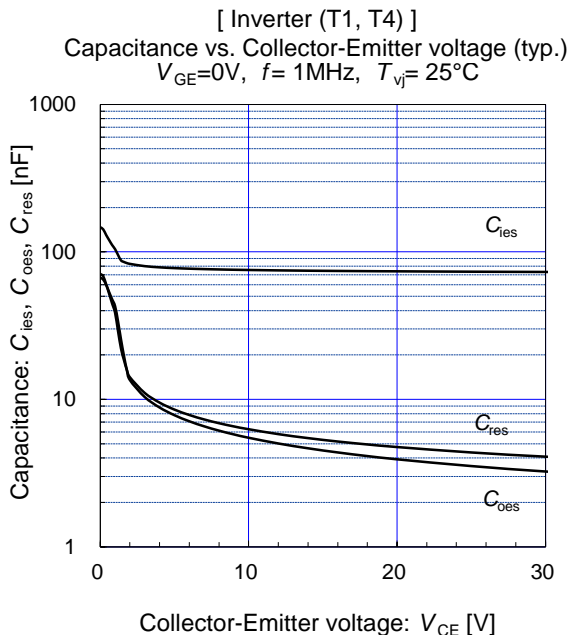
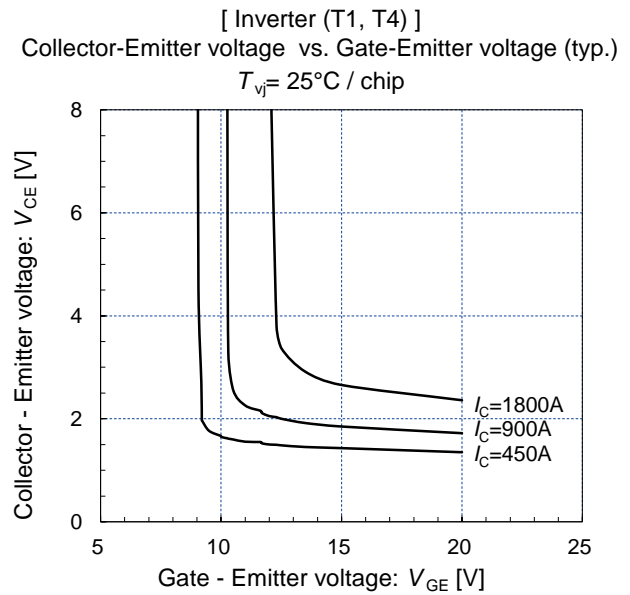
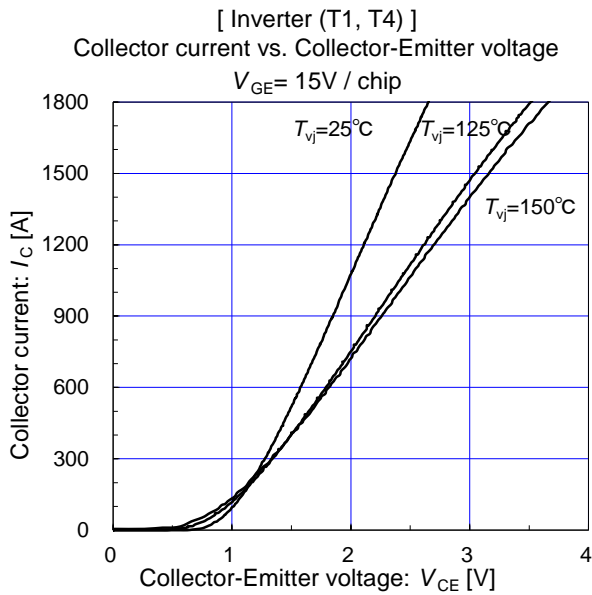
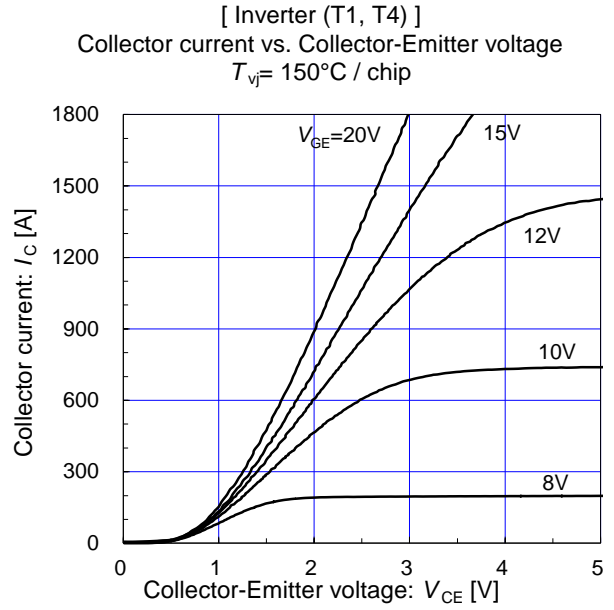
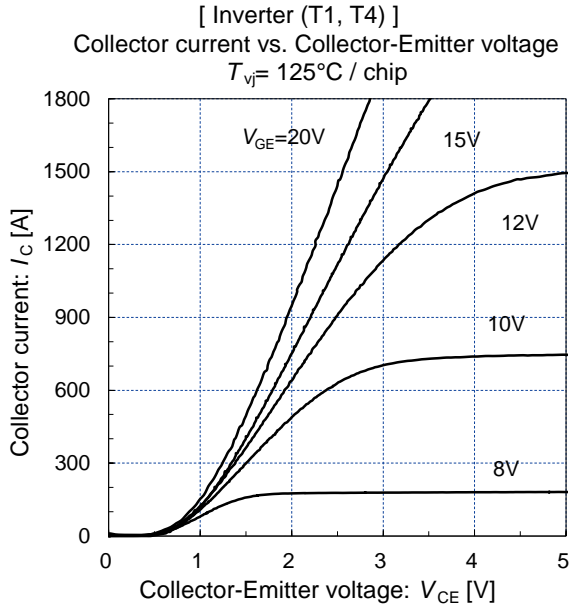
ON: Bias voltage of gate +15V

OFF: Reverse bias voltage of gate -15V

$V_{cc1}=2 \times V_{cc}$

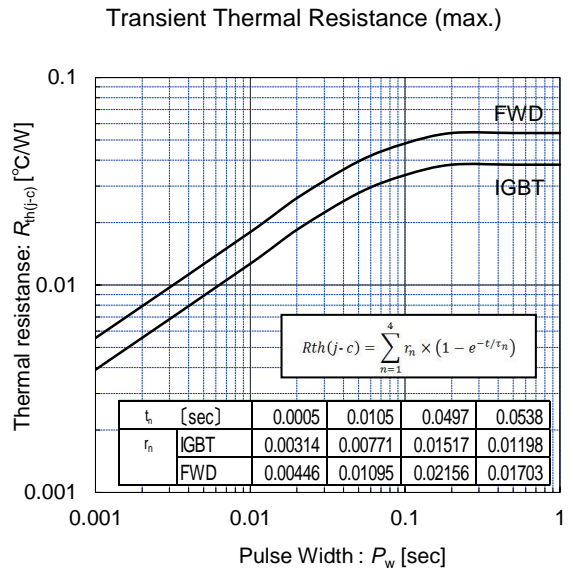
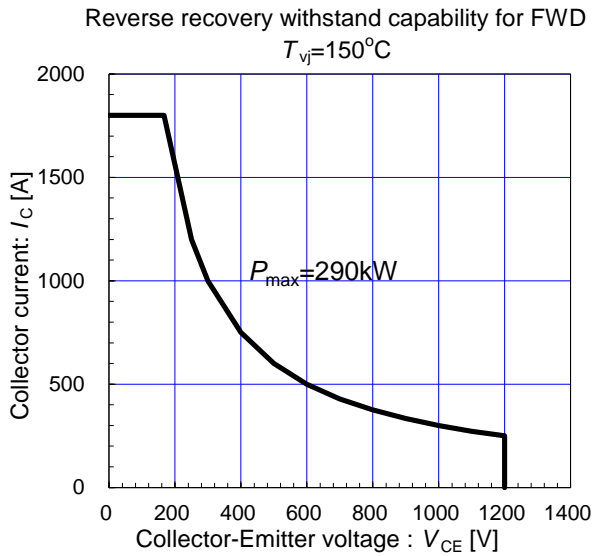
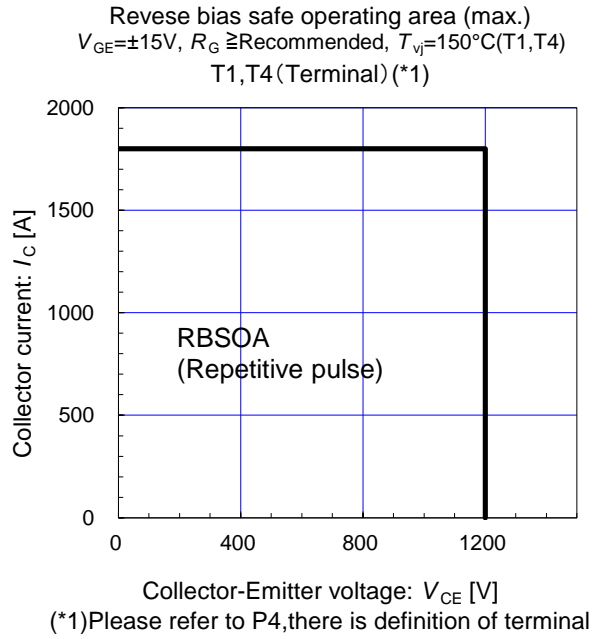
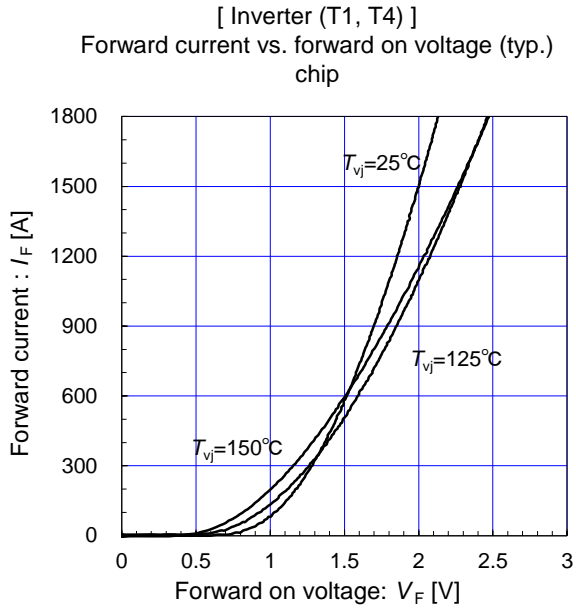
4MBI900VB-120RA-50

IGBT Modules



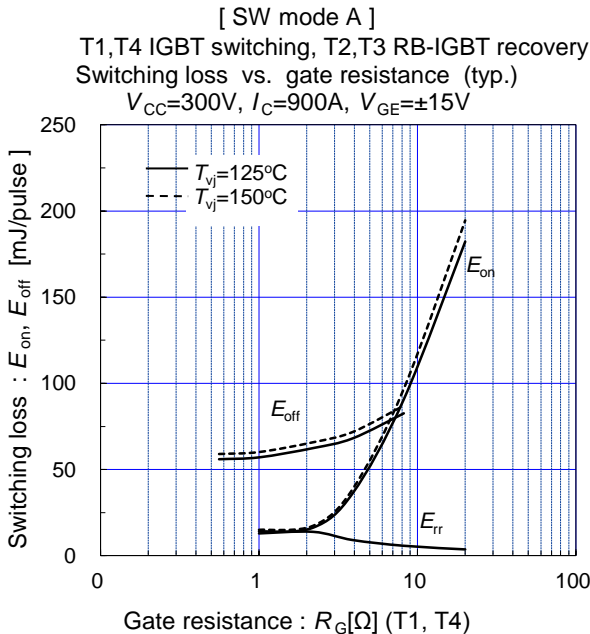
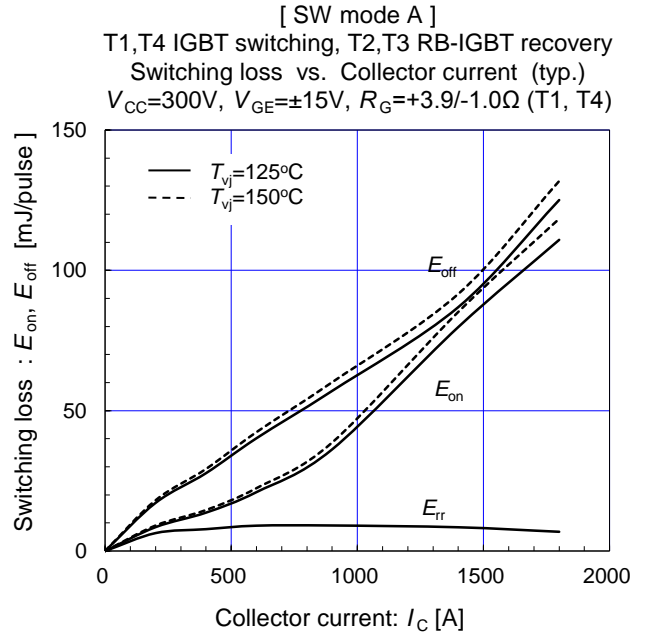
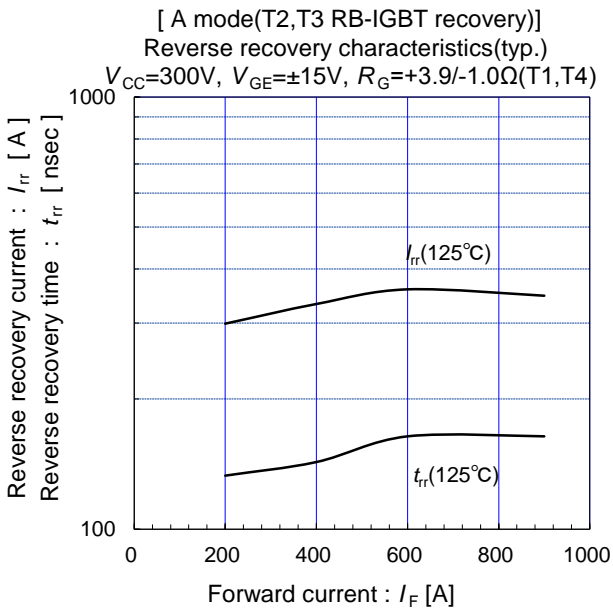
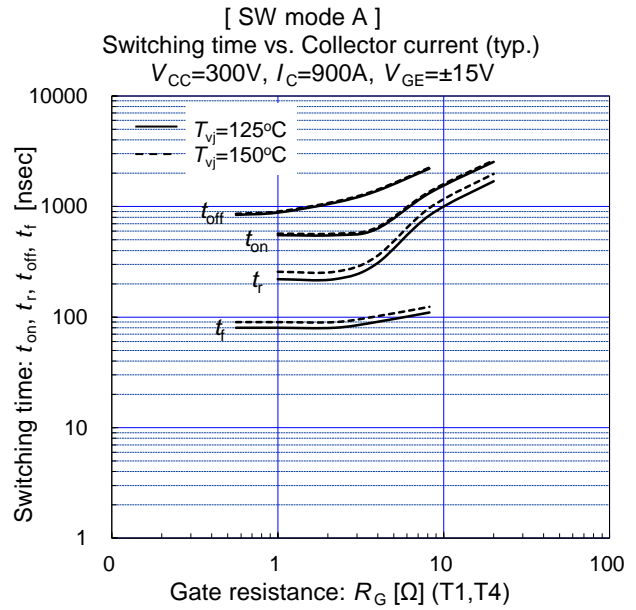
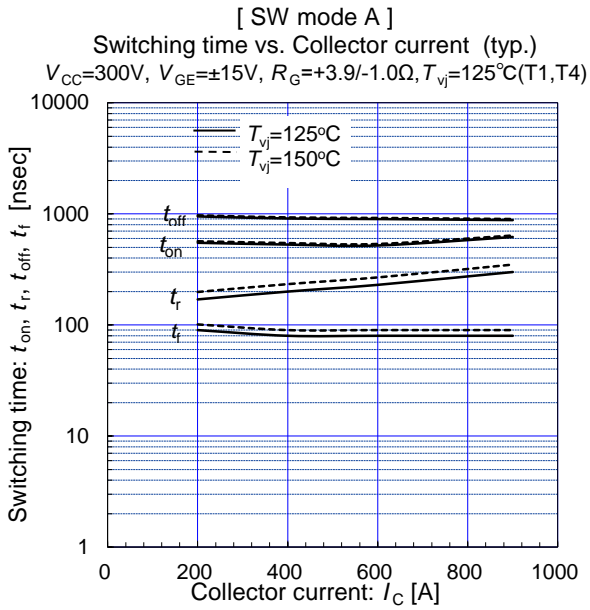
4MBI900VB-120RA-50

IGBT Modules



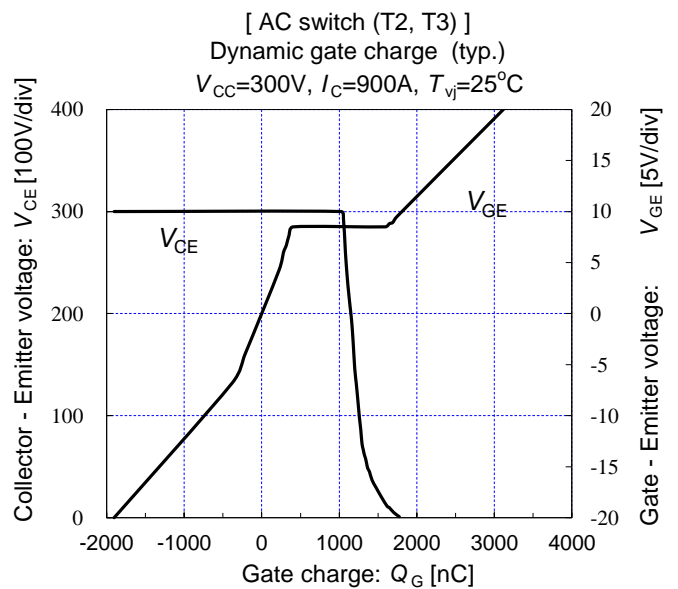
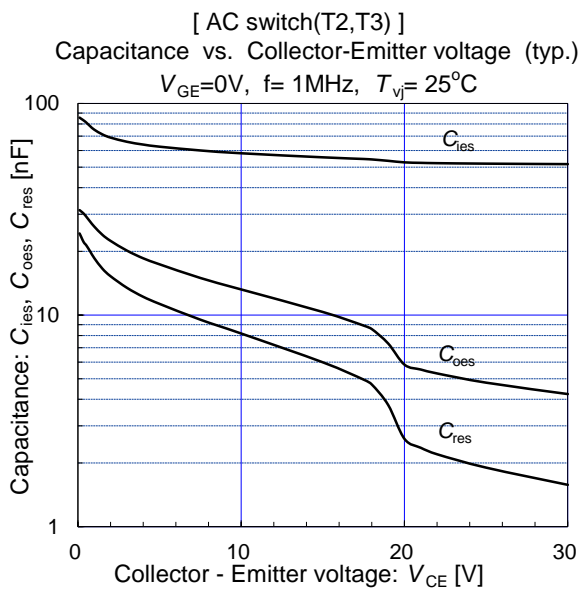
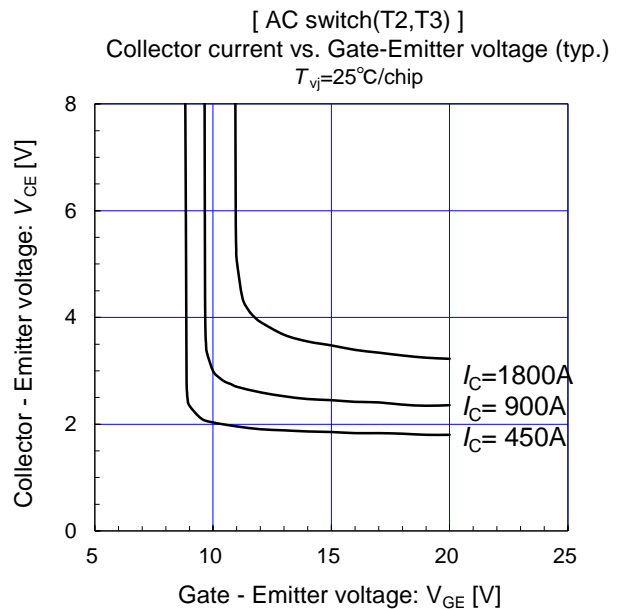
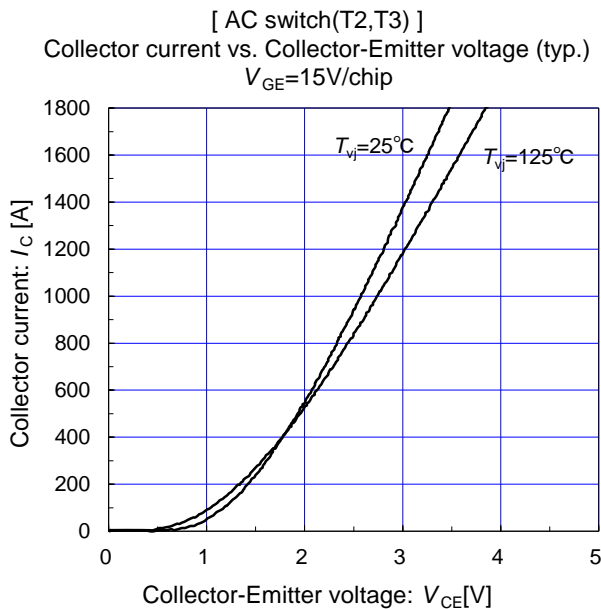
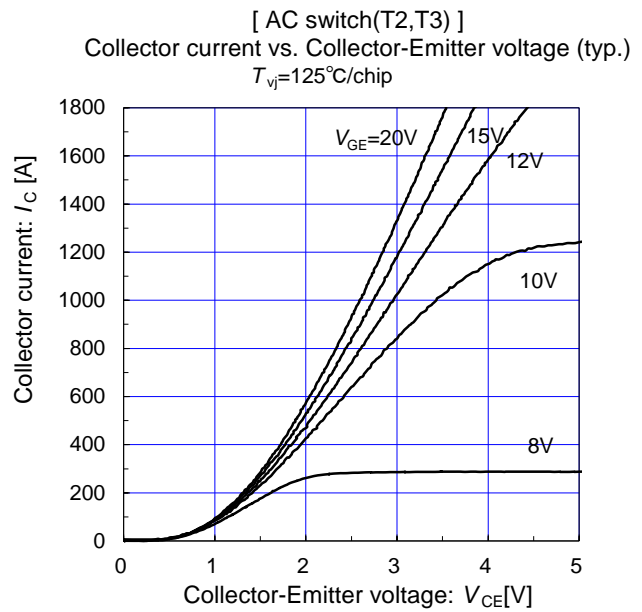
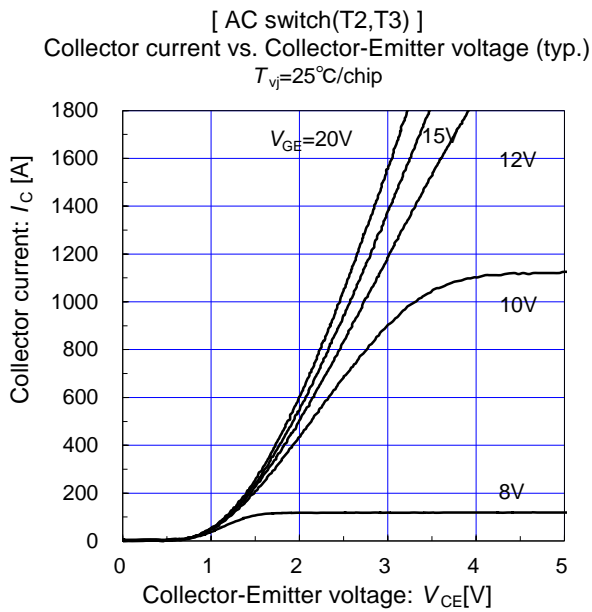
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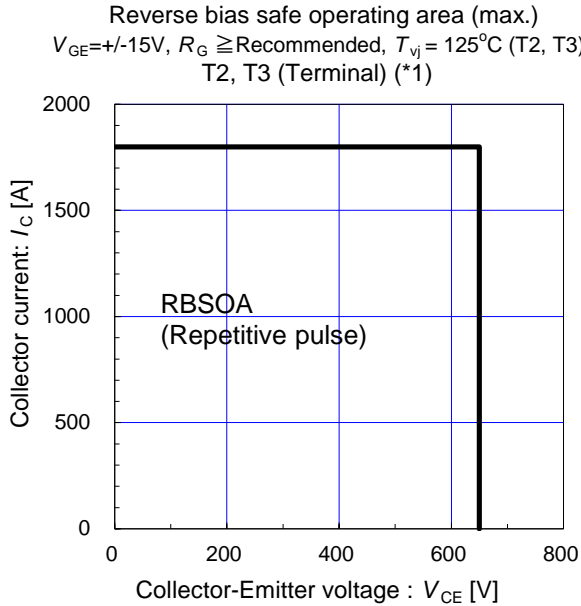
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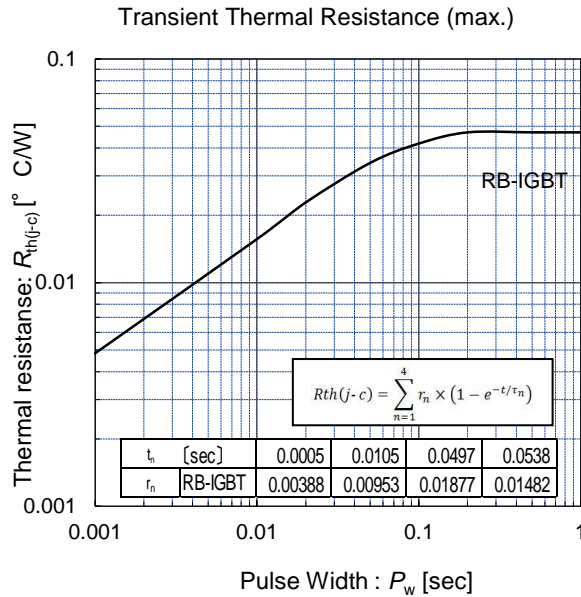
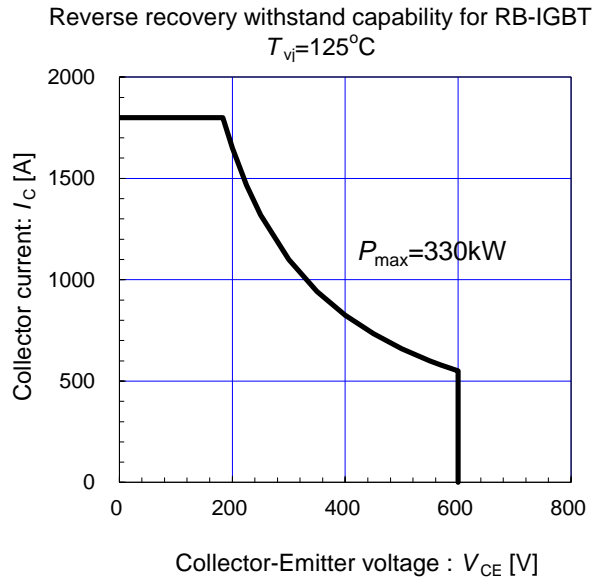


4MBI900VB-120RA-50

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The reverse bias blocking voltage for coupled RB-IGBT can be guaranteed with 650V by applying $V_{GE} = +15V$.
 (*1) Please refer to P4 which has definition of terminal.



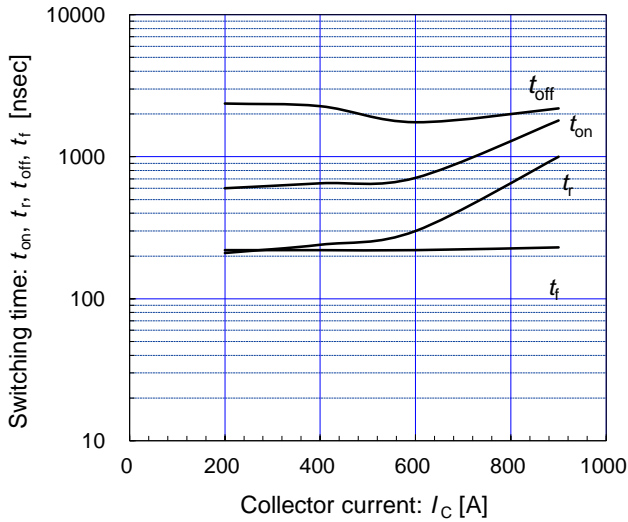
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IGBT Modules

[SW mode B]

Switching time vs. Collector current (typ.)

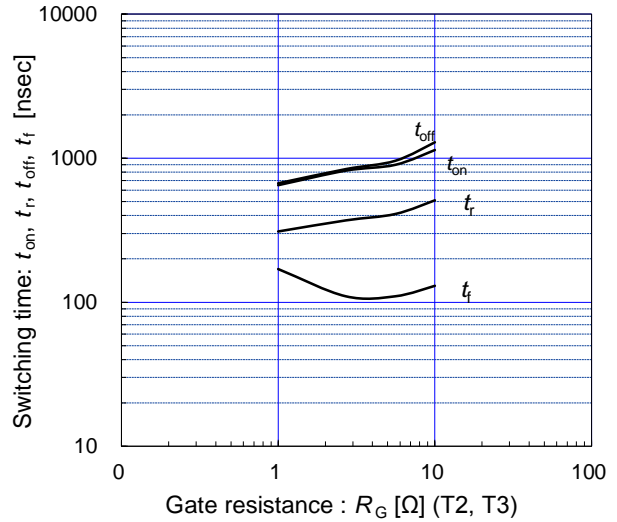
$V_{CC}=300V, V_{GE}=\pm 15V, R_G=+2.7/-15\Omega, T_{vj}=125^\circ C(T2,T3)$



[SW mode B]

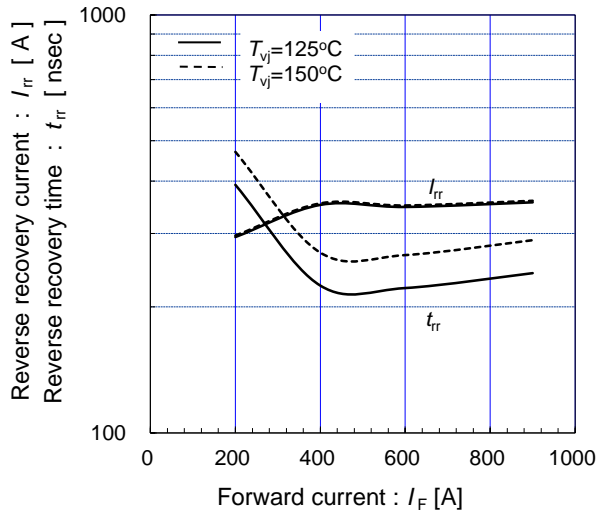
Switching time vs. Collector current (typ.)

$V_{CC}=300V, I_C=900A, V_{GE}=\pm 15V, T_{vj}=125^\circ C$



[B mode(T1,T4 FWD reverse recovery)]
Reverse recovery characteristics(typ.)

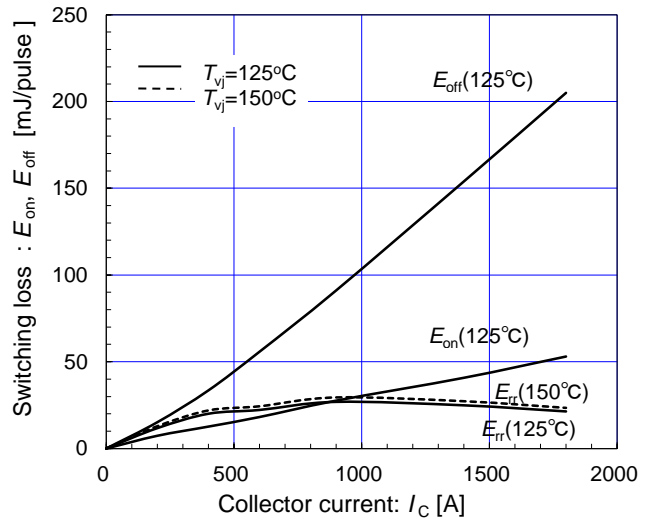
$V_{CC}=300V, V_{GE}=\pm 15V, R_G=+2.7/-15\Omega(T2,T3)$



[SW mode B]

T2,T3 RB- IGBT switching, T1,T4 FWD recovery
Switching loss vs. Collector current (typ.)

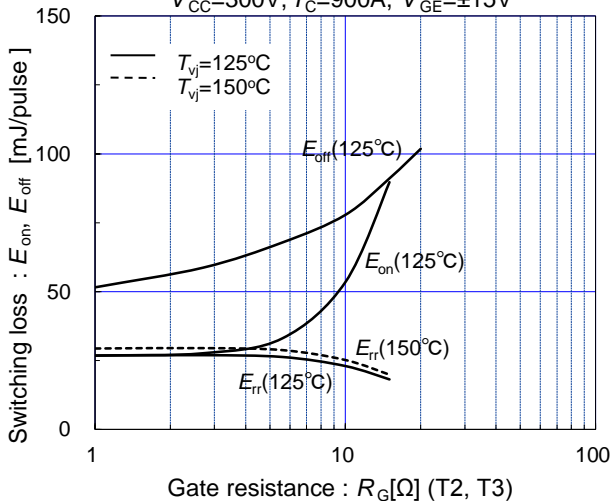
$V_{CC}=300V, V_{GE}=\pm 15V, R_G=+2.7/-15\Omega(T2, T3)$



[SW mode B]

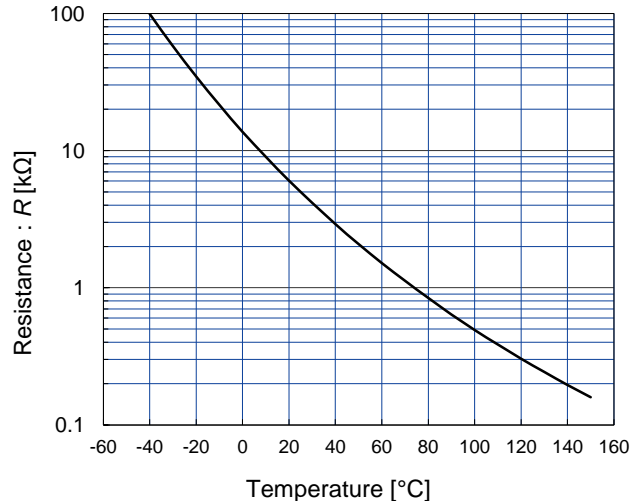
T2,T3 RB-IGBT IGBT switching, T1,T4 FWD recovery
Switching loss vs. gate resistance (typ.)

$V_{CC}=300V, I_C=900A, V_{GE}=\pm 15V$



[THERMISTOR]

Temperature characteristic (typ.)



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IGBT Modules

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