

IGBT Modules

Power Module (X series) 1200V / 300A / 2-in-1 package

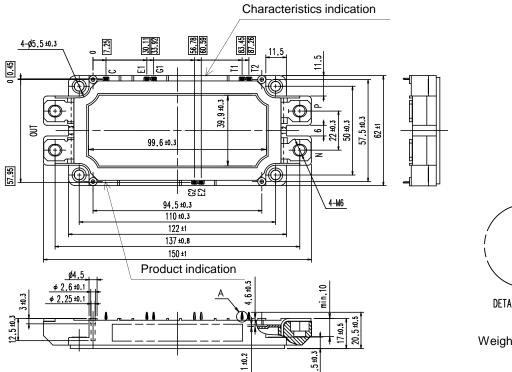
■ Features

Low $V_{\rm CE(sat)}$ Low Inductance Module structure Press fit pin terminals

■ Applications

Inverter for Motor Drives, AC and DC Servo Drives
Uninterruptible Power Supply Systems, Wind Turbines, PV Power Conditioning Systems

■ Outline drawing (Unit:mm)

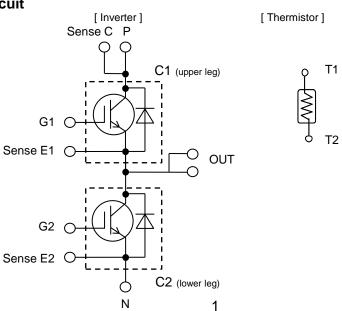




DETAIL A (NTS)

Weight: 350 g(typ.)

NOTE) \longrightarrow shows theoretical dimension and tolerance is \bigcirc \bigcirc \bigcirc Equivalent Circuit





IGBT Modules

■ Absolute Maximum Ratings (at T_C= 25°C unless otherwise specified)

Items			Symbols	Conditions		Maximum Ratings	Units	
	Collecto	ollector-emitter voltage, gate-emitter short-circuited				1200	V	
	Gate-em	nitter voltage, collector-emitter short-circuited	V_{GES}			±20	V	
	Collecto	r current	I _C	Continuous	T _C =100°C	300		
L	Repetitive peak collector current		I _{CRM}	1ms	•	600	A	
<u>و</u>	Forward current		I _F			300		
Inve	Repetitiv	ve peak forward current	/ _{FRM}	1ms		600		
-	Total power dissipation		P _{tot}	1 device		1325	W	
	Virtual junction temperature		$T_{\rm vj}$			175		
	Operating junction temperature		$T_{\rm vjop}$			175		
	(under switching conditions)					175	°C	
Ca	Case temperature		T _c			125		
Storage temperature		$T_{\rm stg}$			-40 ~ 125			
Isolation between terminal and copper base (*1)		1/	AC: 1min.		2500	Vrms		
voltage between thermistor and others (*2)		V_{isol}	AC. IMIN.		2500	VIIIS		
Мо	Mounting torque of screws to heatsink (*3)		Ms	M5		6.0	N·m	
Mounting torque of screws to terminals (*3)		$M_{\rm t}$	M6		6.0			

^(*1) All terminals should be connected together during the test.

(*3) Recommendable Value: : Mounting torque of screws to heatsink Recommendable Value: : Mounting torque of screws to terminals $2.5 \sim 6.0 \text{ N} \cdot \text{m}$ (M5) $3.5 \sim 6.0 \text{ N} \cdot \text{m}$ (M6)

^(*2) Two thermistor terminals should be connected together, other terminals should be connected together and shorted to base plate during the test.

IGBT Modules

■ Electrical characteristics (at T_{vj} = 25°C unless otherwise specified)

	Itomo	Symbols	Conditio	Characteristics			Units	
	Items	Syllibols	Conditio	min.	typ.	max.	Ullits	
	Collector-emitter cut-off current, gate-emitter short-circuited	I _{CES}	$V_{GE} = 0V$ $V_{CE} = 1200V$		-	-	150	μА
	Gate leakage current, collector-emitter short-circuited	I _{GES}	V _{CE} =0V, V _{GE} =±20V		-	-	300	nA
	Gate-Emitter threshold voltage	$V_{\rm GE(th)}$	$V_{CE} = 20V$ $I_{C} = 300 \text{mA}$		6.0	6.5	7.0	V
		$V_{CE(sat)}$ (terminal)		T _{vj} =25°C	-	1.75	2.20	
	Collector-Emitter		V _{GE} = 15V	T _{vj} =25°C	-	1.40	1.85	-
	saturation voltage	$V_{CE(sat)}$	I _C = 300A	T _{vj} =125°C	-	1.70	-	V
		(chip)		T _{vj} =150°C	-	1.80	-	
		(51.11)		T _{vi} =175°C	-	1.85	-	1
	Internal gate resistance	$r_{\rm g}$	-	V)	-	3.00	-	Ω
	<u> </u>	C _{ies}			-	35	-	
	Capacitance	Coes	$V_{\text{CE}}=10\text{V}, V_{\text{GE}}=0$	-	1.2	-	nF	
		C _{res}			-	0.31		-
	Gate charge	Q _G	$V_{\rm CC} = 600 \text{V}, I_{\rm C} = 300 \text{A}$ $V_{\rm GE} = -15 \rightarrow +15 \text{V}$		-	2.2	-	μC
ter	Forward voltage	V _F (terminal)	$V_{GE} = 0V$ $I_F = 300A$	T _{vj} =25°C	-	1.95	2.40	
\ Ve			T _{vi} =25°C		-	1.60	2.05	-
느		V_{F}		T _{vi} =125°C	-	1.65	-	- V
		(chip)		T _{vi} =150°C	-	1.60	-	1
				T _{vi} =175°C	-	1.60	-	
			V _{CC} = 600V	T _{vj} =25°C	-	0.26	-	
	Switching time (*1)	t _{d(on)}	$I_{\rm C}, I_{\rm F} = 300A$	T _{vj} =125°C	-	0.28	-	
			$V_{GE} = +15/-15 \text{ V}$		-	0.32	-	
			$R_{\rm G} = \pm 1\Omega$	T _{vi} =175°C	-	0.34	-	
		t _r	$L_{\rm S} = 35 \rm nH$	T _{vi} =25°C	-	0.08	-	
				T _{vj} =125°C	-	0.09	-	
				T _{vj} =150°C	-	0.09	-	
				T _{vj} =175°C	-	0.09	-	
		$t_{\sf d(off)}$		T _{vj} =25°C	-	0.36	-	
				T _{vj} =125°C	-	0.39	-	μs
				T _{vj} =150°C	-	0.42	-	
			_	$T_{vj} = 175^{\circ}C$	-	0.42	-	_
				T_{vj} =25°C T_{vi} =125°C	-	0.05	-	-
		t_{f}		$T_{vj} = 125^{\circ} \text{C}$ $T_{vj} = 150^{\circ} \text{C}$	-	0.08	-	\dashv
				$T_{vj} = 175^{\circ} \text{C}$	-	0.08	-	-
			1	$T_{\text{vi}}=25^{\circ}\text{C}$	-	0.13	-	1
	Reverse recovery time	<i>+</i>		T _{vi} =125°C	-	0.26	-	1
	IVevelse recovery mile	t _{rr}		T _{vi} =150°C	-	0.31	-	
	Turn on time (t) t	4 Turn off t		T _{vj} =175°C	-	0.35	-	

^(*1) Turn on time $(t_{on}) = t_{d(on)} + t_{r}$, Turn off time $(t_{off}) = t_{d(off)} + t_{f}$

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Items		Symbols		Conditions		Characteristics			Units
		Symbols				min.	typ.	max.	Ullits
Inverter		E _{on}		= 600V	$T_{\rm vj}$ =25°C	-	24.1	-	
	Switching loss (per pulse)				T _{vj} =125°C	-	37.7	-	
					T _{vj} =150°C	-	42.8	-	
				±1Ω	<i>T</i> _{vj} =175°C	-	45.0	-	
		E _{off}	$L_{S} =$	35 nH	$T_{\rm vj}$ =25°C	-	22.9	-	
					T _{vj} =125°C	-	28.5	-	
					T _{vj} =150°C	-	31.5	-	mJ
					T _{vj} =175°C	-	32.2	-	
					T _{vj} =25°C	-	10.6	-	
					T _{vj} =125°C	-	18.8	-	
					T _{vj} =150°C	-	21.1	-	
					T _{vj} =175°C	-	22.6	-	
Thermistor	Resistance	R	T =	25°C		-	5000	-	Ω
	Toolotarioo		<i>T</i> =	100°C		465	495	520	32
Therr	B value	В	T =	25/ 50°C		3305	3375	3450	К

NOTICE:

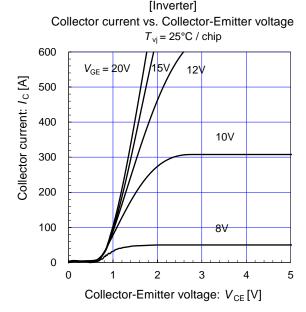
The external gate resistance ($R_{\rm G}$) shown above is one of our recommended value for the purpose of minimum switching loss. However the optimum $R_{\rm G}$ depends on circuit configuration and/or environment. We recommend that the $R_{\rm G}$ has to be carefully chosen based on consideration if IGBT module matches design criteria, for example, switching loss, EMC/EMI, spike voltage, surge current and no unexpected oscillation and so on.

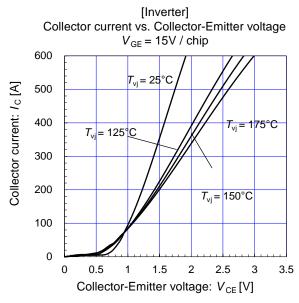
■Thermal resistance characteristics

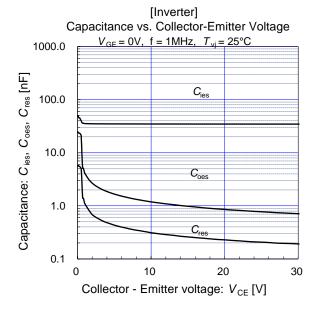
Items	Symbols	Conditions	Ch	Units			
items		Conditions	min.	typ.	max.	Units	
Thermal resistance junction to	R 4h/: ~\	Inverter IGBT	-	-	0.113		
case(1 device)		Inverter FWD	-	-	0.160	K/W	
Thermal resistance case to	$R_{\rm th(c-s)}$	with 1 W/(m·K) thermal grease	_	0.0167	-	1 10 00	
heatsink(1 IGBT+1 FWD) (*1)	· · (II(C-S)	mar r vy (m rt) thornal groups		0.0.0			

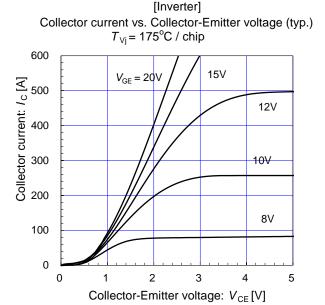
^(*1) This is the value which is defined mounting on the additional heatsink with thermal grease.

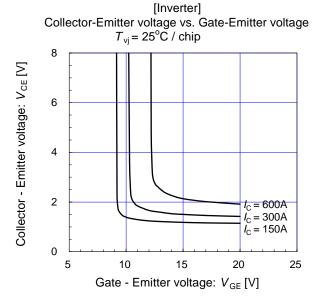


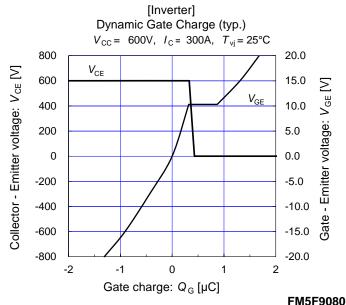


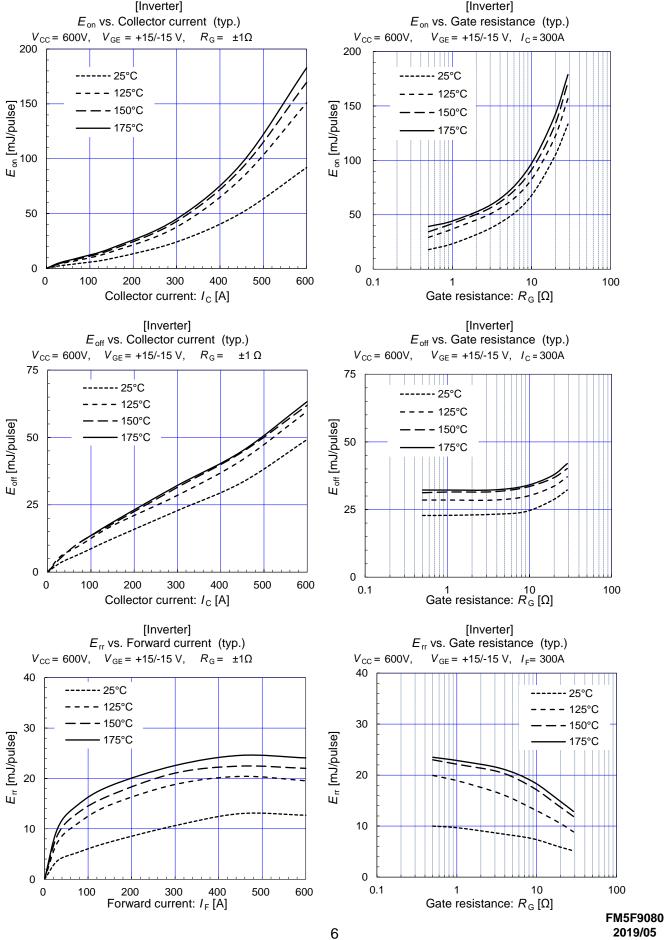


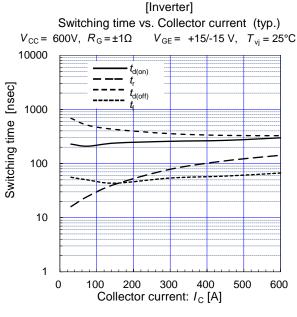


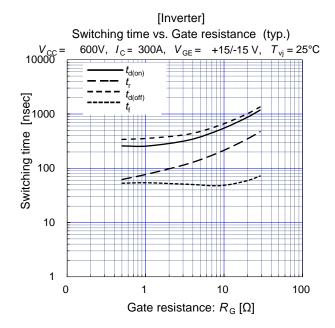


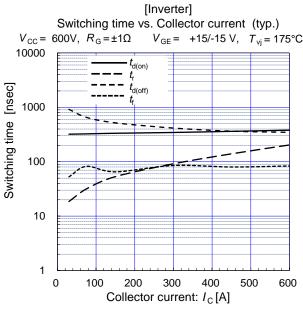


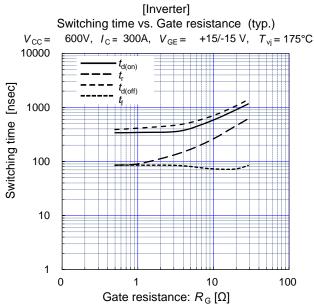


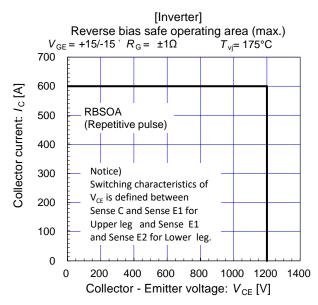


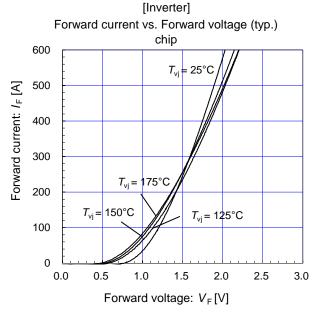


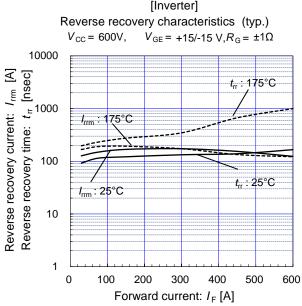


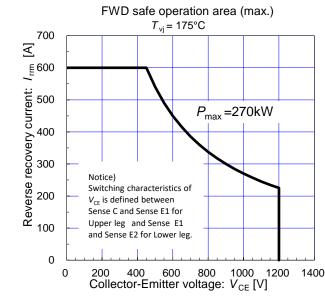


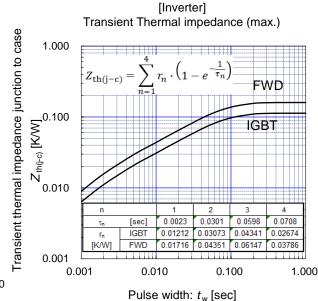


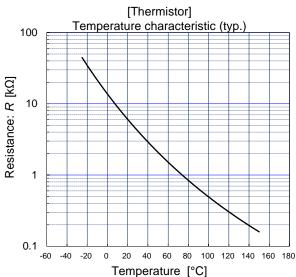












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