

2MBI1000XRNF120-50

IGBT Modules

Power Module (X series)
1200V / 1000A / 2-in-1 package

■ Features

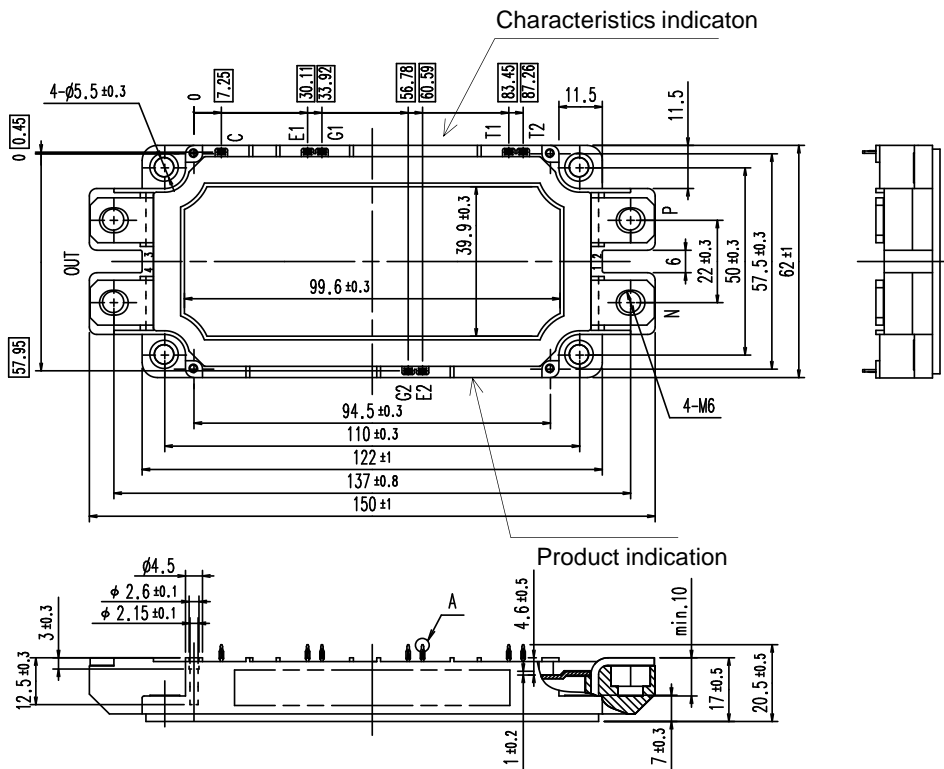
- Low $V_{CE(sat)}$
- Low Inductance Module structure
- Solder pin terminals

■ Applications

- Inverter for Motor Drives, AC and DC Servo Drives
- Uninterruptible Power Supply Systems, Wind Turbines, PV Power Conditioning Systems



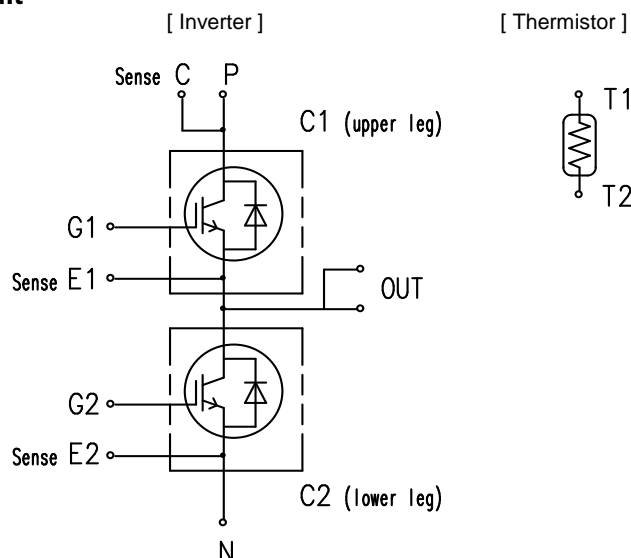
■ Outline drawing (Unit : mm)



NOTE) shows theoretical dimension and tolerance is ± 0.5

Weight: 350 g(typ.)

■ Equivalent Circuit



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■ Absolute Maximum Ratings (at $T_c = 25^\circ\text{C}$ unless otherwise specified)

Items		Symbols	Conditions	Maximum Ratings	Units
Inverter	Collector-emitter voltage, gate-emitter short-circuited	V_{CES}		1200	V
	Gate-emitter voltage, collector-emitter short-circuited	V_{GES}		± 20	V
	Collector current	I_C	Continuous $T_c = 100^\circ\text{C}$	1000	A
	Repetitive peak collector current	I_{CRM}	1ms	2000	
	Reverse-conducting current	I_{RC}		1000	
	Repetitive peak reverse-conducting current	I_{RCRM}	1ms	2000	
	Total power dissipation	P_{tot}	1 device	8330	W
	Virtual junction temperature	T_{vj}		175	$^\circ\text{C}$
	Operating virtual junction temperature (under switching conditions)	T_{vjop}		175	
	Case temperature	T_c		150	
Storage temperature		T_{stg}		-40 ~ 150	
Isolation voltage	between terminals and copper base (*1) between thermistor and others (*2)	V_{isol}	AC: 1min.	4000	Vrms
Mounting torque for screws to heatsink (*3)		M_s	M5	6.0	N·m
Mounting torque for terminal screws (*3)		M_t	M6	6.0	

(*1) All terminals should be connected together during the test.

(*2) Two thermistor terminals should be connected together, other terminals should be connected together

(*3) Recommendable Value: : Mounting torque of screws to heatsink 2.5 ~ 6.0 N·m (M5)
Recommendable Value: : Mounting torque of screws to terminals 3.5 ~ 6.0 N·m (M6)

■ Electrical characteristics (at $T_{vj} = 25^\circ\text{C}$ unless otherwise specified)

Items		Symbols	Conditions		Characteristics			Units
					min.	typ.	max.	
Inverter	Collector-emitter cut-off current, gate-emitter short-circuited	I_{CES}	$V_{GE} = 0V$ $V_{CE} = 1200V$		-	-	200	μA
	Gate leakage current, collector-emitter short-circuited	I_{GES}	$V_{CE}=0V, V_{GE}=\pm 20V$		-	-	400	nA
	Gate-emitter threshold voltage	$V_{GE(th)}$	$V_{CE} = 20V$ $I_C = 1000mA$		5.8	6.4	7.0	V
	Collector-emitter saturation voltage	$V_{CE(sat)}$ (terminal)	$V_{GE} = 15V$ $I_C = 1000A$	$T_{vj}=25^{\circ}C$	-	2.75	3.30	V
		$V_{CE(sat)}$ (chip)		$T_{vj}=25^{\circ}C$	-	1.55	2.00	
				$T_{vj}=125^{\circ}C$	-	1.85	-	
				$T_{vj}=150^{\circ}C$	-	1.95	-	
				$T_{vj}=175^{\circ}C$	-	2.00	-	
	Internal gate resistance	r_g	-		-	0.95	-	Ω
	Input capacitance	C_{ies}	$V_{CE}=10V, V_{GE}=0V, f=1MHz$		-	126	-	nF
	Output capacitance	C_{oes}			-	5.3	-	
	Reverse transfer capacitance	C_{res}			-	1.19	-	
	Gate charge	Q_G	$V_{CC} = 600V, I_C = 1000A$ $V_{GE} = -15 \rightarrow +15V$		-	7.8	-	μC
	Reverse-conducting voltage	V_{RC} (terminal)	$V_{GE} = 0V$ $I_{RC} = 1000A$	$T_{vj}=25^{\circ}C$	-	2.80	3.30	V
		V_{RC} (chip)		$T_{vj}=25^{\circ}C$	-	1.60	2.05	
				$T_{vj}=125^{\circ}C$	-	1.75	-	
				$T_{vj}=150^{\circ}C$	-	1.75	-	
				$T_{vj}=175^{\circ}C$	-	1.75	-	
	Turn-on delay time (*1)	$t_{d(on)}$	$V_{CC} = 600V$ $I_C, I_F = 1000A$ $V_{GE} = +15V / -15V$ $R_G = 0.5\Omega$ $L_S = 35\text{ nH}$	$T_{vj}=25^{\circ}C$	-	0.42	-	μs
				$T_{vj}=125^{\circ}C$	-	0.43	-	
$T_{vj}=150^{\circ}C$				-	0.43	-		
$T_{vj}=175^{\circ}C$				-	0.43	-		
Rise time	t_r	$T_{vj}=25^{\circ}C$		-	0.10	-		
		$T_{vj}=125^{\circ}C$		-	0.11	-		
		$T_{vj}=150^{\circ}C$		-	0.11	-		
		$T_{vj}=175^{\circ}C$		-	0.12	-		
Turn-off delay time (*2)	$t_{d(off)}$	$T_{vj}=25^{\circ}C$		-	0.54	-		
		$T_{vj}=125^{\circ}C$		-	0.55	-		
		$T_{vj}=150^{\circ}C$		-	0.56	-		
		$T_{vj}=175^{\circ}C$		-	0.56	-		
Fall time	t_f	$T_{vj}=25^{\circ}C$		-	0.12	-		
		$T_{vj}=125^{\circ}C$		-	0.15	-		
		$T_{vj}=150^{\circ}C$		-	0.15	-		
		$T_{vj}=175^{\circ}C$		-	0.16	-		
Forward recovery time	t_{fr}	$T_{vj}=25^{\circ}C$		-	0.28	-		
		$T_{vj}=125^{\circ}C$		-	0.38	-		
		$T_{vj}=150^{\circ}C$		-	0.41	-		
		$T_{vj}=175^{\circ}C$		-	0.45	-		

(*1) Turn on time (t_{on}) = $t_{d(on)} + t_r$

(*2) Turn off time (t_{off}) = $t_{d(off)} + t_f$

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■ Electrical characteristics (at $T_{vj}=25^{\circ}\text{C}$ unless otherwise specified)

Items		Symbols	Conditions		Characteristics			Units
					min.	typ.	max.	
Inverter	Turn-on energy (per pulse)	E_{on}	$V_{CC} = 600V$	$T_{vj}=25^{\circ}C$	-	75.7	-	mJ
			$I_C, I_F = 1000A$	$T_{vj}=125^{\circ}C$	-	98.9	-	
			$V_{GE} = +15V / -15V$	$T_{vj}=150^{\circ}C$	-	103.5	-	
			$R_G = 0.5\Omega$	$T_{vj}=175^{\circ}C$	-	110.5	-	
	Turn-off energy (per pulse)	E_{off}	$L_S = 35\text{ nH}$	$T_{vj}=25^{\circ}C$	-	106.6	-	
			$T_{vj}=125^{\circ}C$	-	117.6	-		
			$T_{vj}=150^{\circ}C$	-	125.3	-		
			$T_{vj}=175^{\circ}C$	-	134.1	-		
	Forward recovery energy (per pulse)	E_{fr}		$T_{vj}=25^{\circ}C$	-	93.5	-	
				$T_{vj}=125^{\circ}C$	-	124.7	-	
				$T_{vj}=150^{\circ}C$	-	137.7	-	
				$T_{vj}=175^{\circ}C$	-	139.0	-	
Thermistor	Resistance	R	$T = 25^{\circ}C$	-	5000	-	Ω	
			$T = 100^{\circ}C$	465	495	520		
	B value	B	$T = 25/ 50^{\circ}C$	3305	3375	3450	K	

NOTICE:

The external gate resistance (R_G) shown above is one of our recommended value for the purpose of minimum switching loss. However the optimum R_G depends on circuit configuration and/or environment. We recommend that the R_G has to be carefully chosen based on consideration if IGBT module matches design criteria, for example, switching loss, EMC/EMI, spike voltage, surge current and no unexpected oscillation and so on.

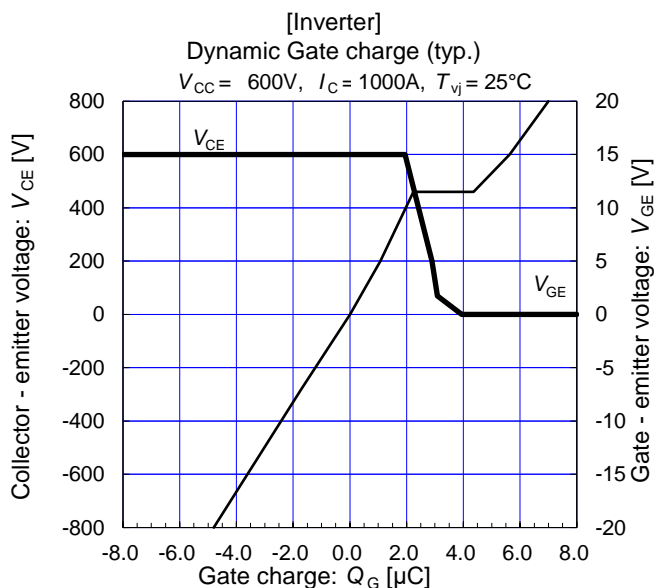
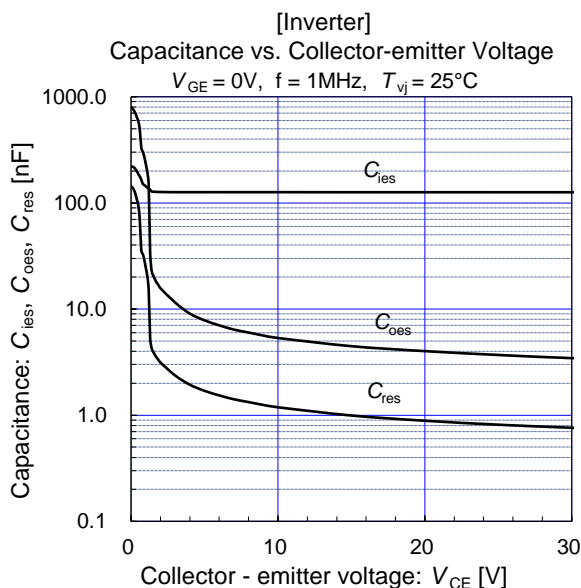
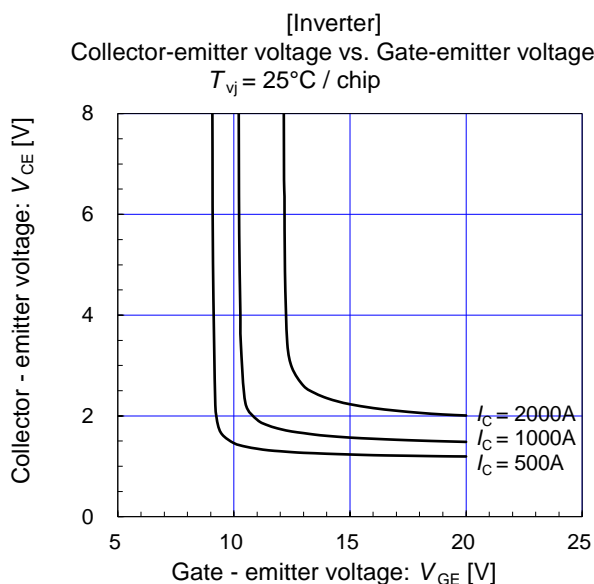
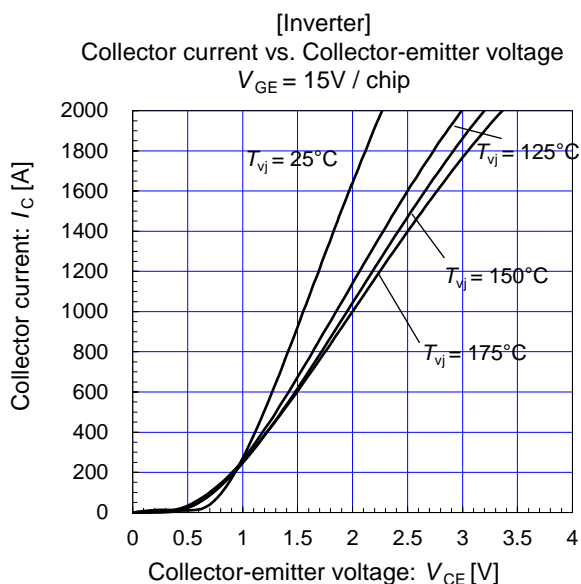
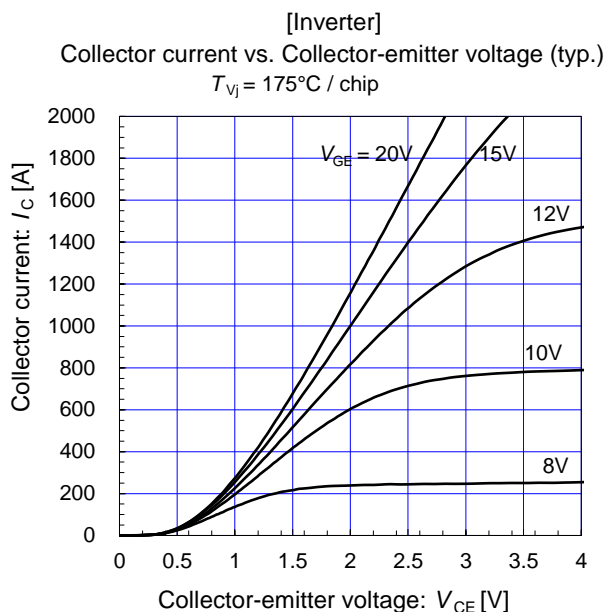
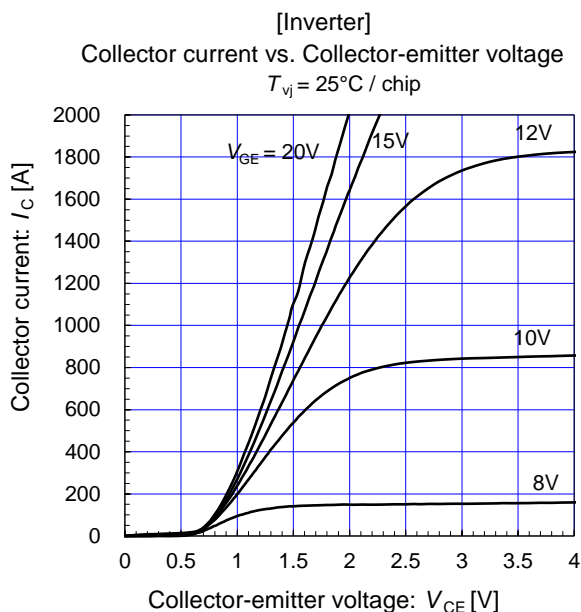
■ Thermal resistance characteristics

Items	Symbols	Conditions	Characteristics			Units
			min.	typ.	max.	
Thermal resistance junction to case(1 device)	$R_{th(j-c)}$	Inverter RC-IGBT	-	-	0.018	K/W
Thermal resistance case to heatsink (1 device) (*1)	$R_{th(c-s)}$	with 1 W/(m·K) thermal grease	-	0.0125	-	

(*1) This is the value which is defined mounting on the additional hestsink with thermal grease.

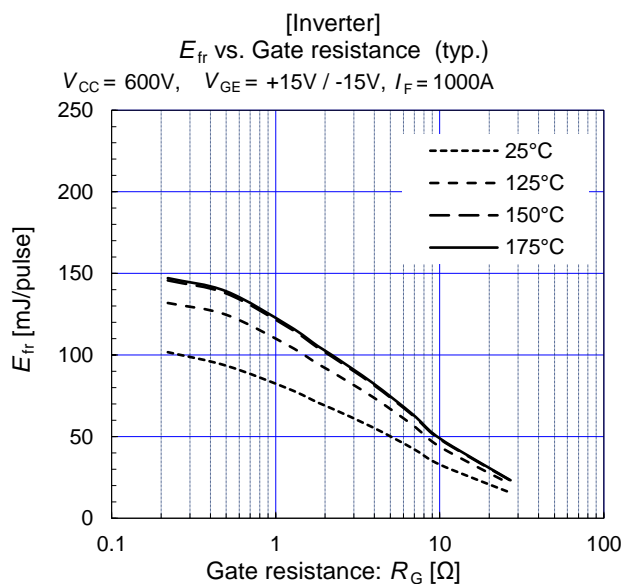
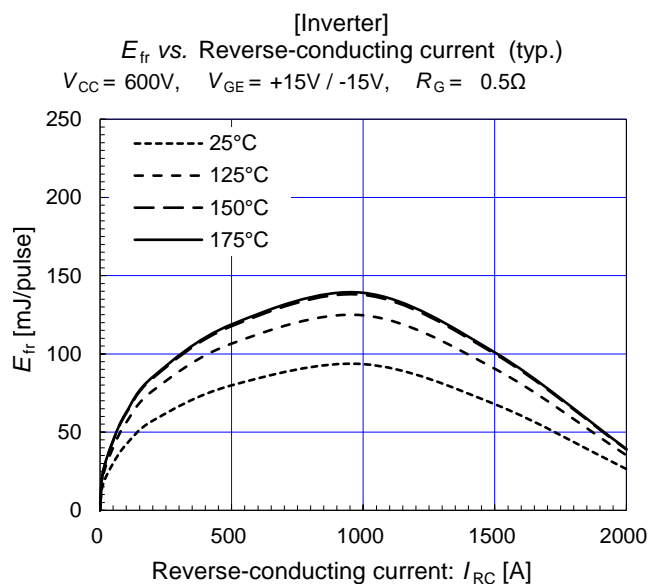
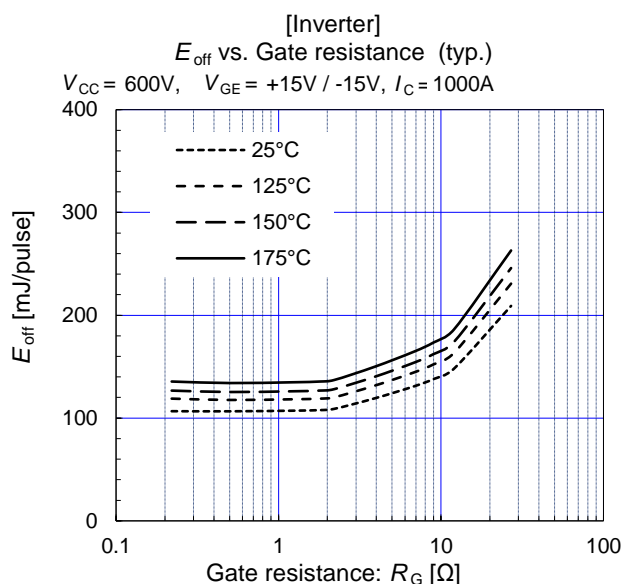
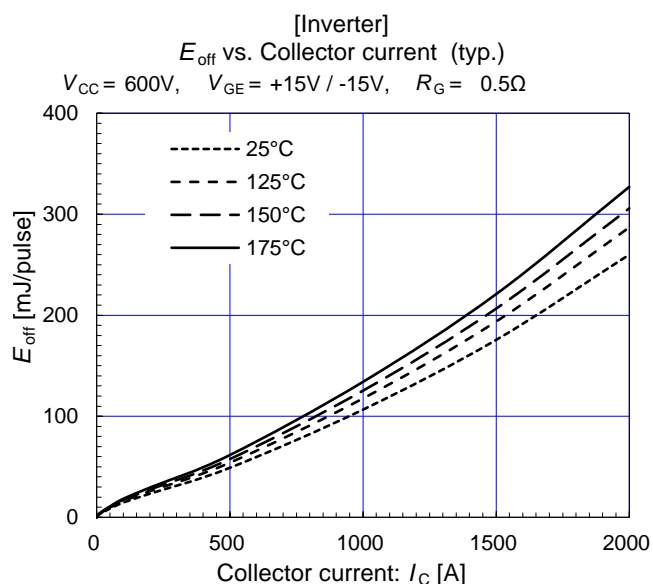
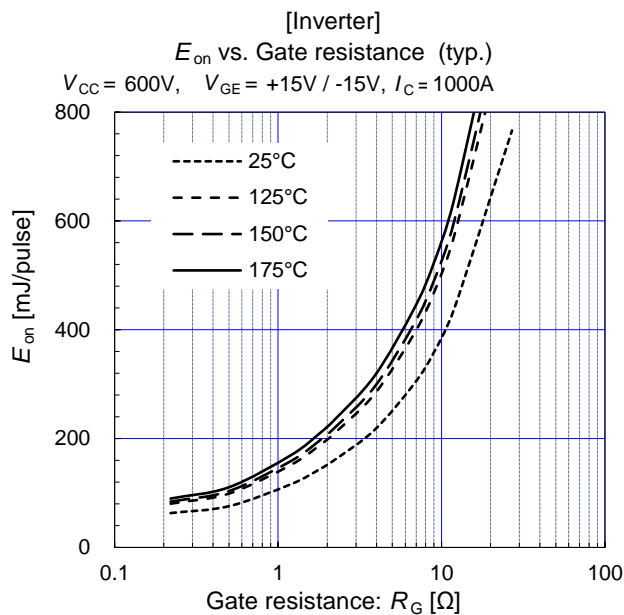
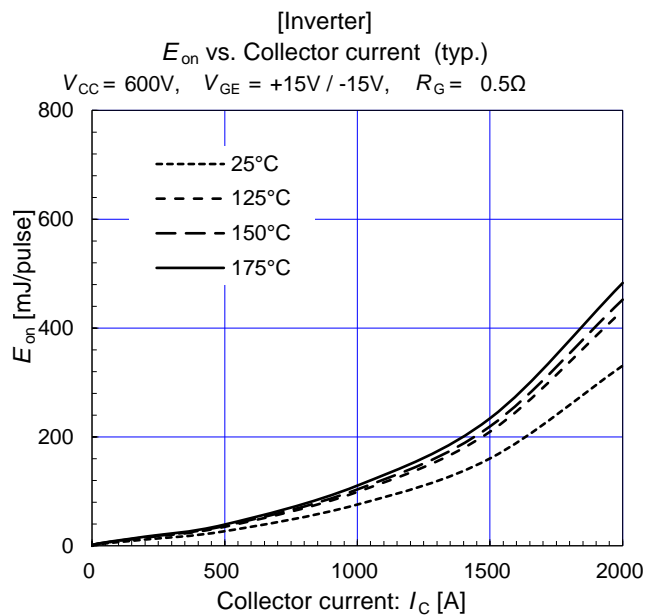
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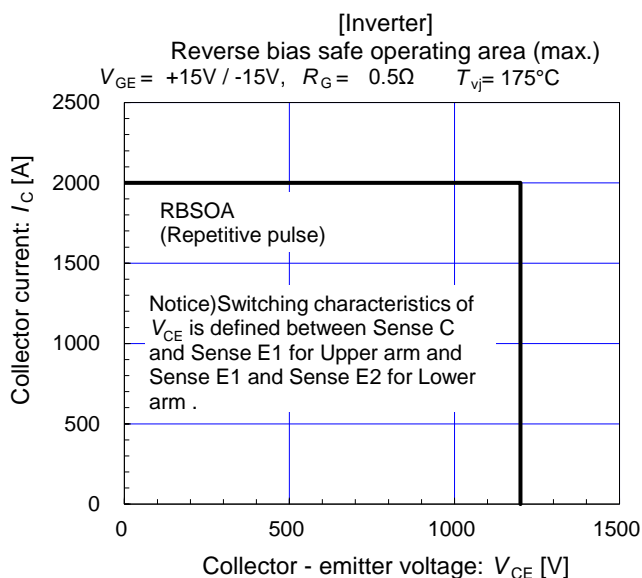
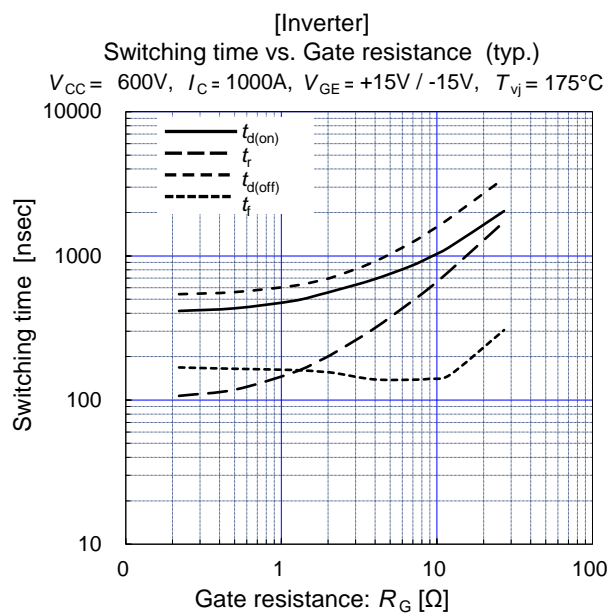
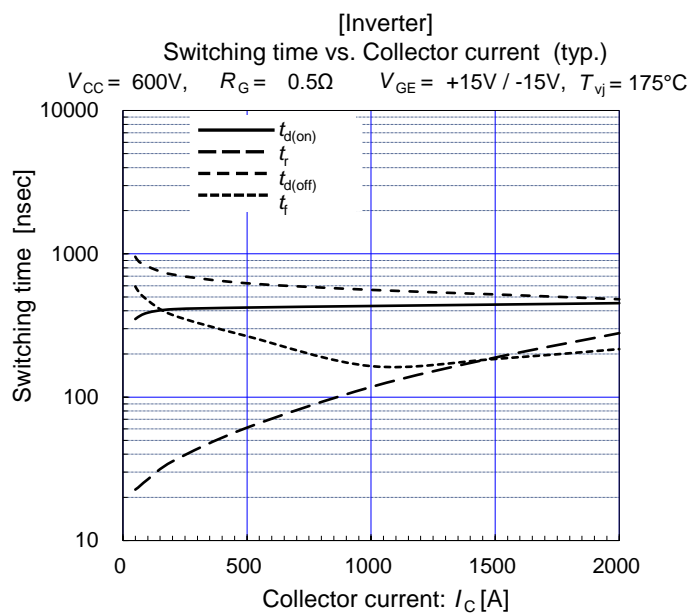
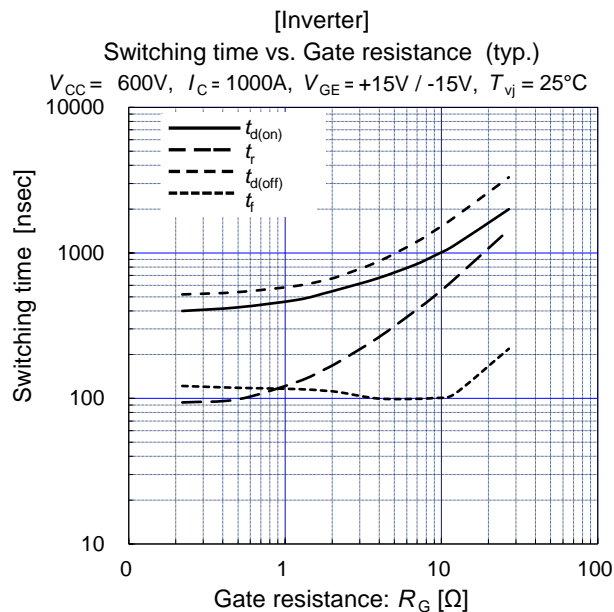
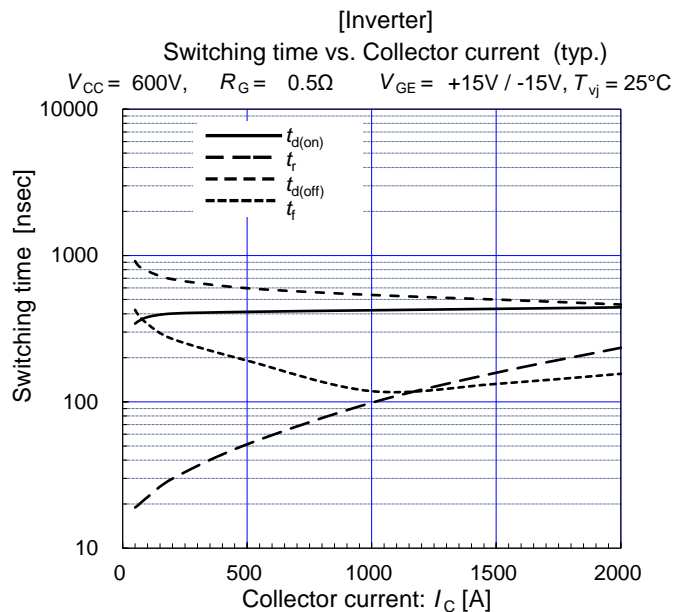
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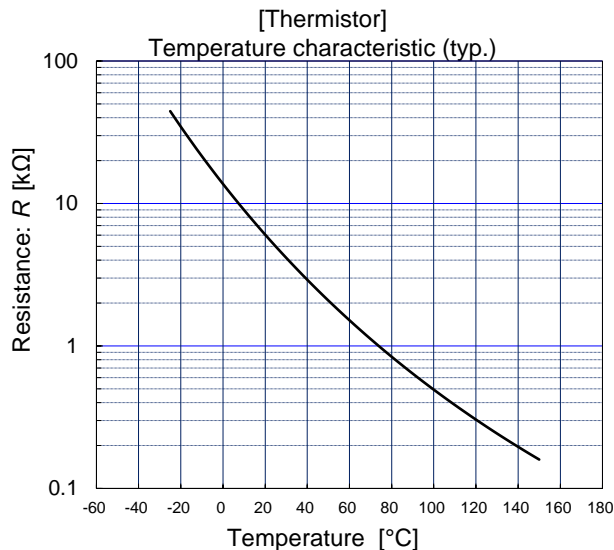
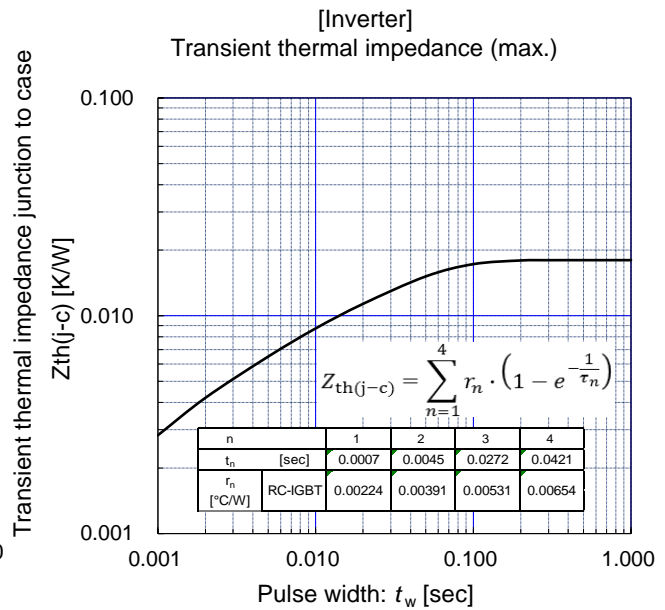
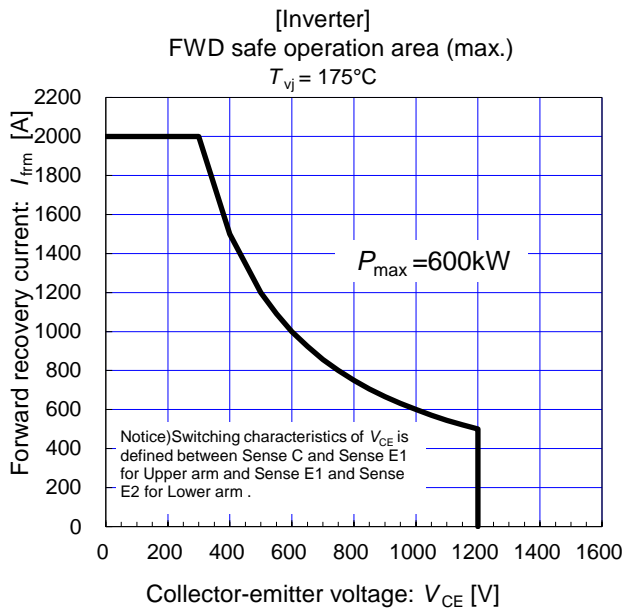
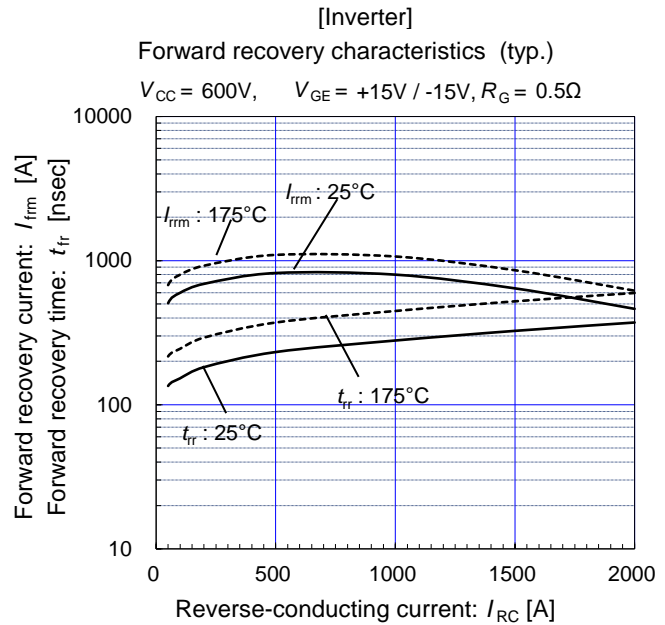
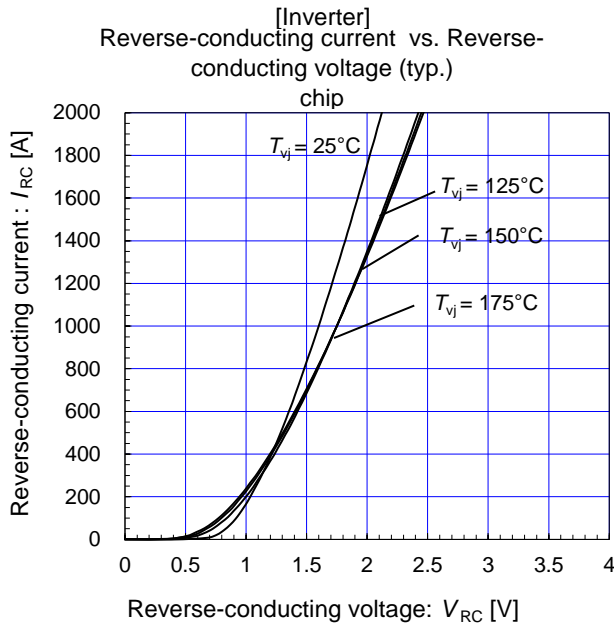
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