

**IGBT Modules** 

# Power Module (X series) 650V / 200A / 2-in-1 package

#### ■ Features

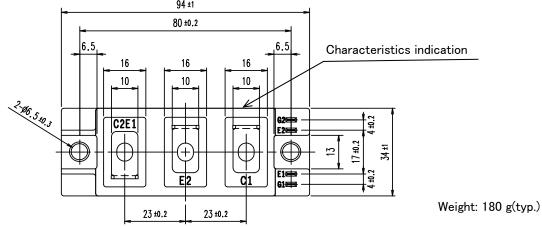
Low  $V_{\rm CE(sat)}$ High speed switching Low Inductance Module structure

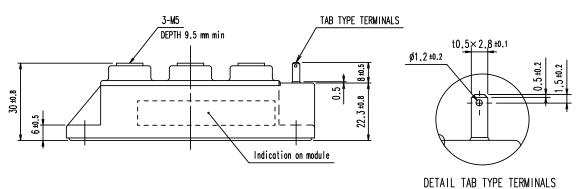
#### ■ Applications

Inverter for Motor Drives, AC and DC Servo Drives Uniterruptible Power Supply Systems, Industrial machines, such as Welding machines

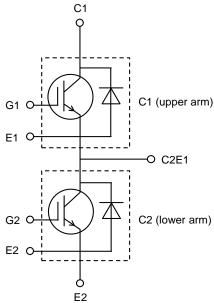


### ■ Outline drawing (Unit:mm)





#### **■** Equivalent Circuit



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### ■ Absolute Maximum Ratings (at T<sub>C</sub>= 25°C unless otherwise specified)

		Items	Symbols	Conc	litions	Maximum Ratings	Units
Collector-emitter voltage,gate-emitter short-circuited			V <sub>CES</sub>			650	V
	Gate-en	nitter voltage,collector-emitter short-	$V_{GES}$			±20	V
<u>_</u>	Collecto	r current	I <sub>C</sub>	Continuous	T <sub>C</sub> =100°C	200	
Inverter	Repetitive peak collector current		I <sub>CRM</sub>	1ms		400	A
므	Forward current		I <sub>F</sub>			200	- A
	Repetitive peak forward current		I <sub>FRM</sub>	1ms		400	
	Total power dissipation		P <sub>tot</sub>	1 device		625	W
	Virtual junction temperature		T <sub>vj</sub>			175	
	Operating virtual junction temperature		$T_{vjop}$			175	°C
Ca	Case temperature		Tc			125	- 10
Storage temperature		${\cal T}_{ m stg}$			-40 ~ 125		
Isolation (*1) voltage		$V_{isol}$	AC: 1min.		4000	Vrms	
Mounting torque of screws to heatsink(*2)		Ms	M5		5.0	NI mr	
Mounting torque of screws to terminals(*3)		$M_{\rm t}$	M5		5.0	- N·m	

<sup>(\*1)</sup> All terminals should be connected together during the test.

<sup>(\*2)</sup> Recommendable Value: 3.0 ~ 5.0 N⋅m (M5 or M6)

<sup>(\*3)</sup> Recommendable Value:  $2.5 \sim 5.0 \text{ N} \cdot \text{m}$  (M5)



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### ■ Electrical characteristics (at T<sub>vj</sub>= 25°C unless otherwise specified)

	Itama	Combala Conditions			Characteristics			Units
	Items	Symbols	Conditions		min.	typ.	max.	Units
	Collector-emitter cut-off current,gate-emitter short-circuited	I <sub>CES</sub>	$V_{GE} = 0V$ $V_{CE} = 650V$		-	-	50	μA
	Gate leakage current,collector-emitter short-circuited	I <sub>GES</sub>	$V_{CE} = 0V, V_{GE} = \pm 20V$		-	-	100	nA
	Gate-emitter threshold voltage	$V_{GE(th)}$	$V_{CE} = 20V$ $I_{C} = 200mA$		6.0	6.5	7.0	V
		V <sub>CE(sat)</sub> (terminal)		T <sub>vj</sub> =25°C	-	1.45	1.90	
	Collector-emitter		V <sub>GE</sub> = 15V	T <sub>vj</sub> =25°C	-	1.30	1.75	
	saturation voltage	$V_{CE(sat)}$	I <sub>C</sub> = 200A	T <sub>vj</sub> =125°C	-	1.45	-	V
		(chip)		T <sub>vi</sub> =150°C	-	1.50	-	
		,		T <sub>vi</sub> =175°C	-	1.55	-	1
	Internal gate resistance	$r_{\rm g}$	-	v)	-	3.00	-	Ω
	Input capacitance	C <sub>ies</sub>			-	23	-	
	Output capacitance	Coes	$V_{CE}=10V, V_{GE}=0V, f=$	=1MHz	-	0.9	-	nF
	Reverse transfer capacitance	C <sub>res</sub>			-	0.31	-	
	Gate charge	Q <sub>G</sub>	$V_{\rm CC} = 300 \text{V}, I_{\rm C} = 200 \text{A}$ $V_{\rm GE} = -15 \rightarrow +15 \text{V}$		-	1.6	-	μC
Inverter		V <sub>F</sub> (terminal)	$V_{GE} = 0V$ $I_F = 200A$	T <sub>vj</sub> =25°C	-	1.70	2.15	
ڪ ا				T <sub>vj</sub> =25°C	-	1.55	2.00	
	Forward voltage	$V_{F}$		T <sub>vj</sub> =125°C	-	1.50	-	V
		(chip)		T <sub>vi</sub> =150°C	-	1.50	-	1
				T <sub>vi</sub> =175°C -	1.45	-	1	
			$V_{CC} = 300V$ $I_{C}, I_{F} = 200A$ $V_{GE} = +15/-15V$	T <sub>vi</sub> =25°C	-	0.31	-	
	T 11 (: (+4)	$t_{\rm d(on)}$		T <sub>vj</sub> =125°C	-	0.33	-	
	Turn-on delay time(*1)			T <sub>vj</sub> =150°C	-	0.34	-	
			$R_{\rm G} = +6.8/-15\Omega$	T <sub>vj</sub> =175°C	-	0.34	-	
			$L_{\rm S} = 30  \rm nH$	$T_{\rm vj}$ =25°C	-	0.10	-	
	Rise time(*1)	<i>‡</i>		T <sub>vj</sub> =125°C	-	0.12	-	
	Noe time( 1)	$t_{r}$		T <sub>vj</sub> =150°C	-	0.12	-	
				T <sub>vj</sub> =175°C	-	0.12	-	
				$T_{\rm vj}$ =25°C	-	0.56	-	
	Turn-off delay time(*1)	$t_{\sf d(off)}$		T <sub>vj</sub> =125°C	-	0.59	-	μs
		• a(off)		$T_{\rm vj} = 150^{\circ} \rm C$	-	0.60	-	4
		t <sub>f</sub>		$T_{\text{vj}}$ =175°C $T_{\text{vj}}$ =25°C	-	0.61	-	_
				$T_{vj} = 25^{\circ} \text{C}$ $T_{vj} = 125^{\circ} \text{C}$	-	0.10 0.15	-	-
	Fall time(*1)			$T_{vj} = 150^{\circ} \text{C}$	-	0.15	-	+
				T <sub>vi</sub> =175°C	-	0.16	-	1
			1	$T_{\rm vi}$ =25°C	-	0.16	-	
	Reverse recovery time	$t_{\rm rr}$		T <sub>vi</sub> =125°C	-	0.26	-	
	Trovorse recovery time	• rr		$T_{\rm vj} = 150^{\circ}{\rm C}$	-	0.29	-	
				T <sub>vj</sub> =175°C	-	0.34	-	

<sup>(\*1)</sup> Turn-on time  $(t_{on}) = t_{d(on)} + t_{r}$ , Turn-off time  $(t_{off}) = t_{d(off)} + t_{f}$ 



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### ■ Electrical characteristics (at T<sub>vj</sub>= 25°C unless otherwise specified)

	Items	Symbols	Conditions		Characteristics			Units
	Items	Symbols			min.	typ.	max.	Ullits
			$V_{\rm CC} = 300 \rm V$	T <sub>vj</sub> =25°C	-	4.5	-	
	Turn-on energy	E <sub>on</sub>	$I_{\rm C}$ , $I_{\rm F} = 200$ A	T <sub>∨j</sub> =125°C	-	7.4	-	
			$V_{GE} = +15/-15V$	T <sub>vj</sub> =150°C	-	8.2	-	
			$R_{\rm G} = +6.8/-15\Omega$	<i>T</i> <sub>∨j</sub> =175°C	-	9.4	-	
	Turn-off energy	E <sub>off</sub>	$L_{\rm S} = 30  \rm nH$	$T_{vj}$ =25°C	-	6.1	-	1
Inverter				T <sub>∨j</sub> =125°C	-	8.2	-	
				T <sub>vj</sub> =150°C	-	8.8	-	mJ
드				T <sub>vj</sub> =175°C	-	8.9	-	
	Reverse recovery energy	Err		T <sub>vj</sub> =25°C	-	0.7	-	
				T <sub>vj</sub> =125°C	-	1.2	-	
				T <sub>vj</sub> =150°C	-	1.4	-	]
				T <sub>vj</sub> =175°C	-	1.7	-	1

#### NOTICE:

The external gate resistance ( $R_{\rm G}$ ) shown above is one of our recommended value for the purpose of minimum switching loss. However the optimum  $R_{\rm G}$  depends on circuit configuration and/or environment. We recommend that the  $R_{\rm G}$  has to be carefully chosen based on consideration if IGBT module matches design criteria, for example, switching loss, EMC/EMI, spike voltage, surge current and no unexpected oscillation and so on.

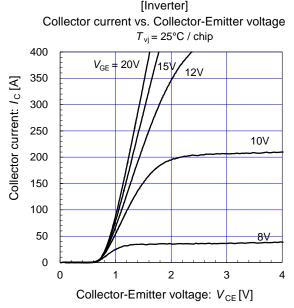
### **■**Thermal resistance characteristics

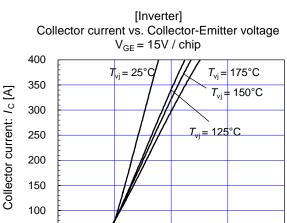
	Symbols	Conditions	Characteristics			no
	Symbols	Conditions	min.	typ.	max.	ns
Thermal resistance junction to case	$R_{ ext{th(j-c)}}$	IGBT	-	-	0.238	K/W
(1device)	th(J-c)	FWD	-	-	0.457	
Thermal resistance case to heatsink (1IGBT + 1FWD) (*1)	R <sub>th(c-s)</sub>	with 1 W/(m·K) thermal grease	-	0.050	-	1000

<sup>(\*1)</sup> This is the value which is defined mounting on the additional heatsink with thermal grease.



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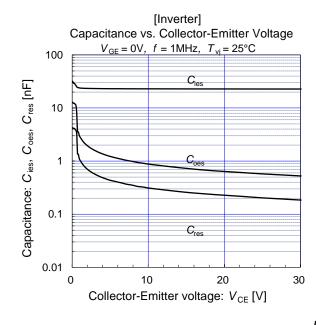
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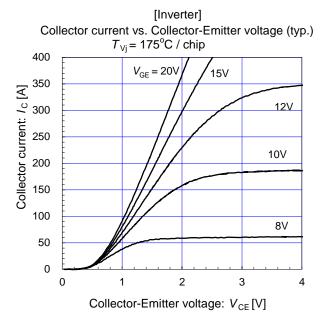
Collector-Emitter voltage: V<sub>CE</sub>[V]

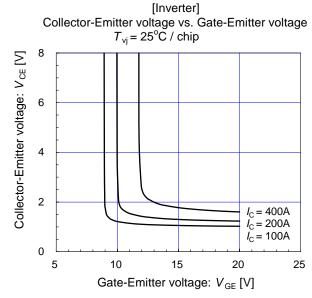
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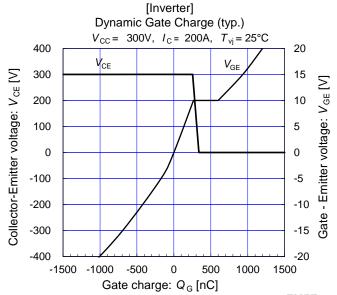
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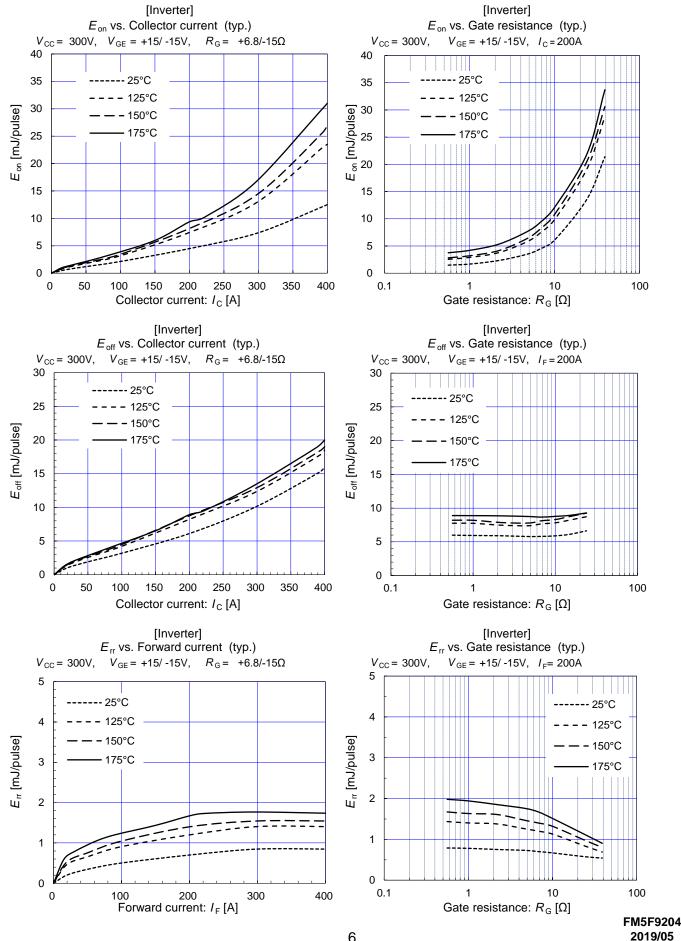
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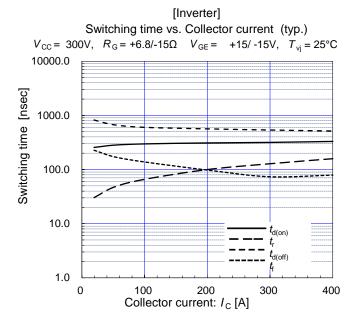


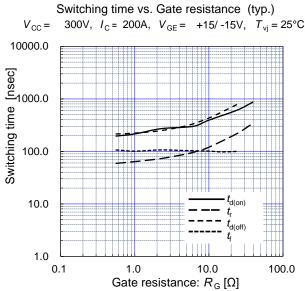


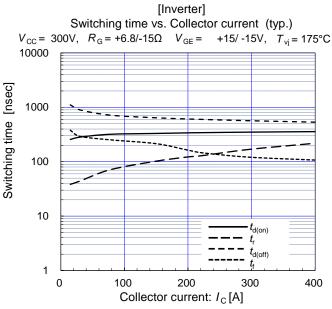
[Inverter]

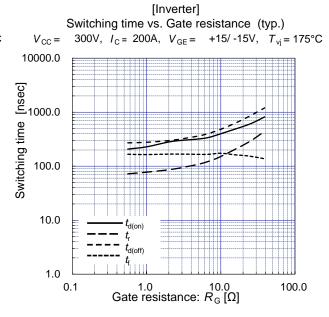


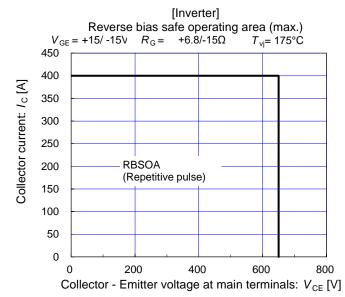
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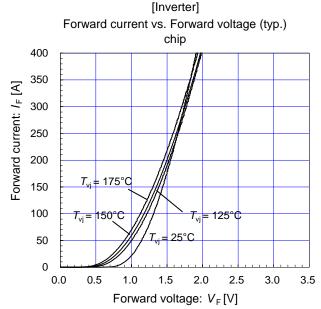


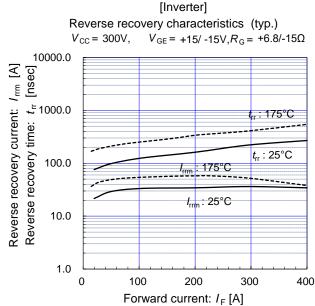




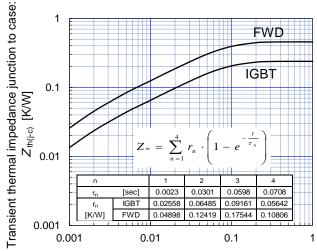








[Inverter]
Transient thermal resistance(max.)



Pulse width:  $t_w$  [sec]

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