

IGBT Modules

Power Module (X series) 1200V / 1200A / 2-in-1 package

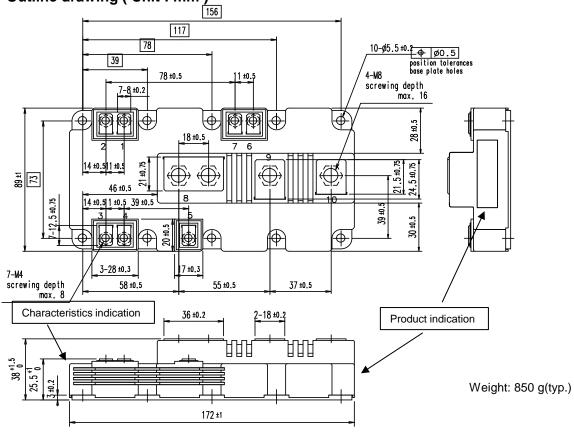
■ Features

Low $V_{\mathrm{CE(sat)}}$ Low Inductance Module structure

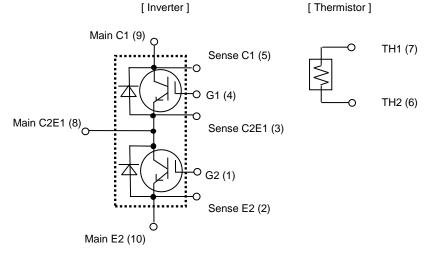
■ Applications

Inverter for Motor Drives, AC and DC Servo Drives
Uninterruptible Power Supply Systems, Wind Turbines, PV Power Conditioning Systems

■ Outline drawing (Unit : mm)



■ Equivalent Circuit





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■ Absolute Maximum Ratings (at T_C= 25°C unless otherwise specified)

Items		Symbols	Cond	itions	Maximum Ratings	Units	
	Collecto	r-emitter voltage, gate-emitter short-circuited	$V_{\sf CES}$			1200	V
	Gate-en	nitter voltage, collector-emitter short-circuited	V_{GES}			±20	V
	Collecto	r current	Ic	Continuous	T _C =100°C	1200	
١.	Repetitiv	ve peak collector current	I _{CRM}	1ms	•	2400	
re	Forward	current	I _F			1200	A
nve	Forward current Repetitive peak forward current		I _{FRM}	1ms		2400	
_	Total power dissipation		P _{tot}	1 device		7.1	kW
	Virtual junction temperature		T_{vj}			175	
	Operating virtual junction temperature		T _{vjop}			175]
	(under s	witching conditions)	vjop			173	°C
Ca	Case temperature		T _c			150	
Sto	Storage temperature		$T_{\rm stg}$			-40 ~ 150	
Isolation between terminal and copper base (*1)		$V_{\rm isol}$	AC: 1min.		4000	Vrms	
voltage between thermistor and others (*2)		v isol	AC. IIIIII.		4000	VIIIIS	
Mounting torque of screws to heatsink (*3)		Ms	M5		6.0		
Мо	Mounting torque of screws to main terminals (*3)		$M_{\rm t}$	M8		10.0	N⋅m
Mounting torque of screws to sense terminals (*3)			IVI t	M4		2.1	

^(*1) All terminals should be connected together during the test.

(*3) Recommendable Value: : Mounting torque of screws to heatsink $3.0 \sim 6.0 \text{ N} \cdot \text{m}$ (M5)

: Mounting torque of screws to main terminals 8.0~ 10.0 N·m (M8)

: Mounting torque of screws to sense terminals 1.8~ 2.1 N·m (M4

^(*2) Two thermistor terminals should be connected together, other terminals should be connected together and shorted to base plate during the test.

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■ Electrical characteristics (at T_{vj} = 25°C unless otherwise specified)

Items		Symbola Canditions			Characteristics			Units
		Symbols	Condition	Conditions			max.	Units
	Collector-emitter cut-off current, gate-emitter short-circuited	I _{CES}	$V_{GE} = 0V$ $V_{CE} = 1200V$		-	-	400	μА
	Gate leakage current, collector-emitter short-circuited	I _{GES}	V _{CE} =0V, V _{GE} =±20V		-	-	800	nA
	Gate-emitter threshold voltage	$V_{GE(th)}$	$V_{\text{CE}} = 20V$ $I_{\text{C}} = 1200\text{mA}$		6.0	6.5	7.0	V
		V _{CE(sat)} (terminal)		T _{vj} =25°C	-	1.50	1.95	
	Collector-emitter		$V_{\rm GE} = 15 \rm V$	T _{vj} =25°C	-	1.45	1.90	V
	saturation voltage	V _{CE(sat)}	I _C = 1200A	T _{vj} =125°C	-	1.70	-	7
		(chip)		T _{vi} =150°C	-	1.75	-	
				T _{vi} =175°C	-	1.85	-	
	Internal gate resistance	$r_{\rm g}$	-	- vj	-	3.13	_	Ω
	miorrial gato recictance	C _{ies}			-	128	_	
	Capacitance	C oes	V _{CE} =10V, V _{GE} =0V, f=1MHz		-	4.4	_	nF
	Capacitarios	C _{res}			-	1.1	-	
	Gate charge	Q _G	$V_{\rm CC} = 600 \text{V}, I_{\rm C} = V_{\rm GE} = -15 \rightarrow +15 \text{V}$	1200A	-	8.3	-	μC
ler.	Forward voltage	V _F (terminal)	$V_{GE} = 0V$ $I_F = 1200A$	T _{vj} =25°C	-	1.65	2.10	
nverter				$T_{\rm vj}$ =25°C	-	1.60	2.05	\ ,
<u>=</u>		V _F	T _{vj} =125°C	-	1.65	-	V	
		(chip)		T _{vi} =150°C	-	1.60	-	1
				T _{vi} =175°C	-	1.60	_	1
			$V_{\rm CC} = 600 \rm V$	$T_{\rm vi}$ =25°C	-	1.10	-	-
			I _C , I _F = 1200A	T _{vj} =125°C	-	1.10	-	
		$t_{d(on)}$	$V_{GE} = +15/-15 \text{ V}$	T _{vj} =150°C	-	1.10	_	
			$R_{\rm G} = +0.82/-0.82 \Omega$		-	1.10	_	
			$L_{\rm S} = 60 \rm nH$	T _{vi} =25°C	-	0.20	_	
	Switching time (*1)		_3	T _{vi} =125°C	-	0.22	-	
				T _{vi} =150°C	-	0.22	-	
				T _{vj} =175°C	-	0.22	_	
			_	$T_{\rm vj}$ =25°C	-	0.90	-	
				T _{vi} =125°C	-	0.94	_	μs
		$t_{d(off)}$		T _{vj} =150°C	-	0.96	_	1
				T _{vi} =175°C	-	0.97	-	
				$T_{\rm vi}$ =25°C	-	0.16	-]
		t _f		T _{vi} =125°C	-	0.17	-	
				T _{vj} =150°C	-	0.18	-	_
			_	T_{vj} =175°C	-	0.20	-	
				T _{vj} =25°C T _{vj} =125°C	-	0.29 0.47	-	
	Reverse recovery time	verse recovery time $t_{ m rr}$		$T_{vj} = 125 \text{ C}$ $T_{vj} = 150 \text{ °C}$	-	0.47	-	
				$T_{vi} = 175^{\circ}C$	-	0.52	-	1

 $[|] T_{vi} = 175^{\circ}C |$ (*1) Turn on time $(t_{on}) = t_{d(on)} + t_{r}$, Turn off time $(t_{off}) = t_{d(off)} + t_{f}$



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■ Electrical characteristics (at T_{vj}= 25°C unless otherwise specified)

Items		Symbols	Conditions			Characteristics		ics	Units
	items	Symbols		Conditions		min.	typ.	max.	Units
		E _{on}		600V	T _{vj} =25°C	-	110	-	
				= 1200A	T _{vj} =125°C	-	165	-	
				+15/-15 V		-	172	-	
			$R_{G} =$	+0.82/-0.82 Ω		-	185	-	
			L _s =	60 nH	T _{vj} =25°C	-	164	-	
Inverter	Switching loss (per pulse) E_{off}	ulse)			T _{vj} =125°C	-	209	-	
					T _{vj} =150°C	-	220	-	mJ
					T _{vj} =175°C	-	227	-	
					T _{vj} =25°C	-	48	-	
					T _{vj} =125°C	-	74	-	
					T _{vj} =150°C	-	88	-	
							T _{vj} =175°C	-	98
ţ	Resistance	R	<i>T</i> =	25°C		-	5000	-	Ω
nis	Nosidianio		$T = 100^{\circ}C$			465	495	520	32
Thermistor	B value	В	T =	25/ 50°C		3305	3375	3450	K

NOTICE:

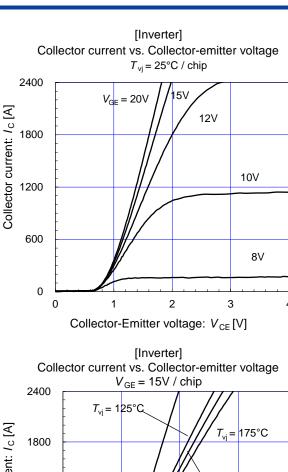
The external gate resistance ($R_{\rm G}$) shown above is one of our recommended value for the purpose of minimum switching loss. However the optimum $R_{\rm G}$ depends on circuit configuration and/or environment. We recommend that the $R_{\rm G}$ has to be carefully chosen based on consideration if IGBT module matches design criteria, for example, switching loss, EMC/EMI, spike voltage, surge current and no unexpected oscillation and so on.

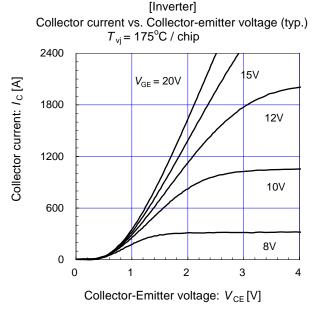
■Thermal resistance characteristics

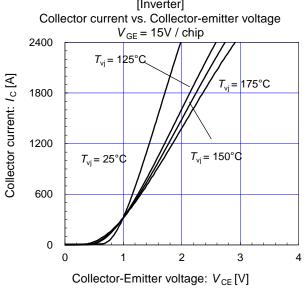
Items	Symbols	Conditions	Characteristics			Units
items	Syllibols	Conditions	min.	typ.	max.	Ullits
Thermal resistance junction to	P	Inverter IGBT	-	-	21.0	
case(1 device)	$R_{th(j-c)}$	Inverter FWD	-	-	34.5	K/kW
Thermal resistance case to	R _{th(c-s)}	with 1 W/(m·K) thermal grease	_	6.3		IVIXV
heatsink(1 IGBT+1 FWD) (*1)		with 1 w/(m·k) thermal grease	-	0.3	-	

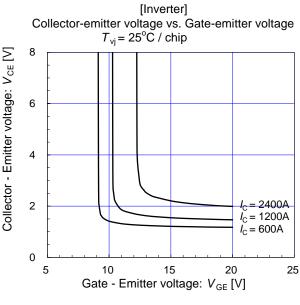
^(*1) This is the value which is defined mounting on the additional heatsink with thermal grease.

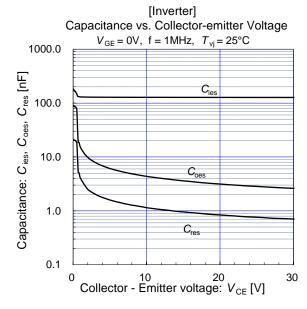


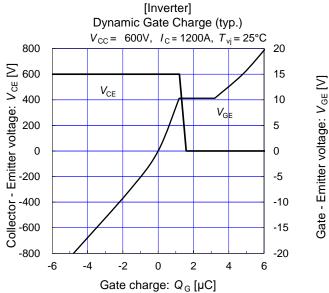




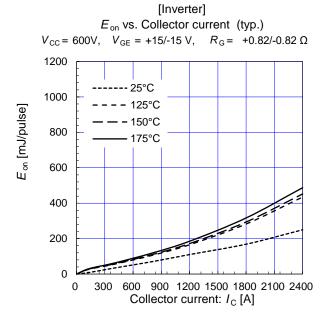


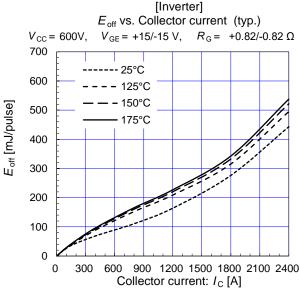


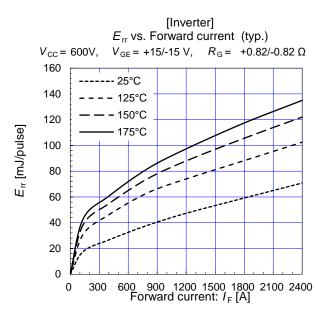


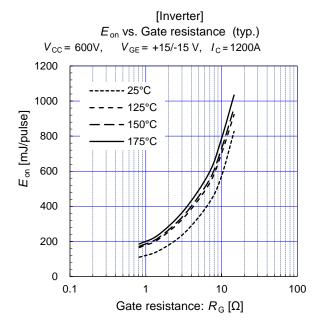


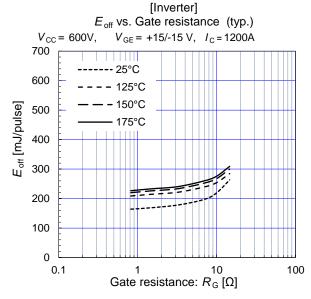


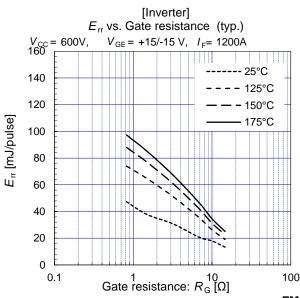




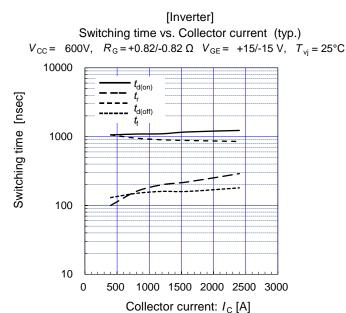




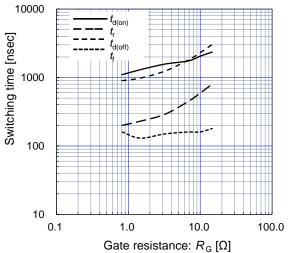






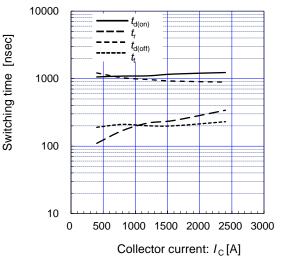


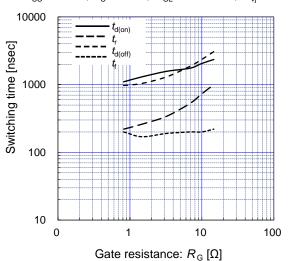
[Inverter] Switching time vs. Gate resistance (typ.) $V_{CC} = 600V$, $I_{C} = 1200A$, $V_{GE} = +15/-15 V$, $T_{vj} = 25$ °C 10000

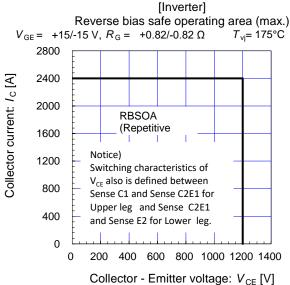


[Inverter] Switching time vs. Collector current (typ.) $V_{CC} = 600 \text{V}, R_G = +0.82/-0.82 \Omega V_{GE} = +15/-15 \text{V}, T_{vi} = 175^{\circ}\text{C}$

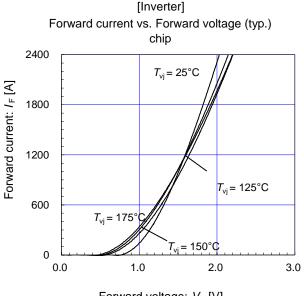
[Inverter] Switching time vs. Gate resistance (typ.) 600V, $I_C = 1200A$, $V_{GE} = +15/-15 \text{ V}$, $T_{vi} = 175^{\circ}\text{C}$ $V_{\rm CC} =$



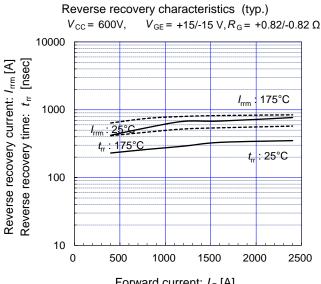




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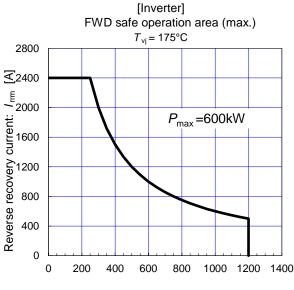
Forward voltage: $V_F[V]$



[Inverter]

Forward current: I_F [A]

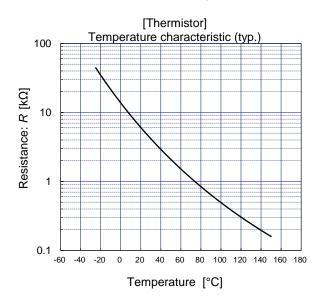
[Inverter]



Collector-Emitter voltage: V_{CE} [V]

Transient Thermal impedance (max.) Transient thermal impedance junction to case 100.0 FWD **IGBT** 0.01 Z_{th(j-c)}[K/kW] 1.0 0.001 0.010 0.100 1.000

Pulse width: tw [sec]



IGBT Modules

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