

IGBT Modules

Power Module (V series) 1700V / 300A / 2-in-1 package

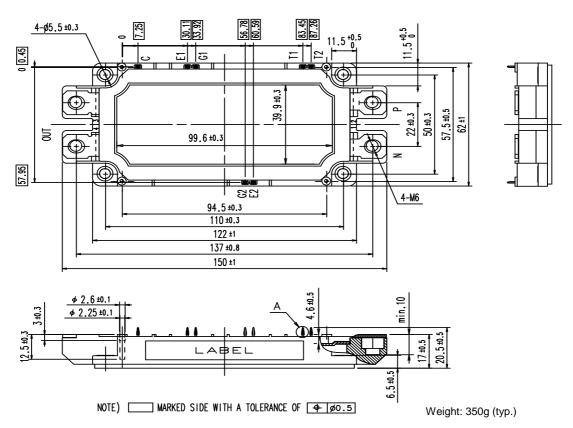
■ Features

Low V_{CE(sat)} Low Inductance Module structure Solderless press-fit terminals

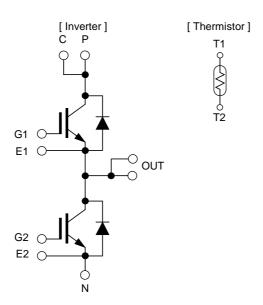
■ Applications

Inverter for Motor Drives, AC and DC Servo Drives
Uninterruptible Power Supply Systems, Wind Turbines, PV Power Conditioning Systems

■ Outline drawing (Unit:mm)



■ Equivalent Circuit



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■ Absolute Maximum Ratings (at T_C= 25°C unless otherwise specified)

Items		Symbols	Conditions		Maximum Ratings	Units
Collector-Emitter voltage		V _{CES}			1700	V
Gate-Emitter voltage		V_{GES}			±20	V
		1-	Continuous	T _C =25°C	450	
Collector current		I _C	Continuous	T _C =100°C	300	
		I _C pulse	1ms		600	A
		-I _C			300	
		-I _C pulse	1ms		600	
Collector power dissipation		P _C	1 device		1665	W
Junction temperature		T _j			175	
Operating junction temperature		T _{jop}			150	°C
(under switching conditions)						
Case temperature		T _c			125	
Storage temperature		T _{stg}			-40 ~ 125	
Isolation	between terminal and copper base (*1)	V_{iso}	AC: 1min.		3400	VAC
voltage	between thermistor and others (*2)	v iso	AC. IIIIII.		3400	VAC
Screw	Mounting (*3)	-			3.5	N m
Torque	Terminals (*4)	-			4.5	

^(*1) All terminals should be connected together during the test.

(*3) Recommendable Value: 2.5-3.5 Nm (M5) (*4) Recommendable Value: 3.5-4.5 Nm (M6)

^(*2) Two thermistor terminals should be connected together, other terminals should be connected together and shorted to base plate during the test.

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■ Electrical characteristics (at T_j= 25°C unless otherwise specified)

Itomo	Cumbala	Conditions		Characteristics			Units	
Items	Symbols			min.	typ.	max.	Units	
Zero gate voltage Collector current	I _{CES}	V _{GE} =0V, V _{CE} =1700V		-	-	3.0	mA	
Gate-Emitter leakage current	I _{GES}	V _{CE} =0V, V _{GE} =±20V		-	-	600	nA	
Gate-Emitter threshold voltage	$V_{\text{GE(th)}}$	V _{CE} =20V, I _C =300mA		6.0	6.5	7.0	V	
	V _{CE(sat)} (terminal)	V _{GE} = 15V	T _j =25°C	-	2.45	2.90	- V	
			T _j =125°C	-	2.90	-		
Collector-Emitter			T _j =150°C	-	2.95	-		
saturation voltage	V _{CE(sat)} (chip)	I _C = 300A	T _j =25°C	-	2.00	2.45		
			T _j =125°C	-	2.45	-		
			T _j =150°C	-	2.50	-		
Internal gate resistance	R _{G(int)}	-	'	-	2.5	-	Ω	
Input capacitance	C_ies	V _{CE} =10V, V _{GE} =0V, f=1MHz		-	30	-	nF	
	t _{on}	V _{CC} = 900V V _{GE} = ±15V		-	900	-		
Turn-on time	t _r		V_{CC} = 900 V	I _C = 300A	-	400	-]
	t _{r(i)}		$R_G = 4.7\Omega$	-	100	-	nsec	
Turn-off time	$t_{\rm off}$	L _s = 80nH		-	1300	-		
Turr-on time	t _f	1		-	100	-	<u> </u>	
	V _F (terminal)		T _j =25°C	-	2.25	2.70	V	
			T _j =125°C	-	2.55	-		
Commend on welters		V _{GE} = 0V I _F = 300A	T _j =150°C	-	2.55	-		
Forward on voltage	V _F (chip)		T _j =25°C	-	1.80	2.25		
			T _j =125°C	-	2.10	-		
			T _i =150°C	-	2.10	-		
Reverse recovery time	recovery time t _{rr} I _F = 300A		,	-	250	-	nsec	
Thermistor Resistance	R	T=25°C T=100°C		-	5000	-	Ω	
THEITHSIOI RESISIANCE				465	495	520		
Thermistor B value	В	T=25/50°C		3305	3375	3450	K	

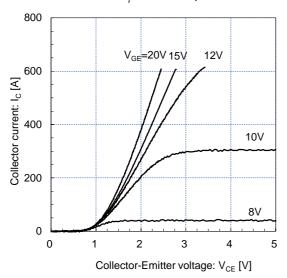
5. Thermal resistance characteristics

Items	Symbols	Conditions	Characteristics			Units
items		Conditions	min.	typ.	max.	Units
Thermal resistance	D	IGBT	-	-	0.09	
(1device)	device) $R_{th(j-c)}$		-	-	0.15	°C/W
Contact thermal resistance (1device) (*1)	R _{th(c-f)}	with thermal compound	-	0.0167	-	- C/VV

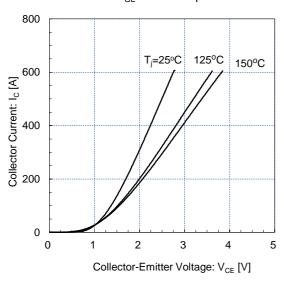
^(*1) This is the value which is defined mounting on the additional cooling fin with thermal compound.

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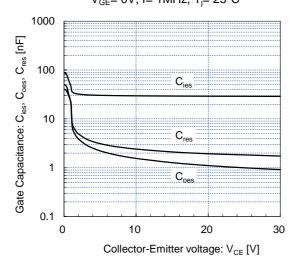
Collector current vs. Collector-Emitter voltage $T_i = 25^{\circ}C$ / chip



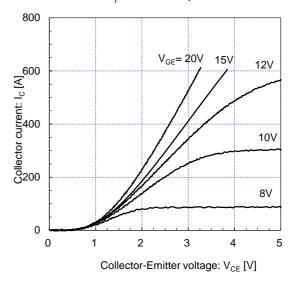
Collector current vs. Collector-Emitter voltage $V_{GE} = 15V / chip$



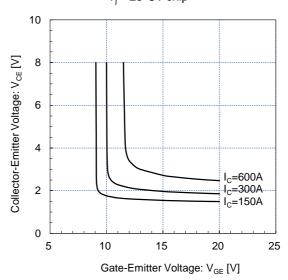
Capacitance vs. Collector-Emitter Voltage V_{GE} = 0V, f= 1MHz, T_i = 25°C



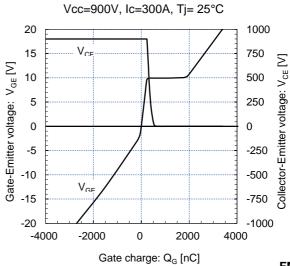
Collector current vs. Collector-Emitter voltage (typ.) $T_i = 150^{\circ}C$ / chip



Collector-Emitter voltage vs. Gate-Emitter voltage $T_i = 25^{\circ}C$ / chip

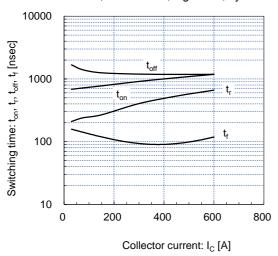


Dynamic Gate Charge (typ.)

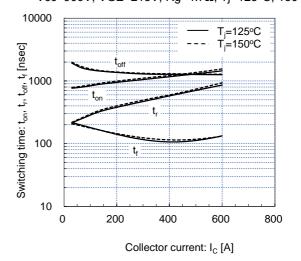


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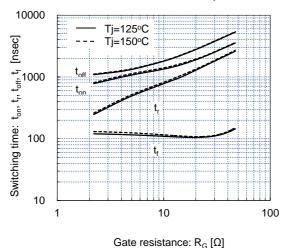
Switching time vs. Collector current (typ.) Vcc=900V, $VGE=\pm15V$, $Rg=4.7\Omega$, $Tj=25^{\circ}C$



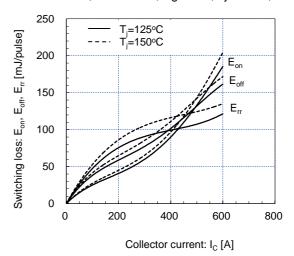
Switching time vs. Collector current (typ.) Vcc=900V, VGE=±15V, Rg=4.7Ω, Tj=125°C, 150°C



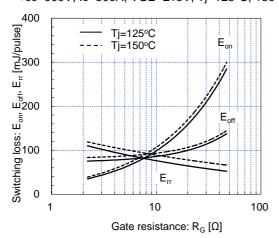
Switching time vs. Gate resistance (typ.) /cc=900V, Ic=300A, VGE=±15V, Tj=125°C, 150°C



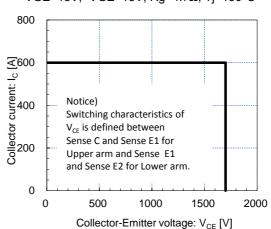
Switching loss vs. Collector current (typ.) Vcc=900V, VGE=±15V, Rg=4.7Ω, Tj=125°C, 150°C



Switching loss vs. Gate resistance (typ.) /cc=900V, Ic=300A, VGE=±15V, Tj=125°C, 150°C

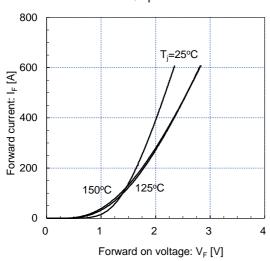


Reverse bias safe operating area (max.) +VGE=15V, -VGE=15V, Rg=4.7 Ω , Tj=150 $^{\circ}$ C

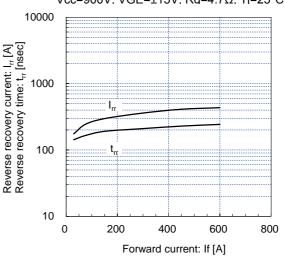


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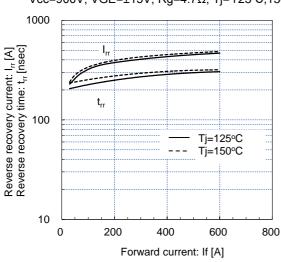
Forward current vs. Forward vltage (typ.) chip



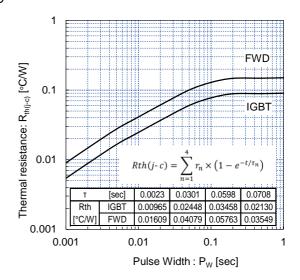
Reverse recovery characteristics (typ.) Vcc=900V, VGE=±15V, Rq=4.7Ω, Ti=25°C



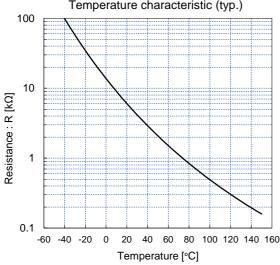
Reverse Recovery Characteristics (typ.) Vcc=900V, VGE=±15V, Rg=4.7Ω, Tj=125°C,150°C



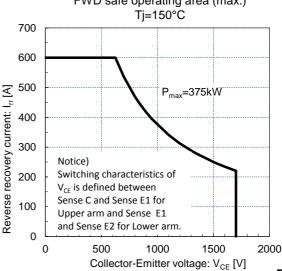
Transient Thermal Resistance (max.)



[THERMISTOR] Temperature characteristic (typ.)



FWD safe operating area (max.)



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