

# 2MBI450VN-170-50

**IGBT Modules** 

### **IGBT MODULE (V series)** 1700V / 450A / 2 in one package

#### Features

High speed switching Voltage drive Low Inductance module structure

#### Applications

Inverter for Motor Drive AC and DC Servo Drive Amplifier Uninterruptible Power Supply Industrial machines, such as Welding machines



#### ■ Maximum Ratings and Characteristics

● Absolute Maximum Ratings (at T<sub>c</sub>=25°C unless otherwise specified)

Item	Items		Symbols	Conditions		Maximum ratings	Units	
	Collector-Emitter voltage		Vces			1700	V	
	Gate-Emitter voltage		V <sub>GES</sub>			±20	V	
<u>~</u> [	Collector current  Collector power dissipation		Ic	Continuous	Tc=25°C	600		
Inverter					Tc=100°C	450		
≥			C pulse	1ms		900	Α	
드			-lc			450		
			-I <sub>C pulse</sub>	1ms		900		
			Pc	1 device		2500	W	
June	ction temperat	ture	Tj			175		
Operating junction temperature (under switching conditions)			Tjop			150	°C	
Storage temperature		T <sub>stg</sub>	-40 ~ 125					
leal	lation voltage between terminal and control between thermistor and	between terminal and copper base (*1)	),,	AC : 1min.		3400	VAC	
15016		between thermistor and others (*2)	Viso			3400		
Scre		Mounting (*3)	-			3.5	N m	
3016		Terminals (*4)	-			4.5	IN III	

Note \*1: All terminals should be connected together during the test.

Note \*2: Two thermistor terminals should be connected together, other terminals should be connected together and shorted to base plate during the test.

Note \*3: Recommendable Value: 2.5-3.5 Nm (M5)

Note \*4: Recommendable Value: 3.5-4.5 Nm (M6)

● Electrical characteristics (at T<sub>i</sub>= 25°C unless otherwise specified)

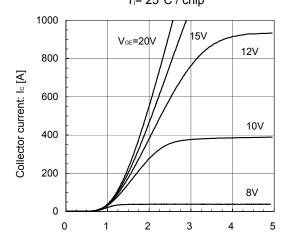
mo	Cymbolo	Conditions		Characteristics			11!4
ms	Symbols			min.	typ.	max.	Units
Zero gate voltage collector current	Ices	V <sub>GE</sub> = 0V, V <sub>CE</sub> = 1700V		-	-	3.0	mA
Gate-Emitter leakage current	Iges	$V_{CE} = 0V, V_{GE} = \pm 20V$		-	-	600	nA
Gate-Emitter threshold voltage	V <sub>GE (th)</sub>	V <sub>CE</sub> = 20V, I <sub>C</sub> = 450mA		6.0	6.5	7.0	V
	V		T <sub>j</sub> =25°C	-	2.65	3.10	V
	V <sub>CE (sat)</sub>		T <sub>j</sub> =125°C	-	3.10	-	
Callactor Emitter acturation valtage	(terminal)	V <sub>GE</sub> = 15V	T <sub>j</sub> =150°C	-	3.15	-	
Collector-Emitter saturation voltage	V	Ic = 450A	T <sub>j</sub> =25°C	-	2.00	2.45	
	V <sub>CE</sub> (sat)		T <sub>j</sub> =125°C	-	2.45	-	
	(chip)		T <sub>j</sub> =150°C	-	2.50	-	
Internal gate resistance	R <sub>G</sub> (int)	-		-	1.67	-	Ω
Input capacitance  Turn-on time	Cies	V <sub>CE</sub> = 10V, V <sub>GE</sub> = 0V, f = 1MHz		-	40	-	nF
Turn-on time	ton	$/_{cc} = 900V$ - 900				-	
	tr	Ic = 450A	-	400	-	nsec	
	t <sub>r (i)</sub>	$V_{GE} = \pm 15V$		-	100		-
Turn-off time	toff	$R_G = 3.3\Omega$ - 1300			-	1	
	t <sub>f</sub>	L <sub>s</sub> = 80nH		-	100	-	1
Forward on voltage	.,	V <sub>GE</sub> = 0V I <sub>F</sub> = 450A	T <sub>i</sub> =25°C	-	2.45	2.90	V
	V <sub>F</sub>		T <sub>i</sub> =125°C	-	2.75	-	
	(terminal)		T <sub>i</sub> =150°C	-	2.70	-	
			T <sub>i</sub> =25°C	-	1.80	2.25	
	V <sub>F</sub>		T <sub>j</sub> =125°C	-	2.10	-	
	(chip)		T <sub>j</sub> =150°C	-	2.05	-	1
Reverse recovery time	trr	I <sub>F</sub> = 450A		-	250	-	nsec
		T = 25°C		-	5000	-	Ω
Resistance B value	R	T = 100°C		465	495	520	
B value	В	T = 25/50°C	,	3305	3375	3450	K

#### Thermal resistance characteristics

Items	Symbols	Conditions	Characteristics			Units
items			min.	typ.	max.	Units
The word was into man (4 daysins)	R <sub>th(j-c)</sub>	Inverter IGBT	-	-	0.06	°C/W
Thermal resistance(1device)		Inverter FWD	-	-	0.10	
Contact thermal resistance (1device) (*5)	R <sub>th(c-f)</sub>	with Thermal Compound	-	0.0167	-	

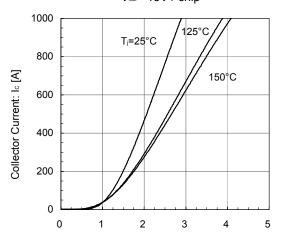
#### ■ Characteristics (Representative)

[INVERTER]
Collector current vs. Collector-Emitter voltage (typ.)
T<sub>i</sub>= 25°C / chip



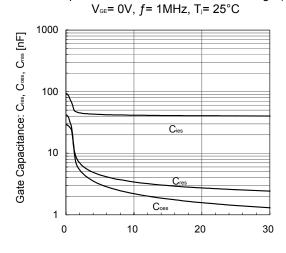
Collector-Emitter voltage: Vce [V]

[INVERTER]
Collector current vs. Collector-Emitter voltage (typ.)  $V_{GE}$ = 15V / chip



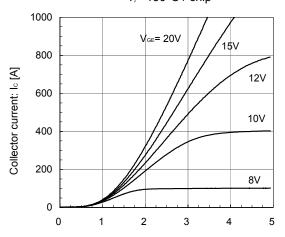
Collector-Emitter Voltage:  $V_{\text{CE}}[V]$ 

[INVERTER]
Gate Capacitance vs. Collector-Emitter Voltage (typ.)



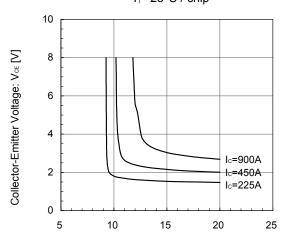
Collector-Emitter voltage:  $V_{\text{CE}}$  [V]

[INVERTER]
Collector current vs. Collector-Emitter voltage (typ.)
T<sub>i</sub>= 150°C / chip



Collector-Emitter voltage: Vce [V]

[INVERTER]
Collector-Emitter voltage vs. Gate-Emitter voltage (typ.)  $T_i$ = 25°C / chip

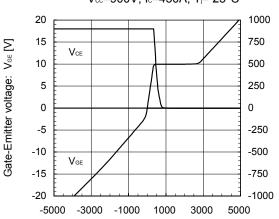


Gate-Emitter Voltage:  $V_{\text{GE}}$  [V]

[INVERTER]

Dynamic Gate Charge (typ.)

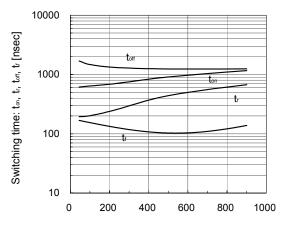
Vcc=900V, Ic=450A, T<sub>i</sub>= 25°C



Gate charge: Q<sub>9</sub> [µC]

#### [INVERTER]

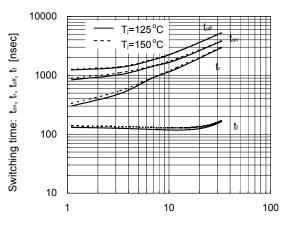
Switching time vs. Collector current (typ.)  $V_{\text{CC}}$ =900V,  $V_{\text{GE}}$ =±15V,  $R_{\text{G}}$ =3.3 $\Omega$ ,  $T_{\text{J}}$ =25°C



Collector current: Ic [A]

#### [INVERTER]

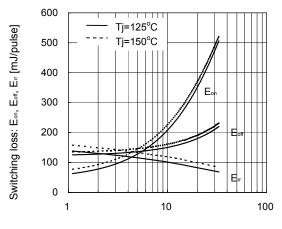
Switching time vs. Gate resistance (typ.)  $V_{cc}$ =900V,  $I_c$ =450A,  $V_{ce}$ =±15V,  $T_j$ =125°C, 150°C



Gate resistance: R<sub>G</sub> [Ω]

#### [INVERTER]

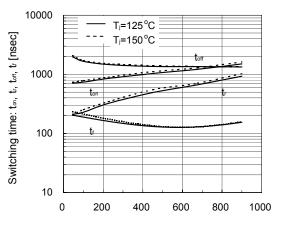
Switching loss vs. Gate resistance (typ.) V<sub>cc</sub>=900V, I<sub>c</sub>=450A, V<sub>GE</sub>= $\pm 15$ V, T<sub>J</sub>=125, 150°C



Gate resistance: R<sub>G</sub> [Ω]

[INVERTER]

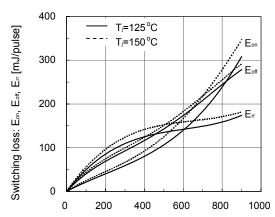
Switching time vs. Collector current (typ.)  $V_{\text{CC}}$ =900V,  $V_{\text{GE}}$ =±15V,  $R_{\text{G}}$ =3.3 $\Omega$ ,  $T_{\text{J}}$ =125°C, 150°C



Collector current: Ic [A]

[INVERTER]

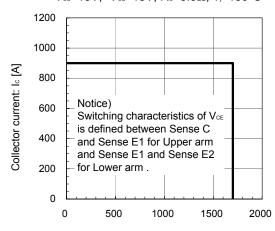
Switching loss vs. Collector current (typ.)  $V_{\text{CC}}$ =900V,  $V_{\text{CE}}$ =±15V,  $R_{\text{G}}$ =3.3 $\Omega$ ,  $T_{\text{J}}$ =125°C, 150°C



Collector current: Ic [A]

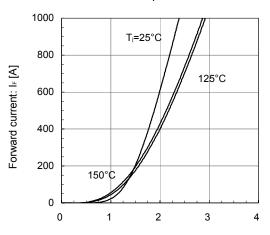
[INVERTER]

Reverse bias safe operating area (max.)  $+V_{GE}=15V$ ,  $-V_{GE}=15V$ ,  $R_{G}=3.3\Omega$ ,  $T_{j}=150^{\circ}C$ 



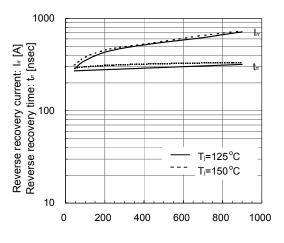
Collector-Emitter voltage: VCE [V]

[INVERTER]
Forward Current vs. Forward Voltage (typ.)
chip



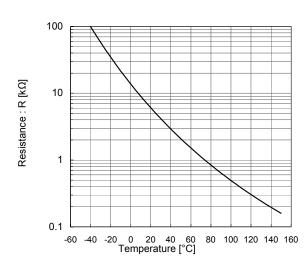
[INVERTER]
Reverse Recovery Characteristics (typ.)
V<sub>CC</sub>=900V, V<sub>CE</sub>=±15V, R<sub>C</sub>=3.3Ω, T<sub>I</sub>=125°C, 150°C

Forward on voltage: V<sub>F</sub> [V]

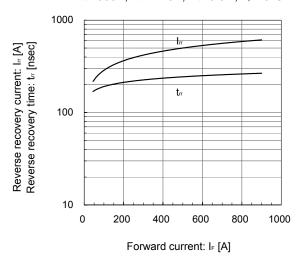


Forward current: I<sub>F</sub> [A]

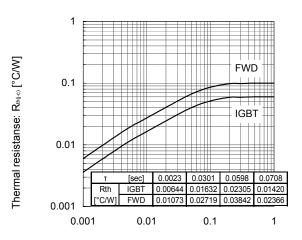
[THERMISTOR]
Temperature characteristic (typ.)



[INVERTER] Reverse Recovery Characteristics (typ.)  $V_{cc}$ =900V,  $V_{ce}$ =±15V,  $R_c$ =3.3 $\Omega$ ,  $T_j$ =25°C

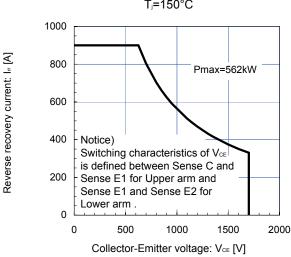


Transient Thermal Resistance (max.)

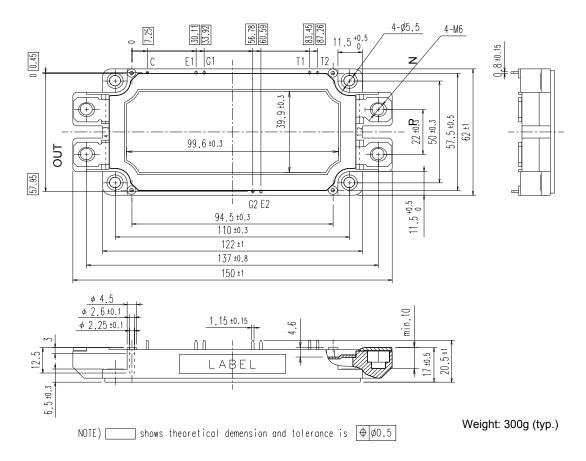


Pulse Width: Pw [sec]

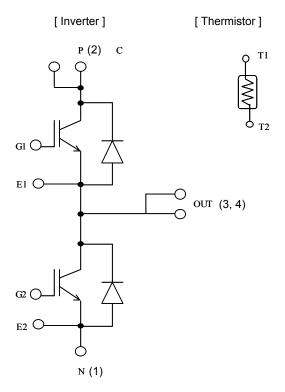
FWD safe operating area (max.) T=150°C



#### ■ Outline Drawings, mm



#### **■** Equivalent Circuit Schematic



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